



**HOSONIC ELECTRONIC CO., LTD.**



**SMD CRYSTAL OSCILLATOR SPECIFICATIONS**

<b>Customer</b>	
<b>Customer P/N</b>	
<b>Product</b>	3225 OSC
<b>Nominal Frequency</b>	12.288000MHz
<b>HOSONIC P/N</b>	D3SX12E0X000BE
<b>Version</b>	10C0
<b>Issue Date</b>	2018/5/3

<b>HOSONIC</b>		
<b>Drawn</b>	<b>Checked</b>	<b>Approved</b>
<b>Drawn</b>		
<b>LUCY</b>	<b>Richard</b>	<b>JOHN</b>

**Approved By Customer :** \_\_\_\_\_



**HOSONIC ELECTRONIC CO., LTD.**



Revised Record

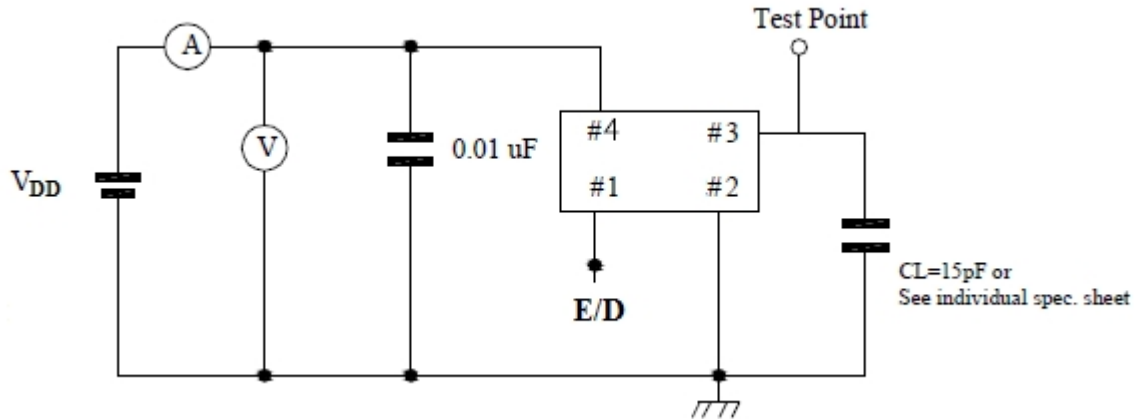
Rev.	Rev. Date	Item	Content	Remark
1.0	2018-05-03		Initial released	

**I ELECTRICAL PARAMETERS**

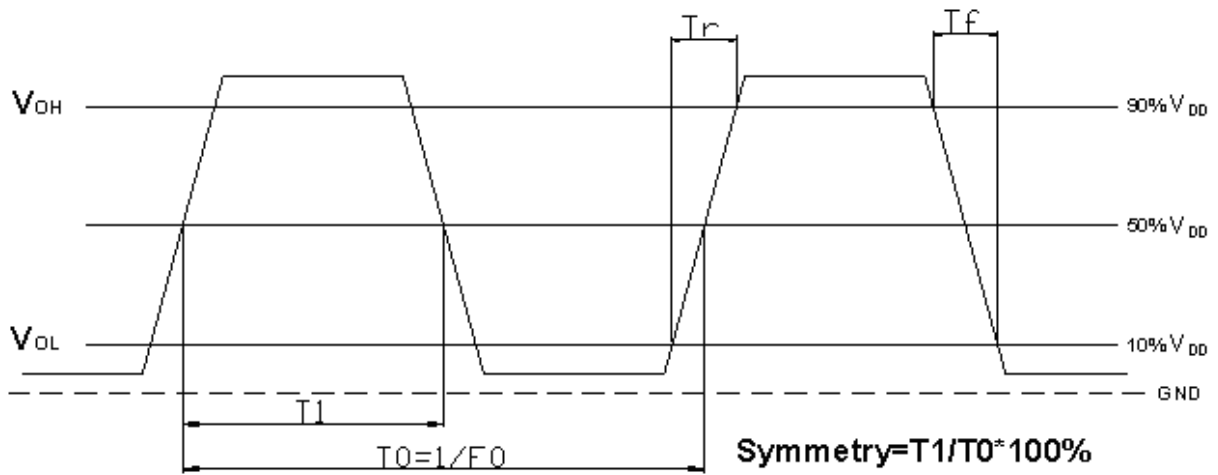
No.	Item	Symb.	Electrical Specification				Remark	
			Min.	Typ.	Max.	Units		
1	Nominal Frequency	F0	12.288000			MHz		
2	Frequency Stability		-25		25	ppm	All condition*	
3	Operating Temperature Range	TOPR	-20		70	°C		
4	Storage Temperature	TSTG	-55		125	°C		
5	Power supply Voltage	V <sub>DD</sub>	1.8 ± 10%			V		
6	Aging Per Year	Fa	-3.0		3.0	ppm	First Year	
7	Supply current	I <sub>DD</sub>			7	mA		
8	Output symmetry	Sym	45		55	%		
9	Rise time	Tr			7.0	ns	10%~90%V <sub>DD</sub>	
10	Fall time	Tf			7.0	ns	90%~10%V <sub>DD</sub>	
11	Output voltage	V <sub>OH</sub>	90%			V <sub>DD</sub>		
		V <sub>OL</sub>			10%	V <sub>DD</sub>		
12	Output load Hcmos Load				15	pF		
13	Start-up time				10	mS		
14	Pin 1, E/D function	pin 1=H or open.....output active at pin 3 pin 1=L.....high impedance at pin 3						
15	Package type	HXO-SE						

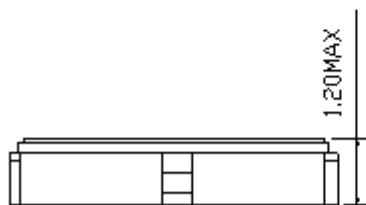
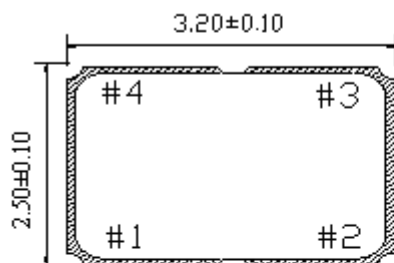
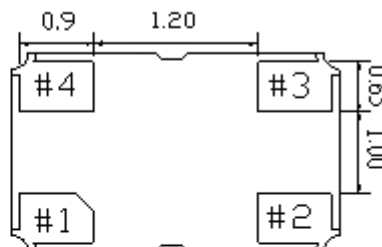
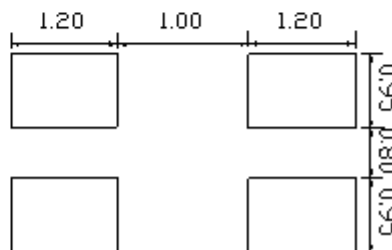
**NOTE: Storage Temperature is only for the product itself,the temperature for the packing material is -4~40°C.**

**All condition\*: Include 25°C tolerance, operating temperature range , input voltage change, aging, load change.**

**I HCMOS Test Circuit**


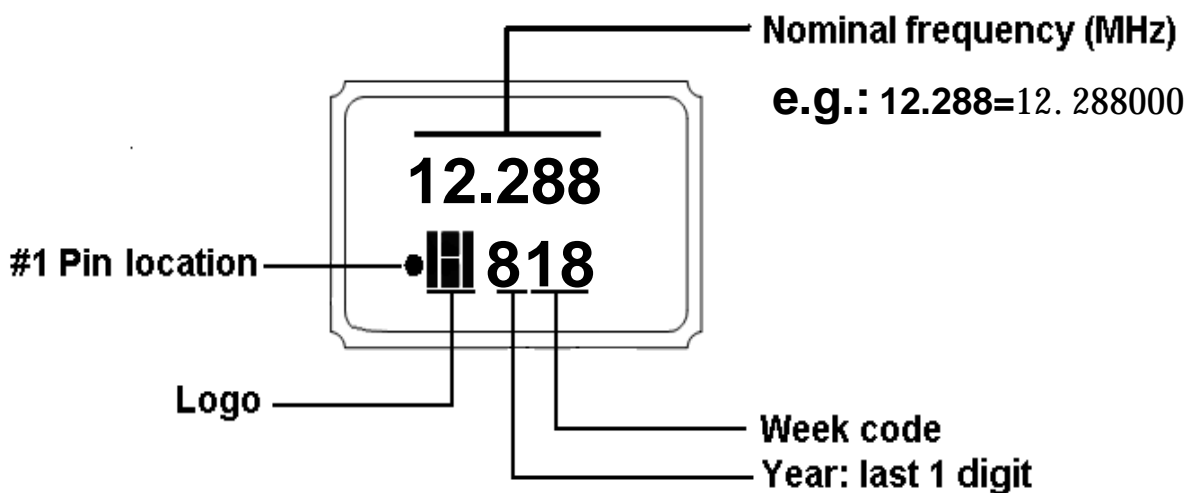
Enable/Disable Function	
Input (pin 1)	Output (pin 3)
Open	Enable
$V_{IH} \geq 0.7V_{DD}$	Enable
$V_{IL} \leq 0.3V_{DD}$	Disable

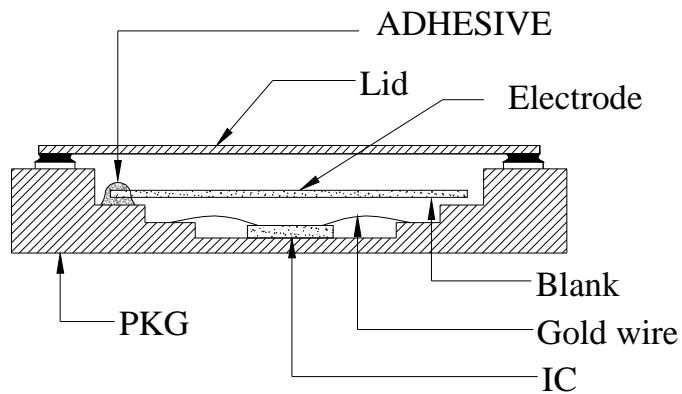
**I HCMOS OUTPUT WAVEFORM**


**I OUTLINE DIMENSIONS (unit: mm)**

**Top View**

**Recommended Solder Pattern**


Pin	Connection
1	E/D
2	GND
3	Output
4	V <sub>DD</sub>

- Note:
- 0.01uF bypass capacitor should be placed between V<sub>DD</sub> (pin 4) and GND (pin 2) to minimize power supply line noise;
  - Line shouldn't be layed under the oscillator in the PCB to minimize signal interference.
  - The Load we advise is only 15pF (that means drive only 1 CMOS/TTL gate).

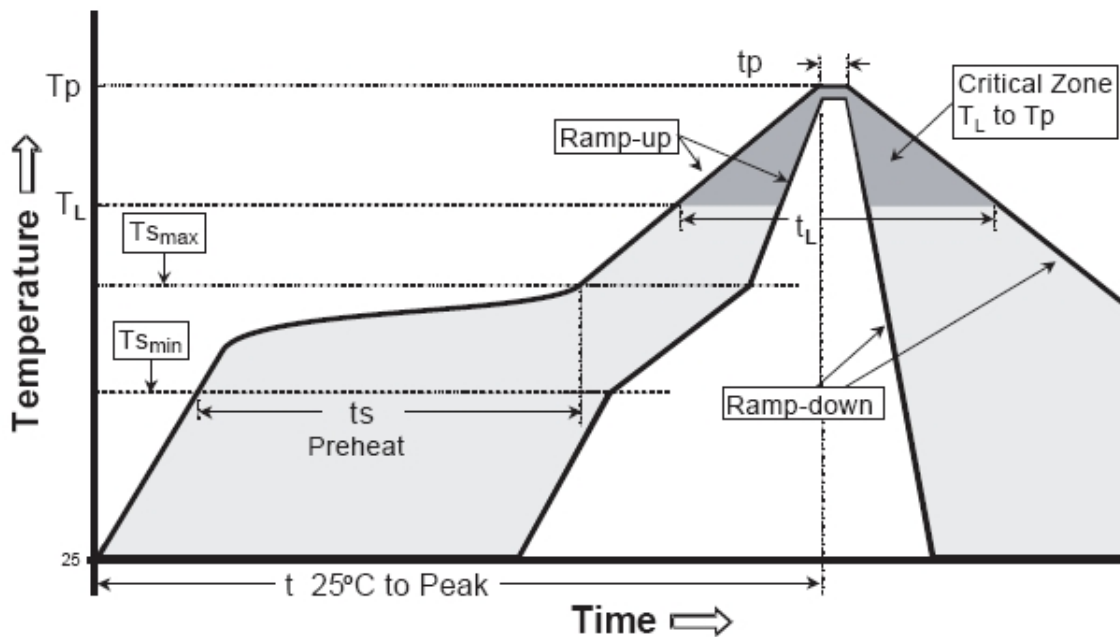
**I MARKING**


**I PRODUCT LAYOUT**


NO.	PART	MATERIAL	REMARK
1	LID	KOVAR(Fe+Co+Ni alloy)	
2	PKG	Al <sub>2</sub> O <sub>3</sub>	Base
3	BLANK	SiO <sub>2</sub>	Quartz
4	ADHESIVE	Ag/Silicon	Support
5	Electrode	Noble metal	
6	IC	Si	
7	Gold wire	Au	Connect

**I REFLOW PROFILES**

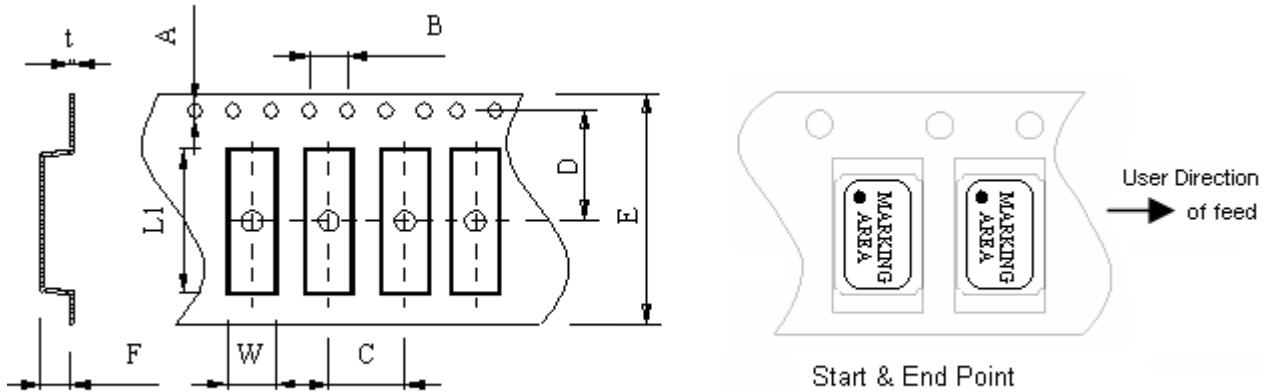
Profiles Feature	Pb-Free Assembly
Average Ramp-up Rate (Ts max to Tp)	3°C/second max.
Preheat	
■ Temperature Min (Ts min)	125°C
■ Temperature Max (Ts max)	200°C
■ Time (ts min to ts max)	60~180 seconds
Time maintained above	
■ Temperature (T <sub>L</sub> )	217°C
■ Time (t <sub>L</sub> )	60~150 seconds
Peak/Classification Temperature (Tp)	260°C
Time within 5°C of actual Peak Temperature (t <sub>p</sub> )	20~40 seconds
Ramp-down rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.
<b>Suggest reflow times</b>	<b>3 Times max</b>



**Remark: To reference JEDEC J-STD-020C**

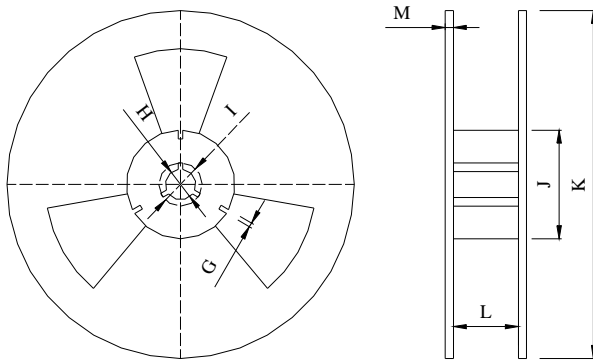
**I PACKAGE(reference to EIA-481)**

*Tape Dimensions(unit : mm)*



A	B	C	D	E	F	L1	W	t
1.50±0.2	4.0±0.2	4.0±0.1	3.5±0.2	8.0±0.2	1.3	3.4±0.1	2.7±0.1	0.3

*Reel Dimensions(unit: mm)*

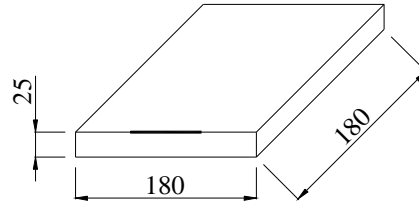
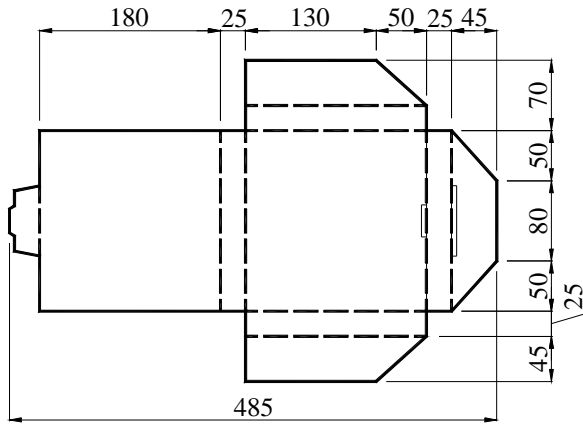


G	H	I	J	K	L	M
2.5	13.5	21.6	60.0	178	9.5	1.6

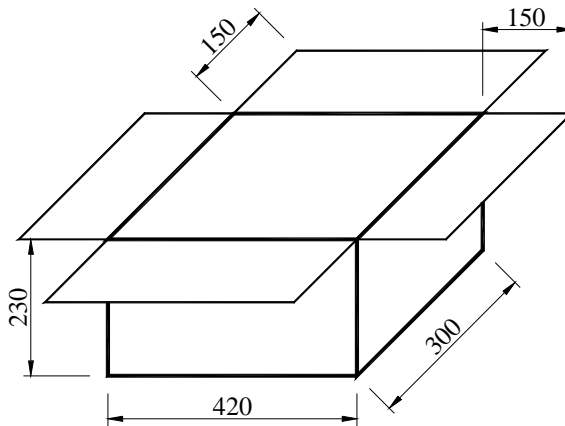
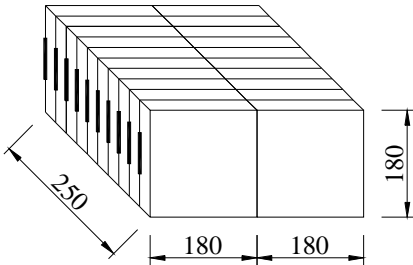
\*3000pcs/Reel



Carton Dimension (unit : mm)



1 reel = 1 Inner box



20 Inner boxes = 1 Carton

60kpcs = 1 Carton

**I RELIABILITY SPECIFICATIONS**

No.	Test Item	Test Conditions	Reference
1	High Temperature Storage	Temperature: 125°C ±5°C Time: 1000±12 Hours	MIL-STD-883E-1016
2	Temperature Cycle	Temperature 1: -55°C ±5°C Temperature 2: 125°C ±5°C Temperature change between T1 and T2 at soonest Run 1000 cycles, maintain T1 and T2 5minutes each in one cycle	JESD22 Method JA-104
3	Solder Heat Resistance	Pre-heat: 125°C 60~120 Seconds Solder Temperature: 260°C ±5°C Time: 30 Seconds	MIL-STD-202F 210 E
4	Drop Test	3 Times Free Fall from 75cm height table to 3cm thickness hard wood board	MIL-STD-202F-203B
5	High Temperature, High Humidity Storage	Temperature: 85°C ±5°C Relative Humidity: 80%~85% Time: 250Hours±24 Hours	MIL-STD-202F-103B
6	Steam Aging	Temperature: 97°C ±5°C Time: 24 Hours 260°C solder pot to check solderability	MIL-STD-883 C-1008.2B
7	Solderability	Dip in flux 5~10 seconds Temperature: 245°C ±5°C Time: 10 Seconds	MIL-STD-202F-208H
8	Aging	Temperature: 85°C ±5°C Time: 250±12Hours	MIL-STD-202 F-108A
9	Thermal Shock	Temperature 1: -55°C ±5°C Temperature 2: 125°C ±5°C Temperature change between T1 and T2: 5 seconds 100 cycles, maintain T1 and T2 for 30 minutes each in one cycle	MIL-STD-883E-1011.9B
10	Vibration	Frequency Range: 10Hz~2000Hz Amplitude: 1.5mm or 20G 4Hours in each direction, total 12Hours	MIL-STD-202F-204D

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