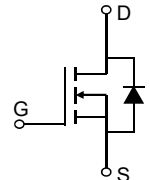
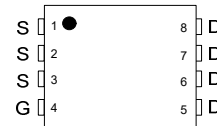
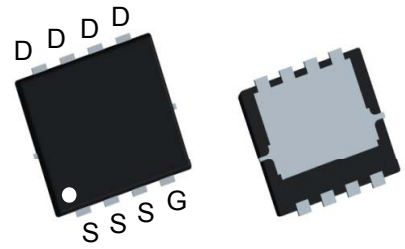


**LOW VOLTAGE MOSFET (N-CHANNEL)**
**FEATURES**

- $V_{DS}=30V, R_{DS(ON)} \leq 5.2m\Omega @ V_{GS}=10V, I_D=20A$
- Ultra Low on-resistance
- For Low power DC to DC converter application
- For Load switch application
- Surface Mount device



PDFN3333

**MECHANICAL DATA**

- Case: PDFN3333
- Case Material: Molded Plastic. UL flammability
- Classification Rating: 94V-0
- Weight: 0.012 grams (approximate)
- Marking: Q80N03

**MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C=25^\circ\text{C}$	Continuous Drain Current <sup>1</sup>	80	A
$I_D @ T_C=100^\circ\text{C}$	Continuous Drain Current <sup>1</sup>	45	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	280	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	56	mJ
$I_{AS}$	Avalanche Current	40	A
$P_D @ T_C=25^\circ\text{C}$	Total Power Dissipation <sup>4</sup>	37	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

**ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$  unless otherwise specified)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10V, I_D=20A$		4.3	5.2	m $\Omega$
		$V_{GS}=4.5V, I_D=20A$		5.5	6.6	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	1.3	2.5	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=30V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	$\mu A$
		$V_{DS}=24V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	10	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$R_g$	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	0.85	---	$\Omega$
$Q_g$	Total Gate Charge (10V)	$V_{DS}=15V, V_{GS}=10V, I_D=20A$	---	38	---	nC
$Q_{gs}$	Gate-Source Charge		---	5.0	---	
$Q_{gd}$	Gate-Drain Charge		---	12	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=15V, V_{GS}=10V, R_G=3\Omega, I_D=20A$	---	8.5	---	ns
$T_r$	Rise Time		---	9	---	
$T_{d(off)}$	Turn-Off Delay Time		---	30	---	
$T_f$	Fall Time		---	10	---	
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$	---	1920	---	pF
$C_{oss}$	Output Capacitance		---	300	---	
$C_{rss}$	Reverse Transfer Capacitance		---	260	---	

LOW VOLTAGE MOSFET (N-CHANNEL)

Typical Characteristics

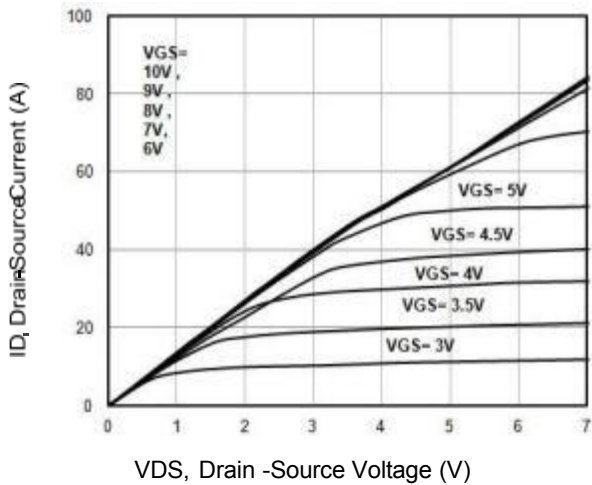


Fig1. Typical Output Characteristics

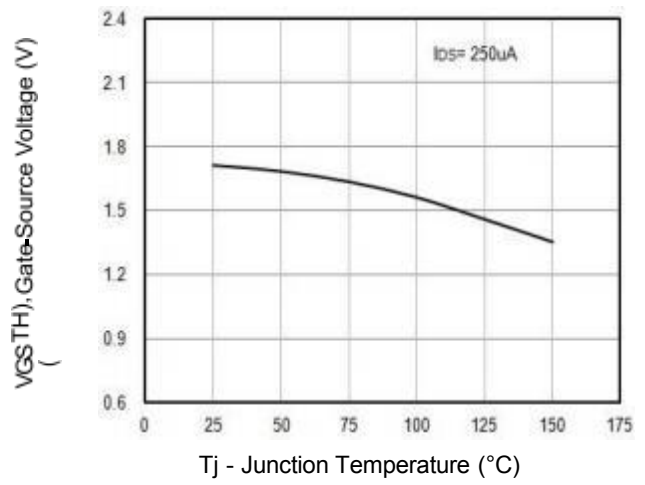


Fig2. VGS(TH) Gate-Source Voltage Vs. Tj

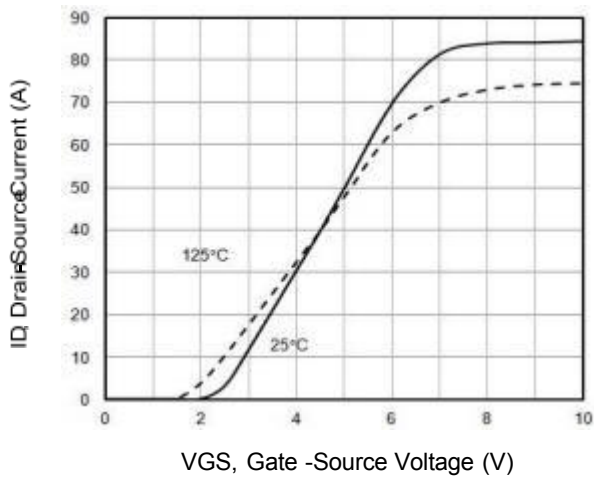


Fig3. Typical Transfer Characteristics

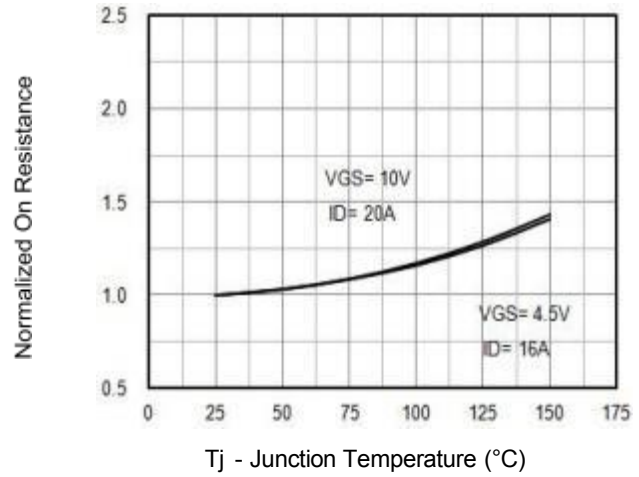


Fig4. Normalized On-Resistance Vs. Tj

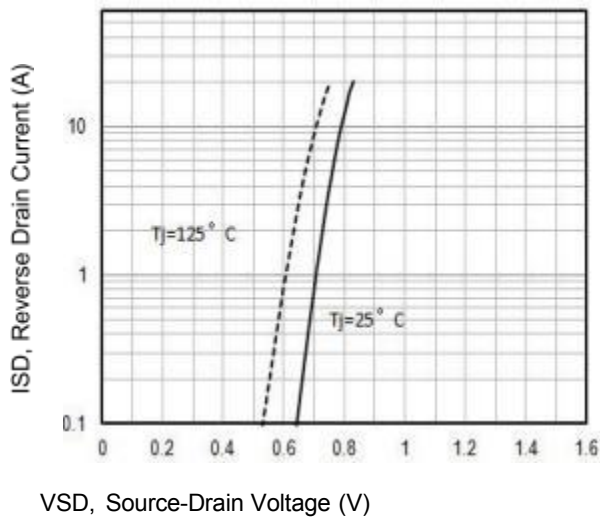


Fig6. Maximum Safe Operating Area Voltage

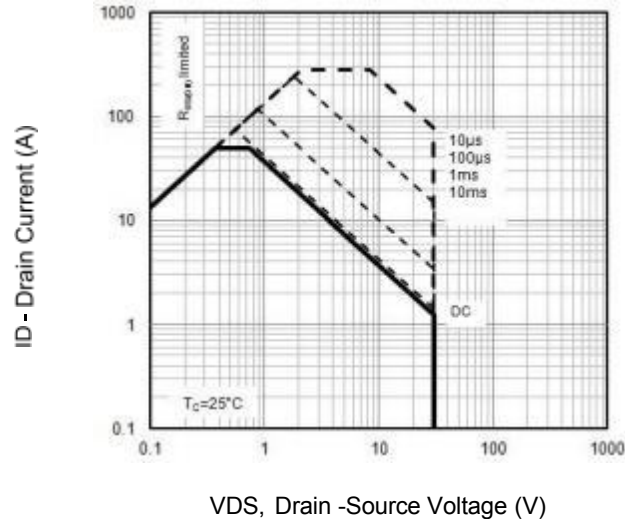


Fig5. Typical Source-Drain Diode Forward

LOW VOLTAGE MOSFET (N-CHANNEL)

Typical Characteristics

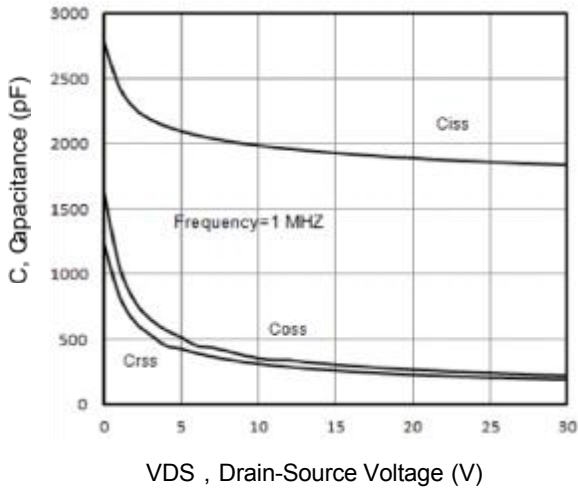


Fig7. Typical Capacitance Vs.Drain-Source Voltage

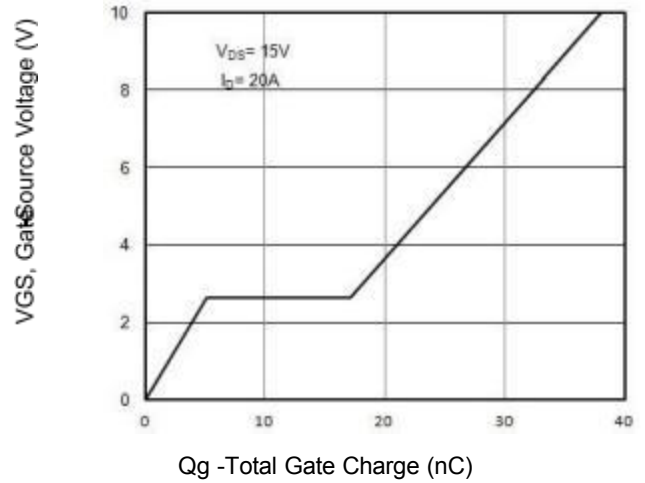


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

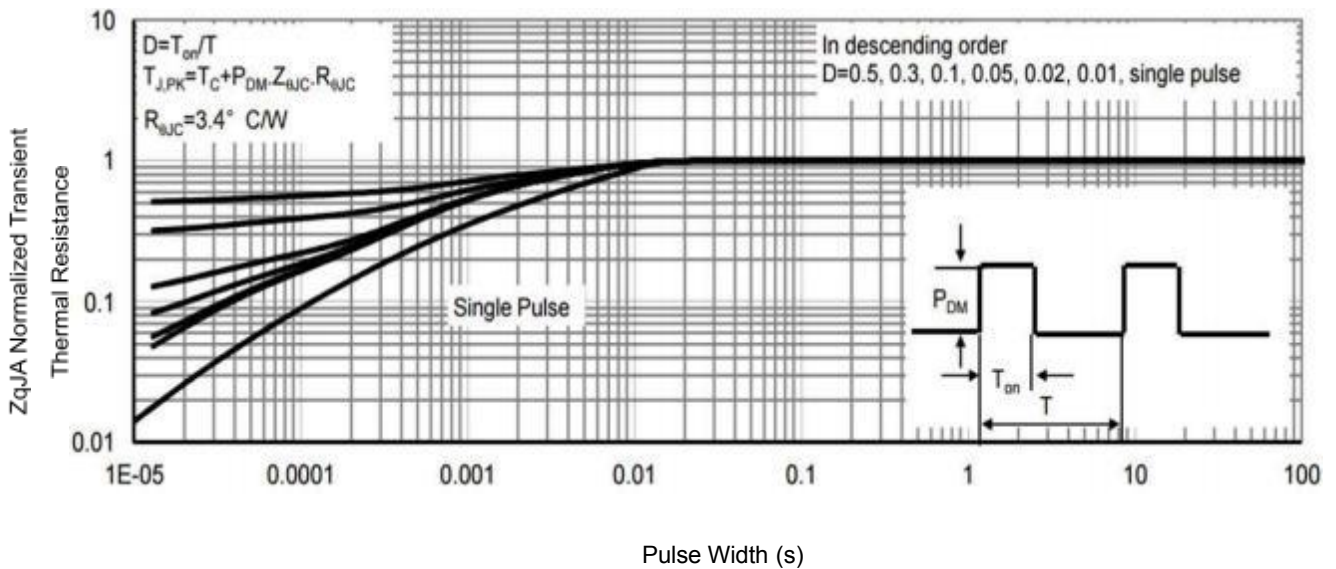


Fig9. Normalized Maximum Transient Thermal Impedance

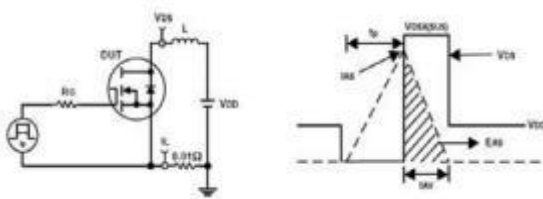


Fig10. Unclamped Inductive Test Circuit and waveforms

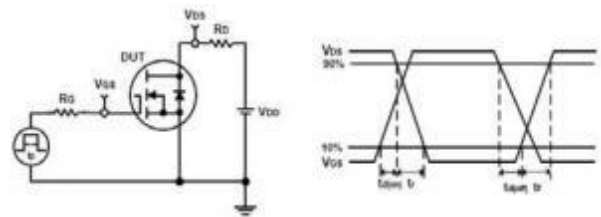
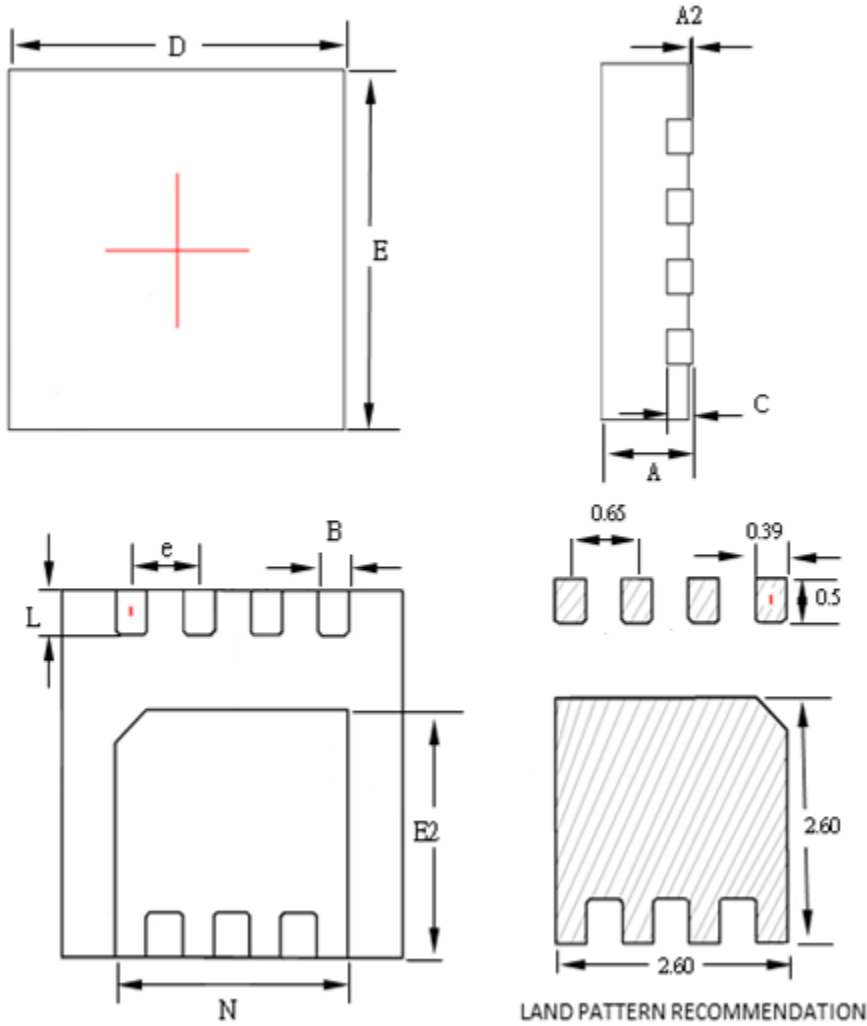


Fig11. Switching Time Test Circuit and waveforms

LOW VOLTAGE MOSFET (N-CHANNEL)

PDFN3333



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.75	0.80	0.028	0.030	0.031
A2	0.00	--	0.05	0.000	--	0.002
B	0.24	0.30	0.35	0.009	0.012	0.014
C	0.10	0.15	0.25	0.004	0.006	0.010
D	3.15	3.30	3.40	0.124	0.130	0.134
E	3.15	3.30	3.40	0.124	0.130	0.134
E2	2.15	2.25	2.35	0.085	0.089	0.093
L	0.35	0.40	0.45	0.014	0.016	0.018
N	2.10	2.25	2.35	0.083	0.089	0.093
e	--	0.65	--	--	0.026	--

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