

FEATURES

- Wide Operating Voltage Range of 3.0V to 18.0V
- Maximum Input Current of 1 μ A at 18V over Full Package-Temperature range, 100nA at 18V and 25°C
- Standardized symmetrical output characteristics

APPLICATIONS

- Logic Conversion
- Pulse Shaping
- Oscillators
- High-Input-Impedance Amplifiers

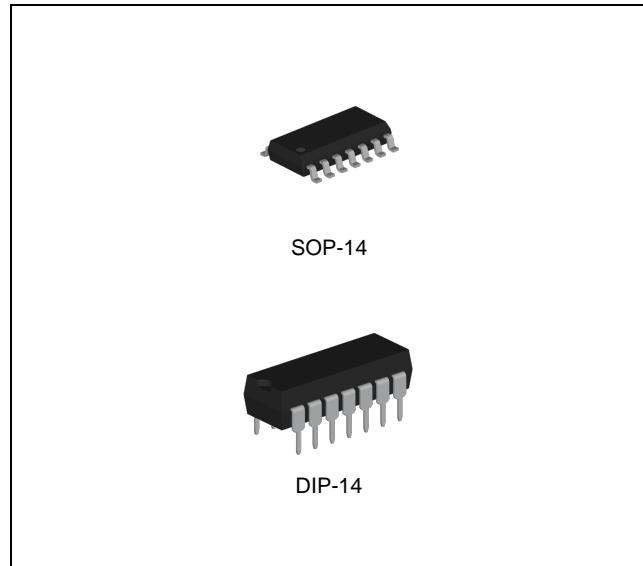
DESCRIPTION

The CD4069UB types consist of six inverter circuits. These devices are intended for all general-purpose inverter applications where the medium-power TTL-drive and logic-level-conversion capabilities of circuits are not required. Each of the six inverters is a single stage.

ABSOLUTE MAXIMUM RATINGS (Note 1)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
DC Supply Voltage (Referenced to V _{SS})	V _{DD}	-0.5	20	V
DC Input Voltage (Referenced to V _{SS})	V _{IN}	-0.5	V _{DD} + 0.5	V
DC Input Current	I _{IN}	-	±10	mA
Maximum Junction Temperature	T _J	-	150	°C
Storage Temperature	T _{STG}	-65	150	°C

Note1. Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.



ORDERING INFORMATION

Device	Package
CD4069UBD	SOP-14
CD4069UBN	DIP-14

CMOS Hex Inverters

CD4069UB

RECOMMENDED OPERATING CONDITIONS (Note 2)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V_{DD}	3	18	V
DC Input Voltage	V_{IN}	0	V_{DD}	V
DC Output Voltage	V_{OUT}	0	V_{DD}	V
Operating Free-Air Temperature Range	T_A	-55	125	°C

Note 2. The device is not guaranteed to function outside its operating ratings.

ORDERING INFORMATION

Package	Order No.	Description	Supplied As	Status
SOP-14	CD4069UBD	Hex Inverters	Tape & Reel	Active
DIP-14	CD4069UBN	Hex Inverters	Tube	Active

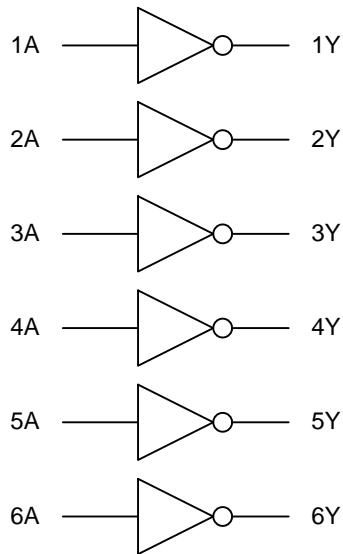
PIN CONFIGURATION

SOP-14		DIP-14	
1A	1	14	VDD
1Y	2	13	6A
2A	3	12	6Y
2Y	4	11	5A
3A	5	10	5Y
3Y	6	9	4A
VSS	7	8	4Y

PIN DESCRIPTION

Pin No.		Pin Name	Pin Function
SOP-14	DIP-14		
1	1	1A	Input 1
2	2	1Y	Output 1
3	3	2A	Input 2
4	4	2Y	Output 2
5	5	3A	Input 3
6	6	3Y	Output 3
7	7	VSS	Ground
8	8	4Y	Output 4
9	9	4A	Input 4
10	10	5Y	Output 5
11	11	5A	Input 5
12	12	6Y	Output 6
13	13	6A	Input 6
14	14	VDD	Power Supply

BLOCK DIAGRAM



DC ELECTRICAL CHARACTERISTICS

Voltages referenced to V_{SS}.

SYMBOL	PARAMETER	TEST CONDITION	V _{DD}	Limit			UNIT	
				-55°C	25°C	125°C		
V _{IH}	Minimum High-Level Input Voltage	V _{OUT} = 0.5V V _{OUT} = 1.0V V _{OUT} = 1.5V	5 V	4.0	4.0	4.0	V	
			10 V	8.0	8.0	8.0		
			15 V	12.5	12.5	12.5		
V _{IL}	Maximum Low-Level Input Voltage	V _{OUT} = V _{DD} - 0.5V V _{OUT} = V _{DD} - 1.0V V _{OUT} = V _{DD} - 1.5V	5 V	1.0	1.0	1.0	V	
			10 V	2.0	2.0	2.0		
			15 V	2.5	2.5	2.5		
V _{OH}	Minimum High-Level Output Voltage	V _{IN} = V _{SS}	5 V	4.95	4.95	4.95	V	
			10 V	9.95	9.95	9.95		
			15 V	14.95	14.95	14.95		
V _{OL}	Maximum Low-Level Output Voltage	V _{IN} = V _{DD}	5 V	0.05	0.05	0.05	V	
			10 V	0.05	0.05	0.05		
			15 V	0.05	0.05	0.05		
I _{IN}	Maximum Input Leakage Current	V _{IN} = V _{DD} or V _{SS}	18 V	±0.1	±0.1	±1.0	µA	
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{DD} or V _{SS}	5 V	0.25	0.25	7.5	µA	
			10 V	0.5	0.5	15		
			15 V	1.0	1.0	30		
			20 V	5.0	5.0	150		
I _{OL}	Minimum Output Low (Sink) Current	V _{IN} = V _{DD} or V _{SS}	V _{OL} = 0.4V	5 V	0.64	0.51	0.36	mA
			V _{OL} = 0.5V	10 V	1.6	1.3	0.9	
			V _{OL} = 1.5V	15 V	4.2	3.4	2.4	
I _{OH}	Minimum Output High (Source) Current	V _{IN} = V _{DD} or V _{SS}	V _{OH} = 2.5V	5 V	-2.0	-1.6	-1.15	mA
			V _{OH} = 4.6V	5 V	-0.64	-0.51	-0.36	
			V _{OH} = 9.5V	10 V	-1.6	-1.3	-0.9	
			V _{OH} = 13.5V	15 V	-4.2	-3.4	-2.4	

AC ELECTRICAL CHARACTERISTICS

$C_L = 50 \text{ pF}$, $R_L = 200\text{k}\Omega$, Input $t_r = t_f = 20 \text{ ns}$

SYMBOL	PARAMETER	V_{DD}	Limit			UNIT
			-55°C	25°C	125°C	
t_{PLH} , t_{PHL}	Maximum Propagation Delay, Input A to Output Y (Figure 1)	5 V	110	110	110	ns
		10 V	60	60	80	
		15 V	50	50	80	
t_{TLH} , t_{THL}	Maximum Output Transition Time, Any Output (Figure 1)	5 V	200	200	200	ns
		10 V	100	100	100	
		15 V	80	80	80	
C_{IN}	Maximum Input Capacitance	—		15		pF

FUNCTION TABLE

Input (A)	Output (Y)
H	L
L	H

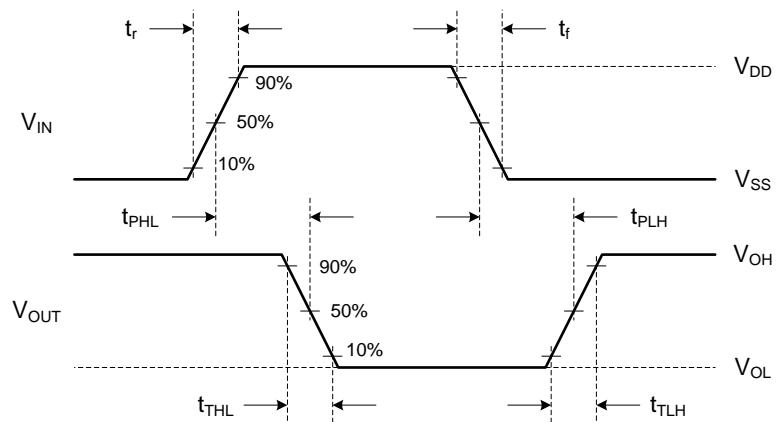
SWITCHING WAVEFORMS

Fig. 1.

TYPICAL OPERATING CHARACTERISTICS

T.B.D.

REVISION NOTICE

The description in this datasheet is subject to change without any notice to describe its electrical characteristics properly.

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