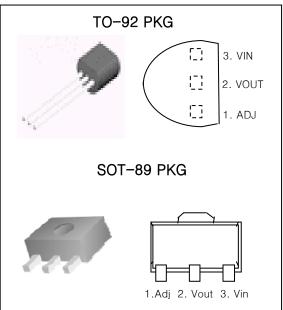
ADJUSTABLE VOLTAGE REGULATOR (POSITIVE)

3-TERMINAL 100mA POSITIVE ADJUSTABLE REGULATOR

FEATURES

- Output current in Excess of 100mA
- Output Adjustable Between 1.2V and 37V
- Internal Thermal-Overload Protection
- Internal Short-Circuit Current-Limiting
- Output Transistor Safe-Area Compensation
- Floating operation for high voltage applications
- Moisture Sensitivity Level 3

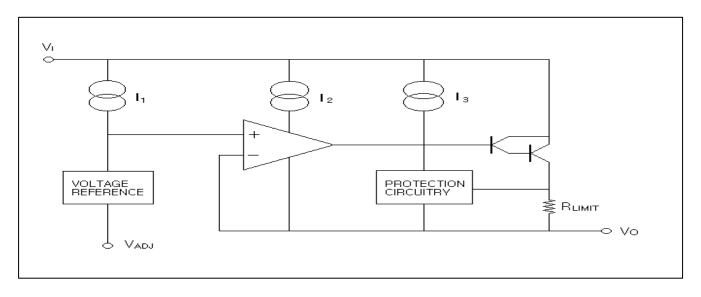


DISCRIPTION

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 100mA of load current with an output voltage adjustable over a 1.2 to 37V. It employs internal current limiting, thermal shut-down and safe area compensation.

ORDERING INFORMATION					
Device	Marking	Package			
LM317L	LM317L	TO-92			
LM317F	317	SOT-89			

BLOCK DIAGRAM



TYPICAL APPLICATIONS

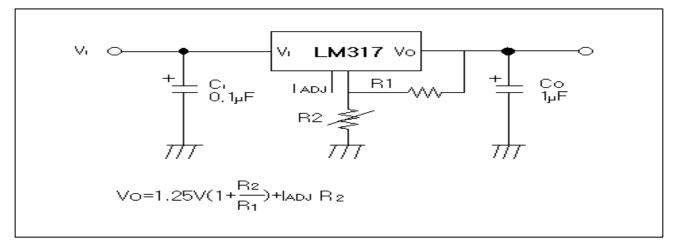


Fig.5 Programmable Regulator

C_I is required when regulator is located in appreciable distance from power supply filter. Co is not needed for stability, however, it does improve transient response. Since I_{ADJ} is controlled to less than 100μ A, the error associated with this term is negligible in most applications.

ADJUSTABLE VOLTAGE REGULATOR (POSITIVE)

ABSOLUTE MAXIMUM RATINGS (TA=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
Input-output Voltage Differential	Vi–Vo	40	V
Lead Temperature	Tlead	230	Ĵ
Power Dissipation	Po	Internally limited	_
Operating Temperature Range	Topr	0 ~ +125	Ĵ
Storage Temperature Range	Tstg	-65 ~ +125	C

ELECTRICAL CHARACTERISTICS

(VI-Vo=5V, Io=40mA, $0^{\circ}C \le T_J \le 125^{\circ}C$, IMAX=100mA, unless otherwise specified)

Characteristic	Symbol	Test condition		Min.	Typ.	Max.	Unit
Line Regulation	△Vo	$T_A{=}0~\sim~125{}^\circ\!\mathrm{C}$	3V≤VI-Vo≤40V		0.01	0.04	%/V
			3V≤VI-Vo≤40V		0.02	0.07	%/V
	∆Vo	T _A =25℃, 10mA≤Io≤I _{MAX}					
Load Regulation		Vo≤5V			10	25	mV
		Vo≥5V			0.1	0.5	%/Vo
		$10 \text{mA} \le 10 \le 1_{\text{MAX}}$					
		Vo≤5V			20	70	mV
		Vo≥5V			0.3	1.5	%/Vo
Adjustable Pin Current	ADJ				46	100	μA
Adjustable Pin Current Cha	\triangle $ $ adj	3V≤VI-Vo≤40V					
		$10 \text{mA} \le 10 \le 1_{\text{MAX}}$			0.2	5	μA
		PSPMAX					
Reference Voltage	Vref	3V≤VIN-VOUT≤40V					
		$10 \text{ mA} \le 10 \le 1_{\text{MAX}}$		1.20	1.25	1.30	V
		Pd≤Pmax		1			
Temperature Stability	ST⊤				0.7		%/Vo
Minimum Load Current to	Laws	VI-V0=40V			3.5	10	mA
Maintain Regulation	L(MIN)				3.5	10	IIIA
Maximum Output Current	O(MAX)	VI-Vo≤5V, Pd≤Pmax		100	200		mA
		Vi−Vo≤40V, Pa	•Vo≤40V, Pd≤Pmax, Ta = 25 ℃		400		mA
RMS Noise, % of Vout	θN	T _A =25℃, 10Hz≤f≤10KHz			0.003	0.01	%/Vo
Ripple Rejection	RR	Vo=10V, f=120Hz					
		without Cadj			60		dB
		Cadj=10 µF		66	75		
Long-Term Stability,	ST	T₄=25℃, for end point		0.1	0.3	1	%
Т _Ј =Т _{нідн}	measurements, 1000Hf		ents, 1000HR		0.5		

* Load and line regulation are specified at constant junction temperature. Change in Vo due to heating effects must be taken into account separately. Pulse testing with low duty is used.

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