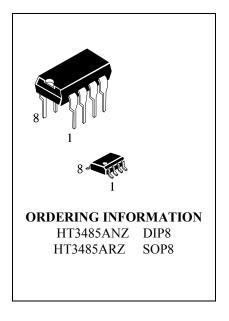
# Low-Power, Slew-Rate-Limited RS-485/RS-422 Transceivers

#### General Description

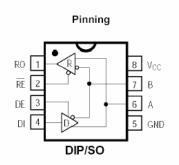
The HT3085 is low-power transceivers for RS-485-3.3 and RS- 422-3.3 communication. IC contains one driver and one receiver. The driver slew rates of the HT3485 is not limited, allowing them to transmit up to 2.5Mbps.

These transceivers draw between  $120\mu A$  and  $500\mu A$  of supply current when unloaded or fully loaded with disabled drivers. All parts operate from a single 3.3V supply. Drivers are short-circuit current limited and are protected against excessive power dissipation by thermal shutdown circuitry that places the driver outputs into a high-impedance state. The receiver input has a fail-safe feature that guarantees a logic-high output if the input is open circuit.



#### **Features**

- Low Quiescent Current: 300μA
- -7V to +12V Common-Mode Input Voltage Range
- Three-State Outputs
- 30ns Propagation Delays, 5ns Skew
- Full-Duplex and Half-Duplex Versions Available
- Operate from a Single 3.3V Supply
- Allows up to 32 Transceivers on the Bus
- Data rate: 2,5 Mbps
- Current-Limiting and Thermal Shutdown for Driver Overload Protection
- The transmitter outputs and receiver inputs are protected to  $\pm 15$ kV Air ESD.



## **ABSOLUTE MAXIMUM RATINGS**

Supply Voltage (V <sub>CC</sub> ) 7V	Continuous Power Dissipation ( $T_A = +70^{\circ}C$ )
Control Input Voltage -0.3V to 7V	8-Pin Plastic DIP (derate 9.09mW/°C above
	+70°C) 727mW
Driver Input Voltage (DI) -0.3V to 7V	8-Pin SOP (derate 5.88mW/°C above +70°C)
	471mW
Driver Output Voltage (A, B) -7.5V to +12.5V	Operating Temperature Ranges 0°C to +70°C
Receiver Input Voltage (A, B) -7.5V to +12.5V	Storage Temperature Range -65°C to +160°C
Receiver Output Voltage (RO) -0.3V to (V <sub>CC</sub> +0.3V)	Lead Temperature (soldering, 10sec) +300°C

## DC ELECTRICAL CHARACTERISTICS

( $V_{CC} = 3.3 V \pm 10\%$ ,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITION	NS	MIN	TYP	MAX	UNITS
Differential Driver Output (no load)	Vodi		2			V	
Differential Driver Output	V <sub>OD2</sub>	R = 100  (RS-422)		1			V
(with load)		R = 54 (RS-485), F	igure 4	0.8			
Change in Magnitude of Driver Differential Output Voltage for Complementary Output States	$\Delta V$ od	R = 54 or 50 , Figure 4				0.2	V
Driver Common-Mode Output Voltage	Voc	R = 54 or 100 , Figure 4				2	V
Change in Magnitude of Driver Common-Mode Output Voltage for Complementary Output States	$\Delta V_{ ext{OD}}$	R = 54 or 100 , Figure 4				0.2	V
Input High Voltage	$V_{\mathrm{IH}}$	DE, DI, RE		2.0			V
Input Low Voltage	VIL	DE, DI, RE				0.8	V
Input Current	I <sub>IN1</sub>	DE, DI, RE				±2	μΑ
Input Current (A, B)	IIN2	$DE = 0V;   V_{IN} = 12V$				1.0	mA
		$V_{CC} = 0V \text{ or } 5.25V, V_{IN} = -7V$				-0.8	
Receiver Differential Threshold Voltage	V <sub>TH</sub>	$-7V \le V_{CM} \le 12V$		-0.2		0.2	V
Receiver Input Hysteresis	$\Delta V_{\text{TH}}$	$V_{CM} = 0V$			70		mV
Receiver Output High Voltage	Voh	$I_0 = -1.5 \text{mA}, \text{ VID} = 200 \text{mV}$		2.5			V
Receiver Output Low Voltage	$V_{\text{OL}}$	$I_0 = 2.5 \text{mA}, VID = -200 \text{mV}$				0.4	V
Three-State (high impedance) Output Current at Receiver	Iozr	$0.4V \le V_0 \le 2.4V$				±1	μА
Receiver Input Resistance	Rin	$-7V \le V_{CM} \le 12V$		12			k

## DC ELECTRICAL CHARACTERISTICS (continued)

(Vcc =  $3.3V \pm 10\%$ , T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TY P	MAX	UNITS
No-Load Supply Current	Icc	$DE = V_{CC}$		500	800	
(Note 3)		$\overline{RE} = 0V \text{ or } V_{CC}$		300	400	μΑ
		DE = 0V				
Driver Short-Circuit Current,						
	Iosdi	$-7V \le V_0 \le 12V \text{ (Note 4)}$			250	mA
Vo= High						
Driver Short-Circuit Current,						
	Iosd2	-7V ≤ Vo≤12V (Note 4)			250	mA
$V_0 = Low$						
Receiver Short-Circuit Current	Iosr	$0V \le V_O \le V_{CC}$	±6.5		95	mA

#### **SWITCHING CHARACTERISTICS**

(Vcc =  $3.3V \pm 10\%$ , T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Input to Output	<b>t</b> PLH	$R_{DIFF} = 54\Omega$	10	30	60	ns
	<b>t</b> PHL	$C_{L1} = C_{L2} = 100 pF$	10	30	60	
Driver Output Skew to Output	tskew	$R_{DIFF} = 54\Omega$ , $CL1 = CL2 = 100pF$		5	10	ns
Driver Enable to Output High	tzн	C <sub>L</sub> = 100pF, S2 closed		45	90	ns
Driver Enable to Output Low	<b>t</b> zl	C <sub>L</sub> = 100pF, S1 closed		45	90	ns
Driver Disable Time from Low	tlz	C <sub>L</sub> = 15pF, S1 closed		40	80	ns
Driver Disable Time from High	thz	C <sub>L</sub> = 15pF, S2 closed		40	80	ns
tPLH - tPHL   Differential	tskd	$R_{DIFF} = 54\Omega$		13		ns
Receiver Skew		$C_{L1} = C_{L2} = 100 pF$				
Receiver Enable to Output Low	tzl	C <sub>RL</sub> = 15pF, S1 closed			50	ns
Receiver Enable to Output High	<b>t</b> zh	C <sub>RL</sub> = 15pF, S2 closed		20	50	ns
Receiver Disable Time from	tlz	C <sub>RL</sub> = 15pF, S1 closed		20	50	ns
Low						
Receiver Disable Time from	tHZ	C <sub>RL</sub> = 15pF, S2 closed		20	50	ns
High						
Maximum Data Rate	fmax	_	2.5			Mbps

Note 1: All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to device ground unless otherwise specified.

Note 2: All typical specifications are given for Voc = 3.3V and TA = +25°C.

Note 3: Supply current specification is valid for loaded transmitters when DE = 0V.

Note 4: Applies to peak current. See Typical Operating Characteristics.

# Test Circuits

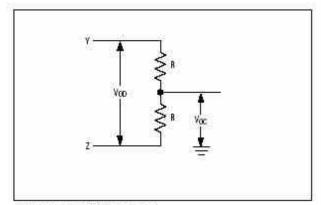
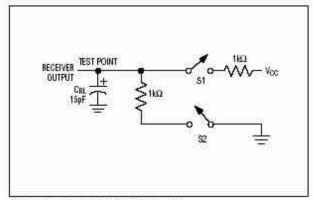


Figure 4. Driver DC Test Load



Rgure 5. Receiver Timing Test Load

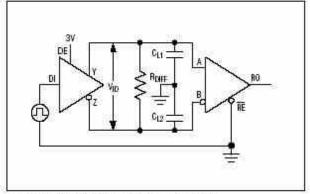
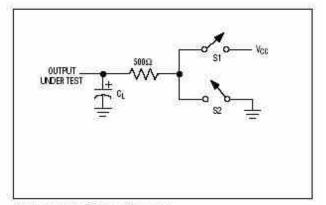


Figure 6. Driver/Receiver Timing Test Circuit



Rgure 7. Driver Timing Test Load

# Operation timing diagrams of HT3485

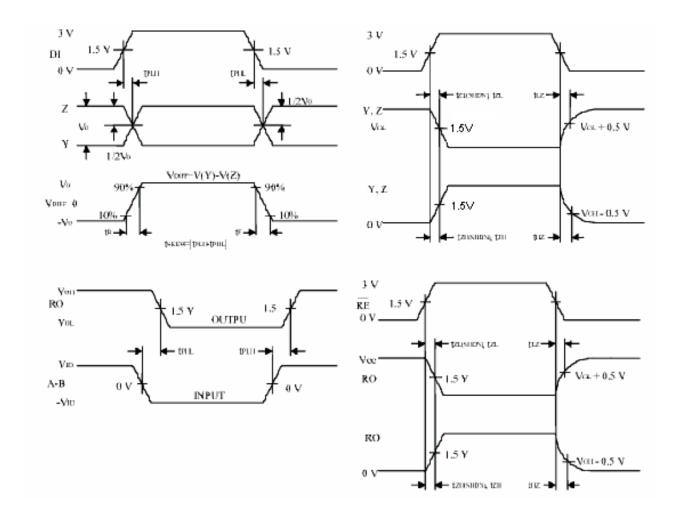
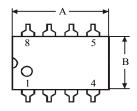


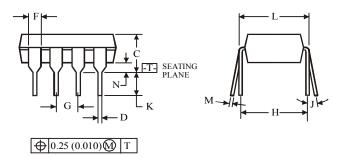
Table of ILX 3485 operation

Transmitting				Reco	eiving			
	Inputs		Outp	uts X		Inputs		Outputs
RE	DE	DI	Z	Y	RE	DE	A-B	RO
X	1	1	0	1	0	0	+0.2V	1
X	1	0	1	0	0	0	-0.2V	0
0	0	X	Z	Z	0	0	open	1
1	0	X	Z	Z	1	0	X	Z

X-don't care Z-high impedance

(DIP8)





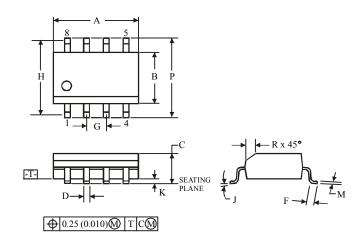
#### NOTES:

Dimensions "A", "B" do not include mold flash or protrusions.
 Maximum mold flash or protrusions 0.25 mm (0.010) per side.



	Dimension, mm				
Symbol	MIN	MAX			
A	8.51 10.16				
В	6.1 7.11				
C		5.33			
D	0.36	0.56			
F	1.14	1.78			
G	2.54				
Н	7.62				
J	0°	10°			
K	2.92	3.81			
L	7.62 8.26				
M	0.2 0.36				
N	0.38				

(SOP8)



#### **NOTES:**

- 1. Dimensions A and B do not include mold flash or protrusion.
- 2. Maximum mold flash or protrusion 0.15 mm (0.006) per side for A; for B 0.25 mm (0.010) per side.



	Dimension, mm				
Symbol	MIN MAX				
A	4.8	5			
В	3.8 4				
C	1.35 1.75				
D	0.33 0.51				
F	0.4 1.27				
G	1.27				
Н	5.	72			
J	0° 8°				
K	0.1 0.25				
M	0.19 0.25				
P	5.8 6.2				
R	0.25 0.5				

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74LVCE1G126FZ4-7 74LVC1G125FW4-7 NL17SH17P5T5G NLV17SZ07DFT2G NLVHCT244ADTR2G NC7WZ17FHX 74HCT126T1413 74LVC2G34FW4-7 NL17SH125P5T5G 74VHC9126FT(BJ) NLV37WZ07USG RHRXH162244K1 74AUP1G34FW5-7
74AUP1G07FW5-7 74LVC1G126FW4-7 74LVC2G126RA3-7 NLX2G17CMUTCG 74LVCE1G125FZ4-7 Le87501NQC
NLVVHCT50ADTR2G TC74HC4050AP(F) 74LVCE1G07FZ4-7 NLX3G16DMUTCG NLX2G06AMUTCG NLVVHC1G50DFT2G
NLU2G17AMUTCG LE87100NQC LE87100NQCT LE87285NQC LE87285NQCT LE87290YQC LE87290YQCT 74AUP1G125FW5-7
NLU2G16CMUTCG