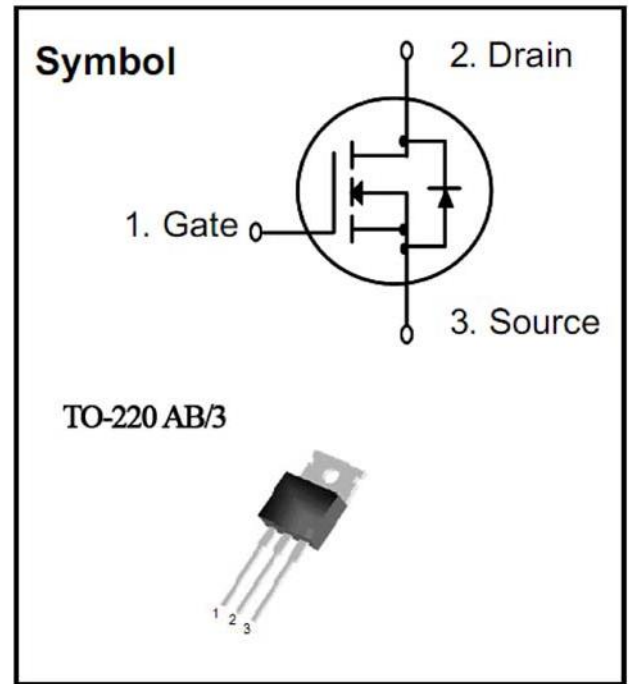**N-Channel MOSFET**

Features

- $R_{DS(on)}$ (Max 0.011 Ω)@V_{GS}=10V
- Gate Charge (Typical 80 nC)
- Maximum Junction Temperature Range (175 °C)

Symbol

2. Drain

1. Gate

3. Source

TO-220 AB/3

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{DSS}	Drain to Source Voltage	80	V
I_D	Continuous Drain Current(@T_C = 25 °C)	80	A
	Continuous Drain Current(@T_C = 100 °C)	75	A
I_{DM}	Drain Current Pulsed	300[1]	A
V_{GS}	Gate to Source Voltage	±20	V
E_{AS}	Single Pulsed Avalanche Energy	1310[2]	mJ
E_{AR}	Repetitive Avalanche Energy	17.3[1]	mJ
dv/dt	Peak Diode Recovery dv/dt	7.0[3]	V/ns
P_D	Total Power Dissipation(@T_C = 25 °C)	173	W
	Derating Factor above 25 °C	1.15	W/°C
T_{STG}	Operating Junction Temperature	-55 ~ 175	°C
T_J	Storage Temperature	150	°C
Notes			

Notes

[1] Repeativity rating : pulse width limited by junction temperature

[2] L = 0.32 mH, I_{AS} = 75 A, V_{DD} = 50 V, R_G = 25 Ω , Starting T_J = 25 °C

[3] I_{SD} ≤ 75 A, di/dt ≤ 300 A/us, V_{DD} ≤ BV_{DSS}, Starting T_J = 25 °C

Thermal Characteristics

Symbol	Parameter	Value			Units
		Min.	Typ.	Max.	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	-	-	0.87	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient*	-	0.5	-	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	-	-	62.5	°C/W

* When mounted on the minimum pad size recommended (PCB Mount)

Source-Drain Diode Characteristics and Maximum Ratings

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I_S	Maximum Continuous Source-Drain Diode Forward Current		-	-	80	A
I_{SM}	Maximum Pulsed Source-Drain Diode Forward Current		-	-	320	
V_{SD}	Diode Forward Voltage	$I_S = 100$ A, $V_{GS} = 0$ V	-	-	1.5	V
t_{rr}	Reverse Recovery Time	$I_S = 100$ A, $V_{GS} = 0$ V, dIF/dt = 100 A/us	-	90	-	ns
Q_{rr}	Reverse Recovery Charge		-	250	-	uC

Electrical Characteristics (T_C = 25 °C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 uA	80	-	-	V
$\Delta BV_{DSS}/$ ΔT_J	Breakdown Voltage Temperature coefficient	I_D = 250 uA, referenced to 25 °C	-	0.08	-	V/°C
I_{DSS}	Drain-Source Leakage Current	V_{DS} = 80 V, V_{GS} = 0 V	-	-	10	uA
		V_{DS} = 64 V, T_C = 125 °C	-	-	100	uA
I_{GSS}	Gate-Source Leakage, Forward	V_{GS} = 20 V, V_{DS} = 0 V	-	-	100	nA
	Gate-source Leakage, Reverse	V_{GS} = -20 V, V_{DS} = 0 V	-	-	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250 uA	2.0	-	4.0	V
$R_{DS(ON)}$	Static Drain-Source On-state Resis-tance	V_{GS} = 10 V, I_D = 37.5 A	-	-	0.008	Ω
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0 V, V_{DS} =25 V, f = 1 MHz	-	2600	3380	pF
C_{oss}	Output Capacitance		-	940	1220	
C_{rss}	Reverse Transfer Capacitance		-	210	275	
Dynamic Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DD} = 40 V, I_D = 100 A, R_G =25 Ω Pulse Width ≤ 300us, Q > 50	-	30	70	ns
t_r	Rise Time		-	225	460	
$t_{d(off)}$	Turn-off Delay Time		-	165	340	
t_f	Fall Time		-	155	320	
Q_g	Total Gate Charge	V_{DS} = 64 V, V_{GS} = 10 V, I_D = 100 A	-	80	105	nC
Q_{gs}	Gate-Source Charge		-	15	-	
Q_{gd}	Gate-Drain Charge(Miller Charge)		-	32	-	

Chip size

Technological
mark 75N08

4,6±0,02

5,5±0,02

0.4±0.02

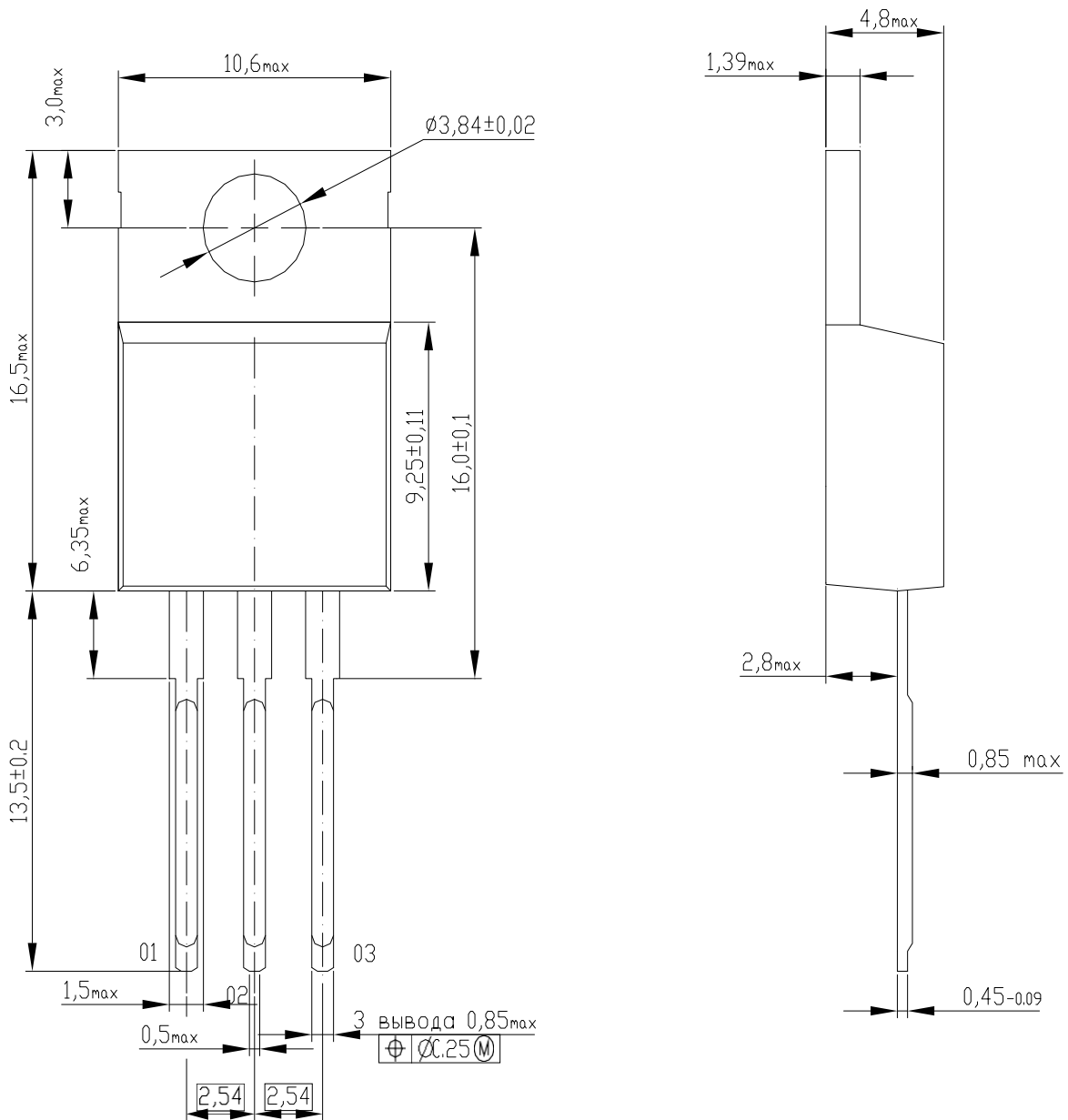## Package Chip

01   02   03
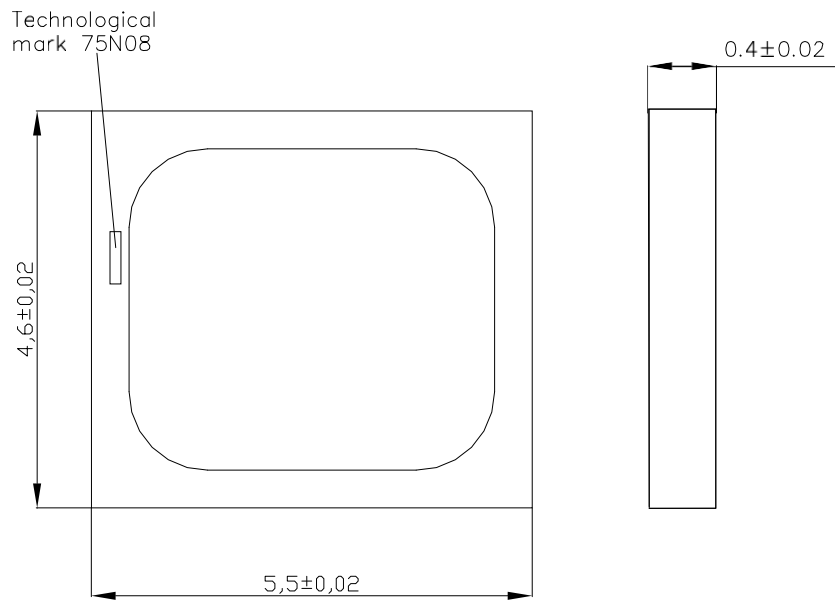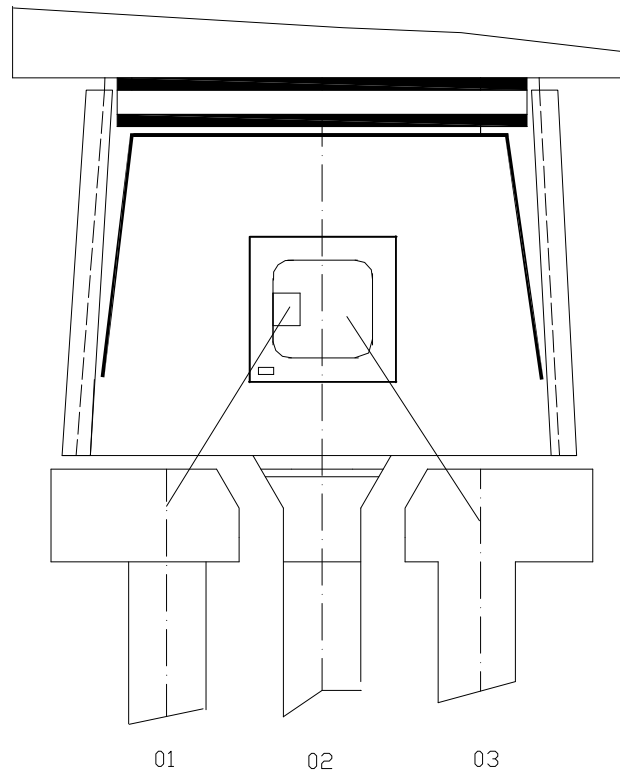
Package Dimensions TO-220 AB/3

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