



CS4N80 A4HD-G

General Description:

CS4N80 A4HD-G, the silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-252, which accords with the RoHS standard.

Features:

- **Fast Switching**
- **ESD Improved Capability**
- **Low Gate Charge** (Typical Data: 25nC)
- **Low Reverse transfer capacitances**(Typical:7.5pF)
- **100% Single Pulse avalanche energy Test**
- **Halogen Free**

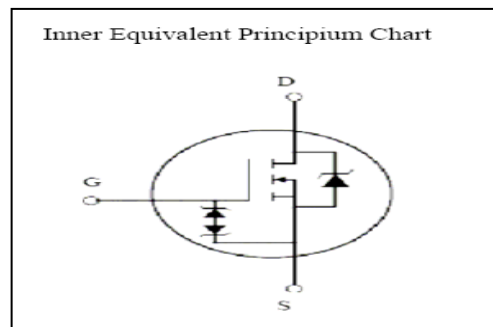
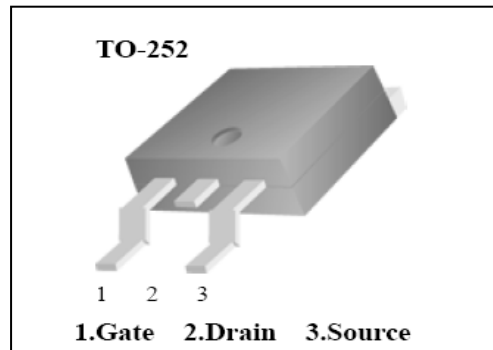
Applications:

Power switch circuit of adaptor and charger.

Absolute (Tc= 25°C unless otherwise specified):

Symbol	Parameter	Rating	Units
V _{DSS}	Drain-to-Source Voltage	800	V
I _D	Continuous Drain Current	4	A
	Continuous Drain Current T _C = 100 °C	2.5	A
I _{DM} ^{a1}	Pulsed Drain Current	16	A
V _{GS}	Gate-to-Source Voltage	± 30	V
E _{AS} ^{a2}	Single Pulse Avalanche Energy	230	mJ
dv/dt ^{a3}	Peak Diode Recovery dv/dt	5.0	V/ns
P _D	Power Dissipation	100	W
	Derating Factor above 25 °C	0.8	W/°C
V _{ESD(G-S)}	Gate source ESD (HBM-C= 100pF, R=1.5kΩ)	4000	V
T _J , T _{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	°C
T _L	Maximum Temperature for Soldering	300	°C

V _{DSS}	800	V
I _D	4	A
P _D (T _C =25 °C)	100	W
R _{DS(ON)Typ}	2.2	Ω



Electrical Characteristics (Tc= 25 °C unless otherwise specified):

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Unit s
			Min.	Typ.	Max.	
V _{DSS}	Drain to Source Breakdown Voltage	V _{GS} =0V, I _D =250 μA	800	--	--	V
Δ BV _{DSS} / Δ T _J	Bvdss Temperature Coefficient	I _D =250uA, Reference 25 °C	--	0.9	--	V/°C
I _{DSS}	Drain to Source Leakage Current	V _{DS} =800V, V _{GS} = 0V, T _a = 25 °C	--	--	25	μA
		V _{DS} =640V, V _{GS} = 0V, T _a = 125 °C	--	--	250	μA
I _{GSS(F)}	Gate to Source Forward Leakage	V _{GS} =+20V	--	--	10	μA
I _{GSS(R)}	Gate to Source Reverse Leakage	V _{GS} =-20V	--	--	-10	μA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R _{DS(ON)}	Drain-to-Source On-Resistance	V _{GS} =10V, I _D =2A	--	2.2	2.8	Ω
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0	--	4.0	V
Pulse width tp ≤ 300 μs, δ ≤ 2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
g _{fs}	Forward Trans conductance	V _{DS} =15V, I _D =2A	--	5.5	--	S
C _{iss}	Input Capacitance	V _{GS} = 0V V _{DS} = 25V f = 1.0MHz	--	865	--	pF
C _{oss}	Output Capacitance		--	82	--	
C _{rss}	Reverse Transfer Capacitance		--	7.5	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t _{d(ON)}	Turn-on Delay Time	I _D =4A V _{DD} = 400V R _G =10Ω	--	18	--	ns
t _r	Rise Time		--	16	--	
t _{d(OFF)}	Turn-Off Delay Time		--	45	--	
t _f	Fall Time		--	14	--	
Q _g	Total Gate Charge	I _D =4A V _{DD} =640V V _{GS} = 10V	--	25	--	nC
Q _{gs}	Gate to Source Charge		--	4	--	
Q _{gd}	Gate to Drain ("Miller") Charge		--	11.5	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)		--	--	4	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	16	A
V_{SD}	Diode Forward Voltage	$I_S=4.0A, V_{GS}=0V$	--	--	1.5	V
t_{rr}	Reverse Recovery Time	$I_S=4.0A, T_j = 25^\circ C$ $dI_F/dt=100A/us,$ $V_{GS}=0V$	--	274	--	ns
Q_{rr}	Reverse Recovery Charge		--	1150	--	nC
I_{RRM}	Reverse Recovery Current		--	8.4	--	A
Pulse width $t_p \leq 300 \mu s, \delta \leq 2\%$						

Symbol	Parameter	Max.	Units
$R_{\theta JC}$	Junction-to-Case	1.25	$^\circ C/W$
$R_{\theta JA}$	Junction-to-Ambient	100	$^\circ C/W$

^{a1}: Repetitive rating; pulse width limited by maximum junction temperature

^{a2}: $L=10mH, I_D=6.8A, Start T_j=25^\circ C$

^{a3}: $I_{SD}=4A, di/dt \leq 100A/us, V_{DD} \leq BV_{DS}, Start T_j=25^\circ C$

Characteristics Curve:

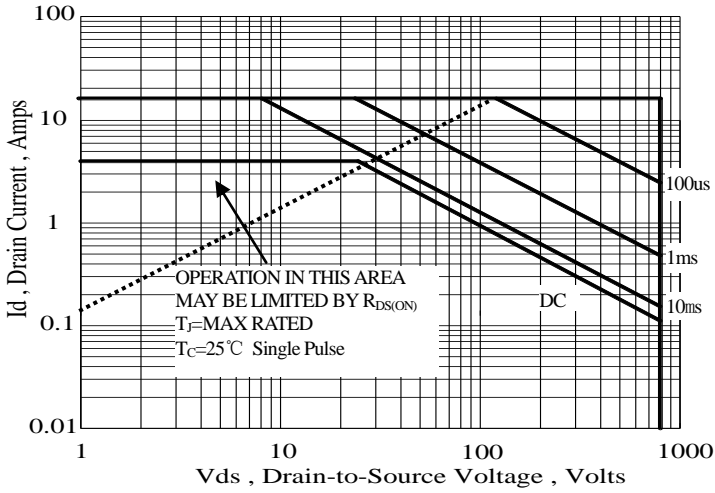


Figure 1 Maximum Forward Bias Safe Operating Area

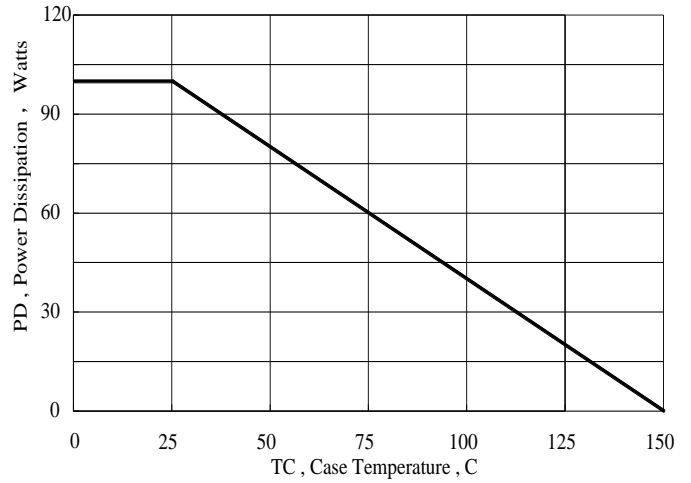


Figure 2 Maximum Power Dissipation vs Case Temperature

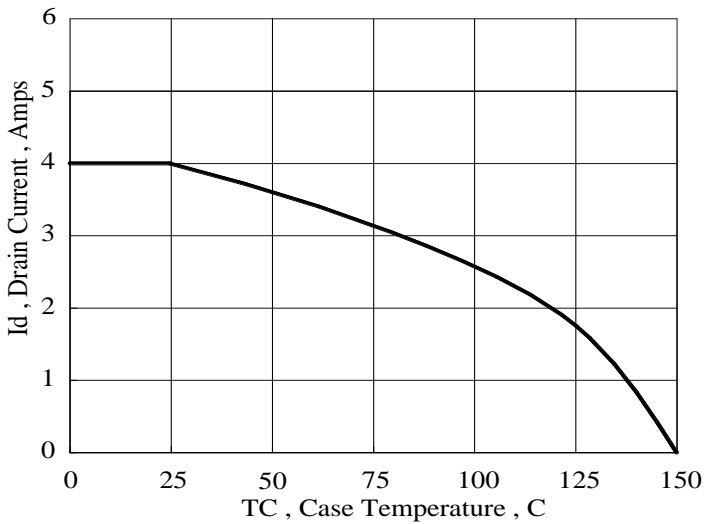


Figure 3 Maximum Continuous Drain Current vs Case Temperature

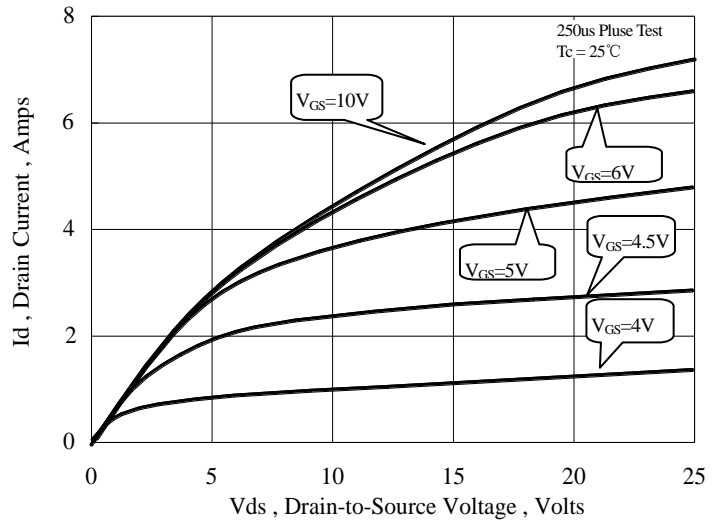


Figure 4 Typical Output Characteristics

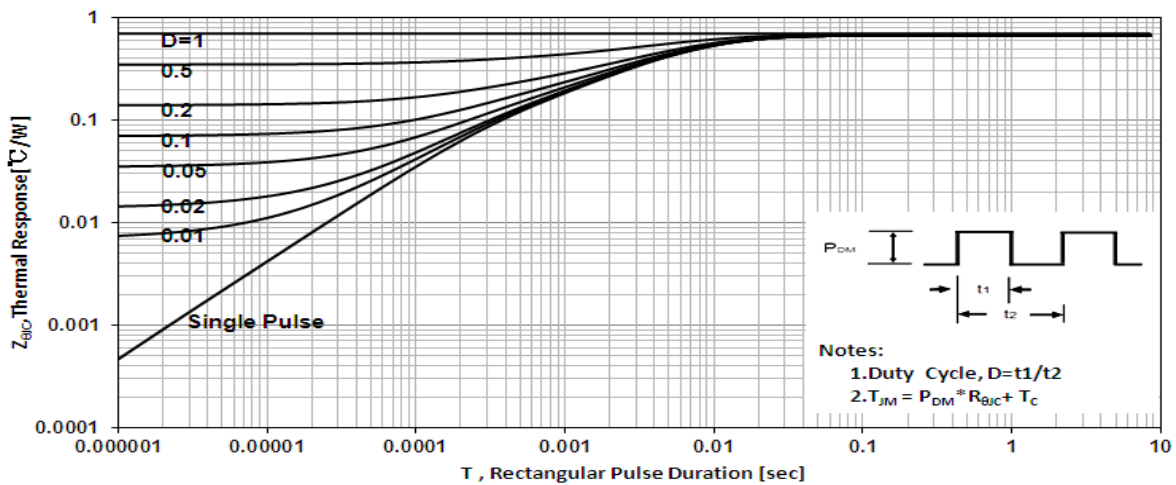


Figure 5 Maximum Effective Thermal Impedance, Junction to Case

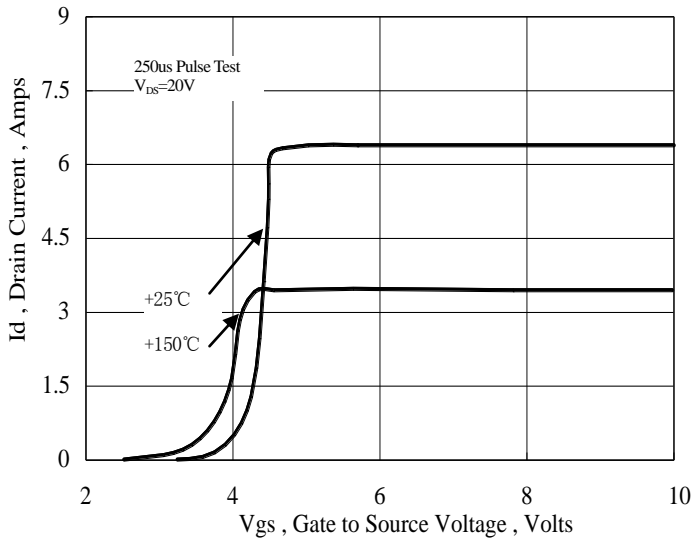


Figure 6 Typical Transfer Characteristics

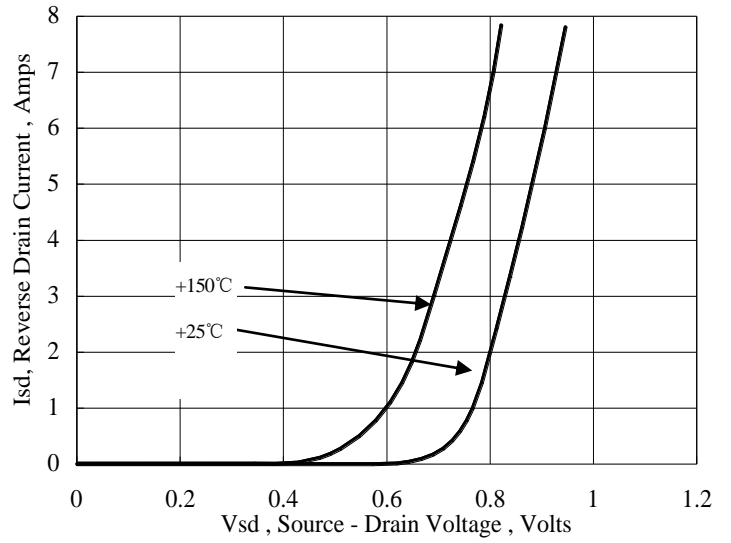


Figure 7 Typical Body Diode Transfer Characteristics

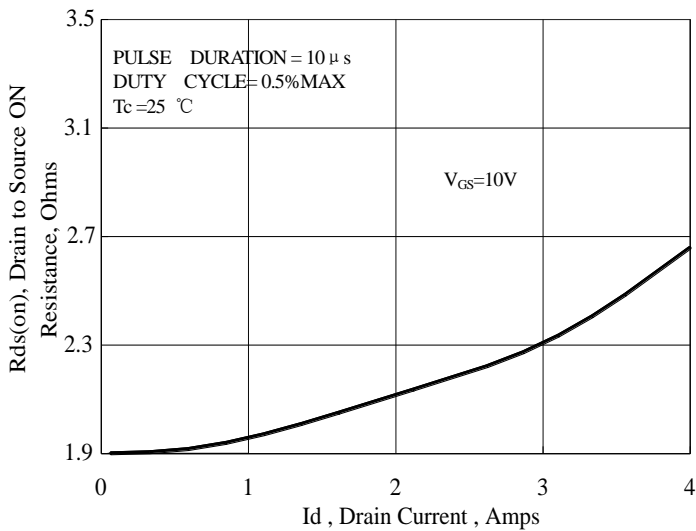


Figure 8 Typical Drain to Source ON Resistance vs Drain Current

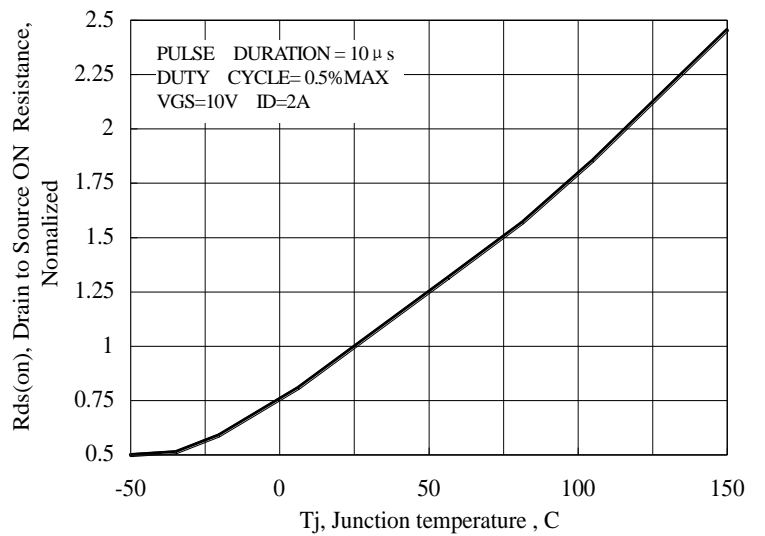


Figure 9 Typical Drain to Source on Resistance vs Junction Temperature

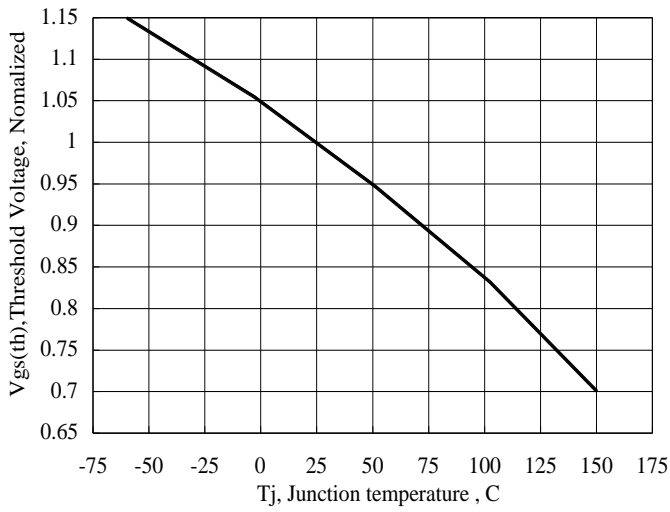


Figure 10 Typical Theshold Voltage vs Junction Temperature

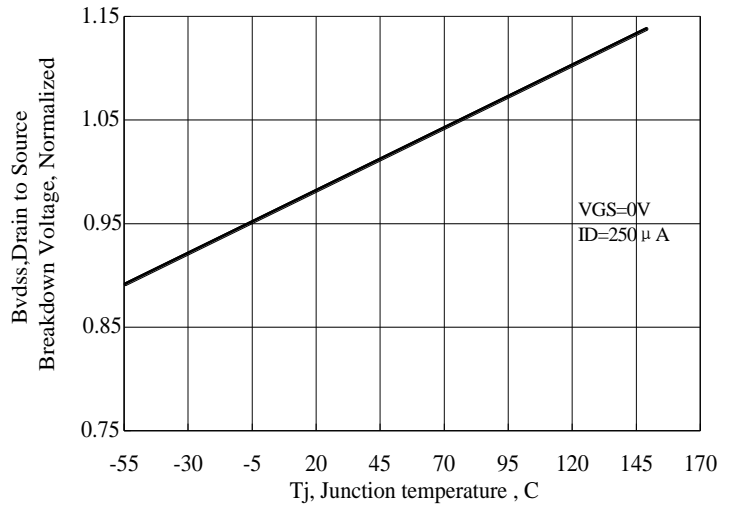


Figure 11 Typical Breakdown Voltage vs Junction Temperature

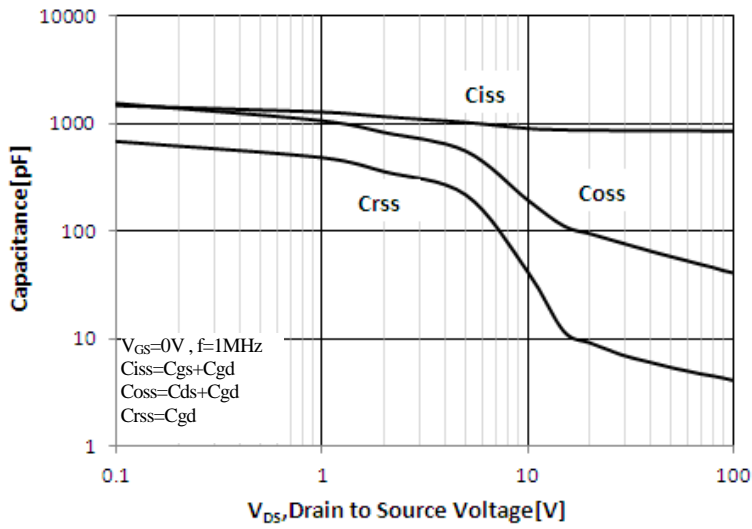


Figure 12 Typical Capacitance vs Drain to Source Voltage

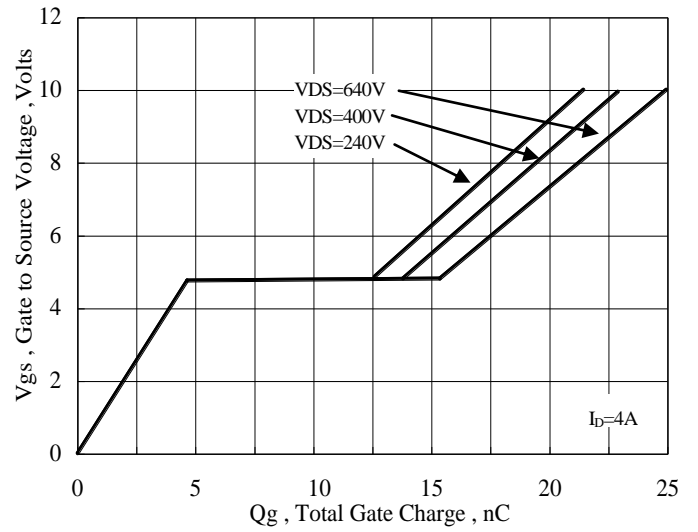


Figure 13 Typical Gate Charge vs Gate to Source Voltage

Test Circuit and Waveform:

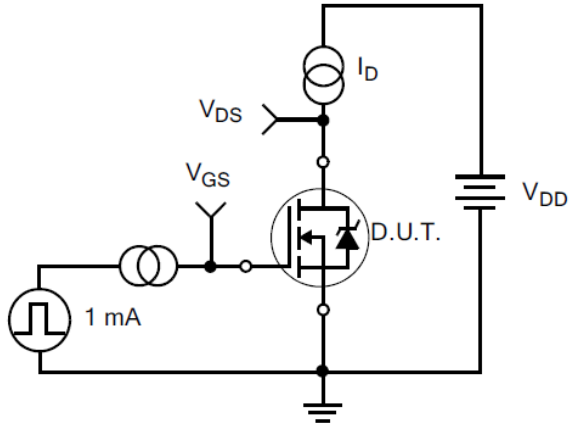


Figure 17. Gate Charge Test Circuit

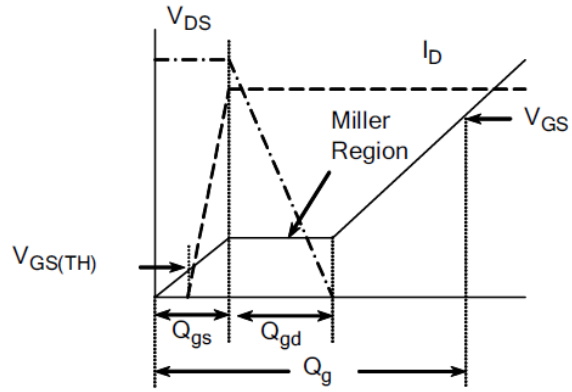


Figure 18. Gate Charge Waveform

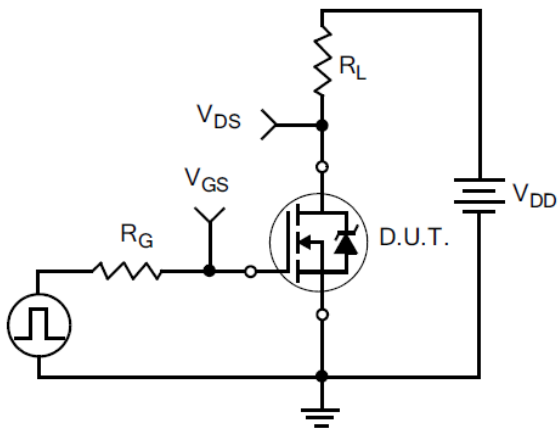


Figure 19. Resistive Switching Test Circuit

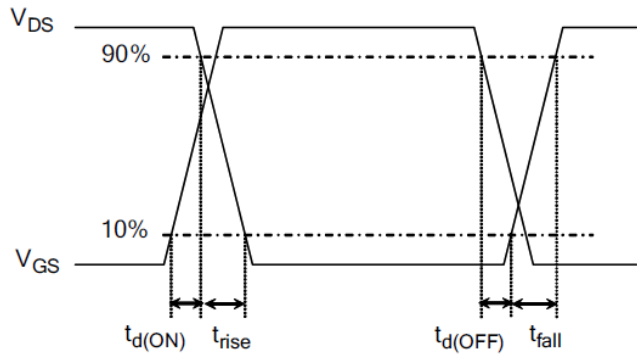


Figure 20. Resistive Switching Waveforms

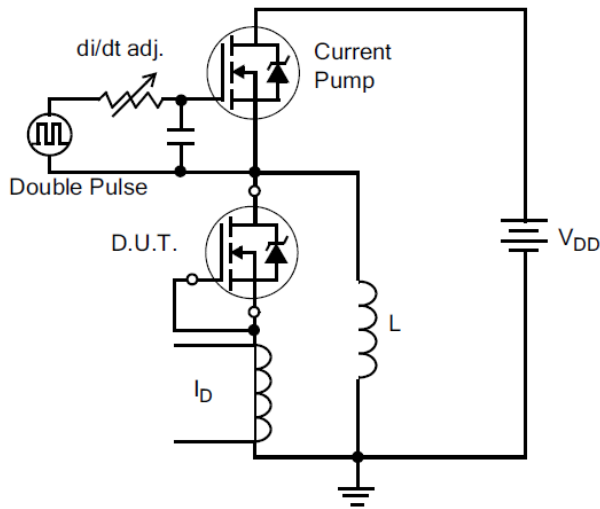


Figure 21. Diode Reverse Recovery Test Circuit

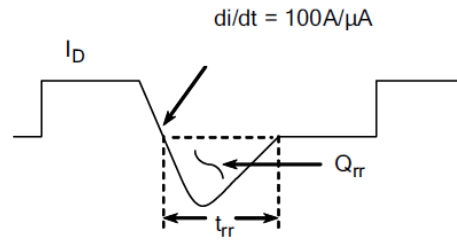


Figure 22. Diode Reverse Recovery Waveform

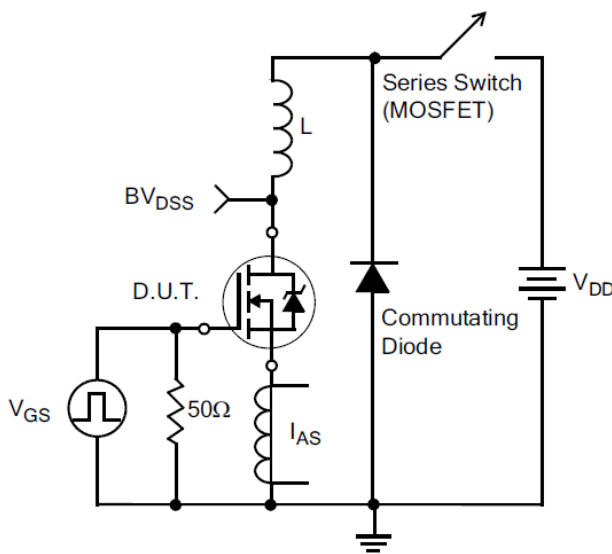


Figure 23. Unclamped Inductive Switching Test Circuit

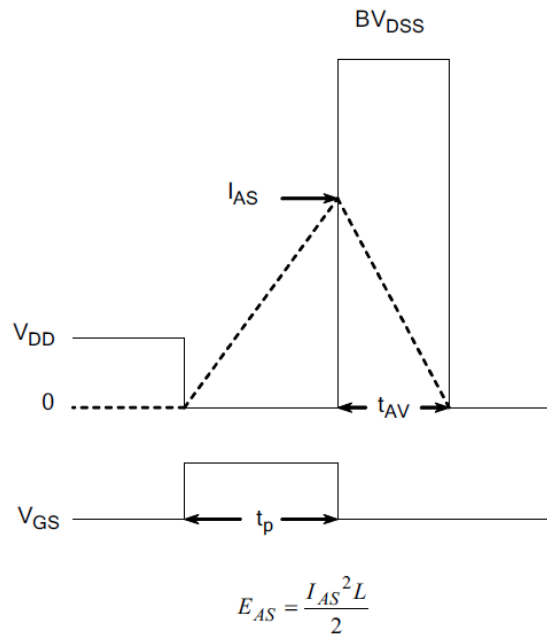
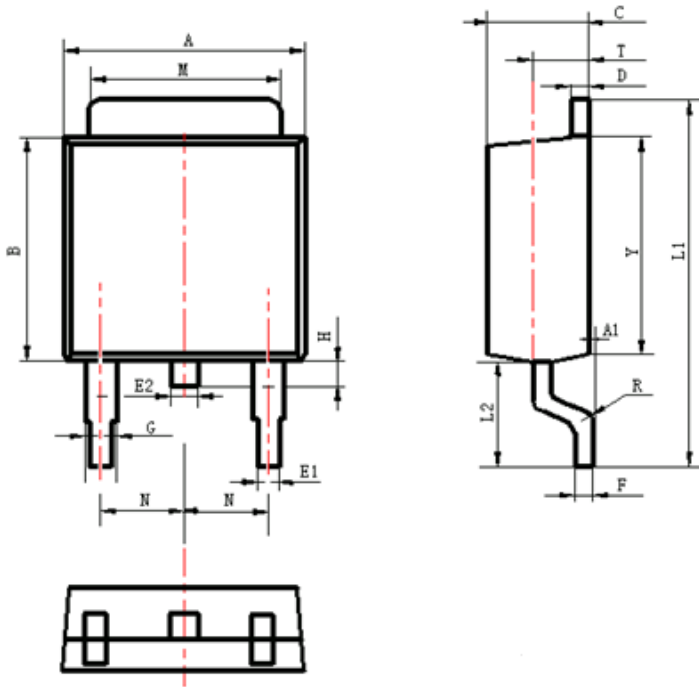


Figure 24. Unclamped Inductive Switching Waveforms

Package Information:



Items	Values(mm)	
	MIN	MAX
A	6.30	6.90
A1	0	0.13
B	5.70	6.30
C	2.10	2.50
D	0.30	0.70
E1	0.60	0.90
E2	0.70	1.00
F	0.30	0.60
G	0.70	1.20
L1	9.60	10.50
L2	2.70	3.10
H	0.60	1.00
M	5.10	5.50
N	2.09	2.49
R	0.3	
T	1.40	1.60
Y	5.10	6.30

TO-252 Package

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