

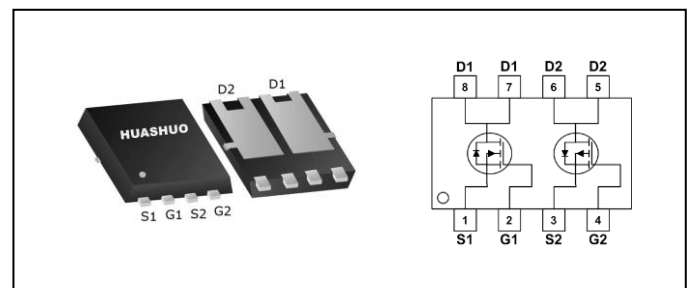
N-Ch and P-Ch Fast Switching MOSFETs
Application

- Power Management.
- DC Motor Control.

Product Summary

| BVDSS | RDSON | ID |
|--------------|--------------|-----------|
| 100V | 100mΩ | 6A |
| -100V | 220mΩ | -4.2A |

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

PRPAK3*3 Pin Configuration

Absolute Maximum Ratings

| Symbol | Parameter | Rating | | Units |
|------------------------|--|------------------|------------------|--------------|
| | | N-Channel | P-Channel | |
| V_{DS} | Drain-Source Voltage | 100 | -100 | V |
| V_{GS} | Gate-Source Voltage | ±20 | ±20 | V |
| $I_D@T_A=25^{\circ}C$ | Continuous Drain Current, $V_{GS} @ 10V^1$ | 2.6 | -1.8 | A |
| $I_D@T_A=100^{\circ}C$ | Continuous Drain Current, $V_{GS} @ 10V^1$ | 1.6 | -1.1 | A |
| $I_D@T_C=25^{\circ}C$ | Continuous Drain Current, $V_{GS} @ 10V^1$ | 6 | -4.2 | A |
| $I_D@T_C=100^{\circ}C$ | Continuous Drain Current, $V_{GS} @ 10V^1$ | 3.8 | -2.6 | A |
| I_{DM} | Pulsed Drain Current ² | 20 | -20 | A |
| EAS | Single Pulse Avalanche Energy ³ | 25 | 49 | mJ |
| I_{AS} | Avalanche Current | 10 | -14 | A |
| $P_D@T_C=25^{\circ}C$ | Total Power Dissipation ⁴ | 7.8 | 7.8 | W |
| T_{STG} | Storage Temperature Range | -55 to 150 | -55 to 150 | °C |
| T_J | Operating Junction Temperature Range | -55 to 150 | -55 to 150 | °C |

Thermal Data

| Symbol | Parameter | Typ. | Max. | Unit |
|-----------------|--|-------------|-------------|-------------|
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient ¹ | --- | 85 | °C/W |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case ¹ | --- | 16 | °C/W |

N-Channel Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|------------------------------|--|--|------|-------|-----------|----------------------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{GS}=0V, I_D=250\mu A$ | 100 | --- | --- | V |
| $\Delta BV_{DSS}/\Delta T_J$ | BV_{DSS} Temperature Coefficient | Reference to 25°C , $I_D=1\text{mA}$ | --- | 0.063 | --- | $V/^\circ\text{C}$ |
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance ² | $V_{GS}=10V, I_D=2.5A$ | --- | --- | 100 | m Ω |
| | | $V_{GS}=4.5V, I_D=2A$ | --- | --- | 125 | |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{GS}=V_{DS}, I_D=250\mu A$ | 1.2 | --- | 2.7 | V |
| $\Delta V_{GS(th)}$ | $V_{GS(th)}$ Temperature Coefficient | | --- | -5.24 | --- | $\text{mV}/^\circ\text{C}$ |
| I_{DSS} | Drain-Source Leakage Current | $V_{DS}=80V, V_{GS}=0V, T_J=25^\circ\text{C}$ | --- | --- | 1 | μA |
| | | $V_{DS}=80V, V_{GS}=0V, T_J=55^\circ\text{C}$ | --- | --- | 30 | |
| I_{GSS} | Gate-Source Leakage Current | $V_{GS}=\pm 20V, V_{DS}=0V$ | --- | --- | ± 100 | nA |
| R_g | Gate Resistance | $V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$ | --- | 3.5 | --- | Ω |
| Q_g | Total Gate Charge (4.5V) | $V_{DS}=50V, V_{GS}=4.5V, I_D=2A$ | --- | 15 | --- | nC |
| Q_{gs} | Gate-Source Charge | | --- | 3.2 | --- | |
| Q_{gd} | Gate-Drain Charge | | --- | 2.3 | --- | |
| $T_{d(on)}$ | Turn-On Delay Time | $V_{DD}=30V, V_{GS}=10V, R_G=3.3\Omega, I_D=1A$ | --- | 8 | --- | ns |
| T_r | Rise Time | | --- | 12 | --- | |
| $T_{d(off)}$ | Turn-Off Delay Time | | --- | 20 | --- | |
| T_f | Fall Time | | --- | 6 | --- | |
| C_{iss} | Input Capacitance | $V_{DS}=30V, V_{GS}=0V, f=1\text{MHz}$ | --- | 990 | --- | pF |
| C_{oss} | Output Capacitance | | --- | 36 | --- | |
| C_{rss} | Reverse Transfer Capacitance | | --- | 24 | --- | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|----------|--|---|------|------|------|------|
| I_S | Continuous Source Current ^{1,5} | $V_G=V_D=0V$, Force Current | --- | --- | 6 | A |
| V_{SD} | Diode Forward Voltage ² | $V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$ | --- | --- | 1.2 | V |

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.5\text{mH}, I_{AS}=10A$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

P-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------------|--|--|------|-------|------|-------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =-250uA | -100 | --- | --- | V |
| ΔBV _{DSS} /ΔT _J | BV _{DSS} Temperature Coefficient | Reference to 25°C, I _D =-1mA | --- | -0.03 | --- | V/°C |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =-10V, I _D =-2A | --- | --- | 220 | mΩ |
| | | V _{GS} =-4.5V, I _D =-1.6A | --- | --- | 255 | |
| V _{GS(th)} | Gate Threshold Voltage | V _{GS} =V _{DS} , I _D =-250uA | -1.2 | --- | -2.5 | V |
| ΔV _{GS(th)} | V _{GS(th)} Temperature Coefficient | | --- | 4.56 | --- | mV/°C |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =-80V, V _{GS} =0V, T _J =25°C | --- | --- | 1 | uA |
| | | V _{DS} =-80V, V _{GS} =0V, T _J =55°C | --- | --- | 30 | |
| I _{GSS} | Gate-Source Leakage Current | V _{GS} =±20V, V _{DS} =0V | --- | --- | ±100 | nA |
| R _g | Gate Resistance | V _{DS} =0V, V _{GS} =0V, f=1MHz | --- | 13 | --- | Ω |
| Q _g | Total Gate Charge (-10V) | V _{DS} =-50V, V _{GS} =-10V, I _D =-2A | --- | 19 | --- | nC |
| Q _{gs} | Gate-Source Charge | | --- | 3.1 | --- | |
| Q _{gd} | Gate-Drain Charge | | --- | 2.95 | --- | |
| T _{d(on)} | Turn-On Delay Time | V _{DD} =-30V, V _{GS} =-10V, R _G =3.3Ω, I _D =-1A | --- | 9 | --- | ns |
| T _r | Rise Time | | --- | 6 | --- | |
| T _{d(off)} | Turn-Off Delay Time | | --- | 38 | --- | |
| T _f | Fall Time | | --- | 33 | --- | |
| C _{iss} | Input Capacitance | V _{DS} =-30V, V _{GS} =0V, f=1MHz | --- | 1229 | --- | pF |
| C _{oss} | Output Capacitance | | --- | 41 | --- | |
| C _{rss} | Reverse Transfer Capacitance | | --- | 29 | --- | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|-----------------|--|--|------|------|------|------|
| I _S | Continuous Source Current ^{1,5} | V _G =V _D =0V, Force Current | --- | --- | -4.2 | A |
| V _{SD} | Diode Forward Voltage ² | V _{GS} =0V, I _S =-1A, T _J =25°C | --- | --- | -1.2 | V |

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=-25V,V_{GS}=-10V,L=0.5mH,I_{AS}=-14A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

N-Channel Typical Characteristics

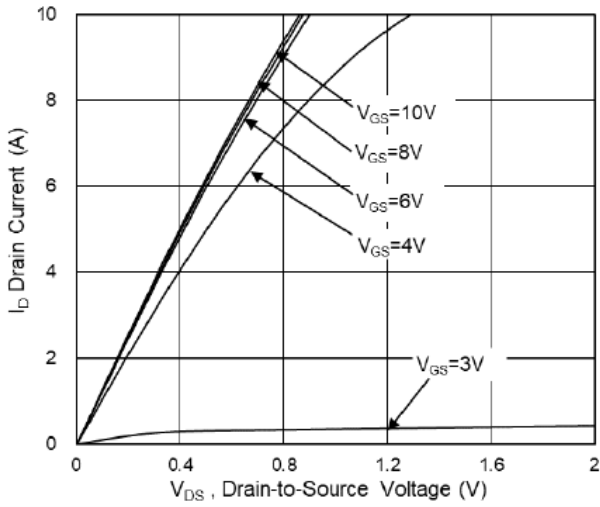


Fig.1 Typical Output Characteristics

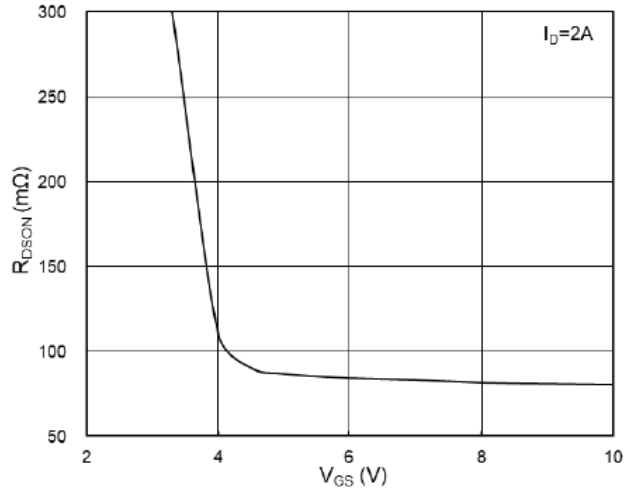


Fig.2 On-Resistance v.s Gate-Source

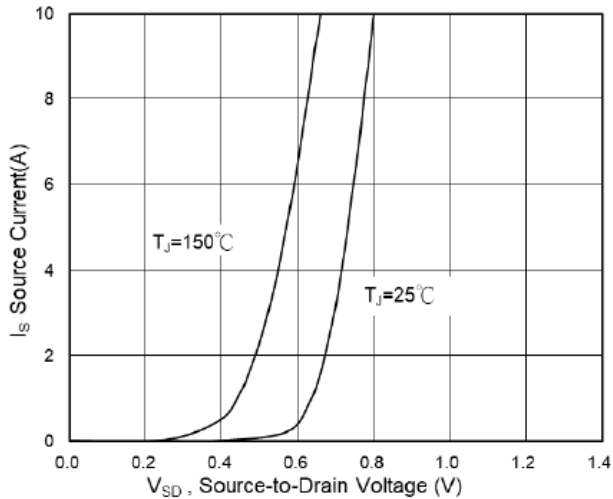


Fig.3 Forward Characteristics of Reverse

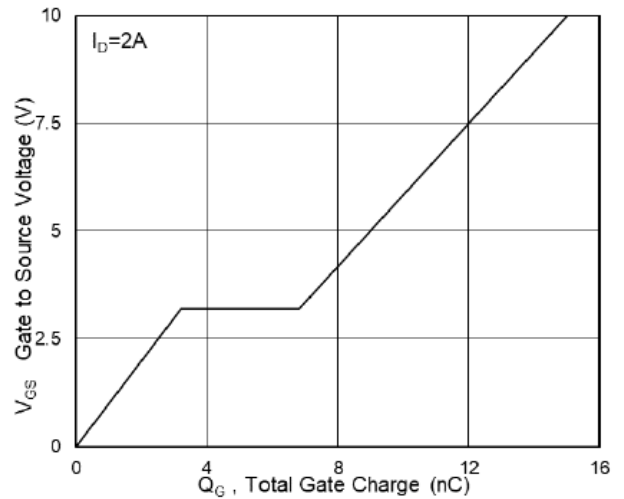


Fig.4 Gate-Charge Characteristics

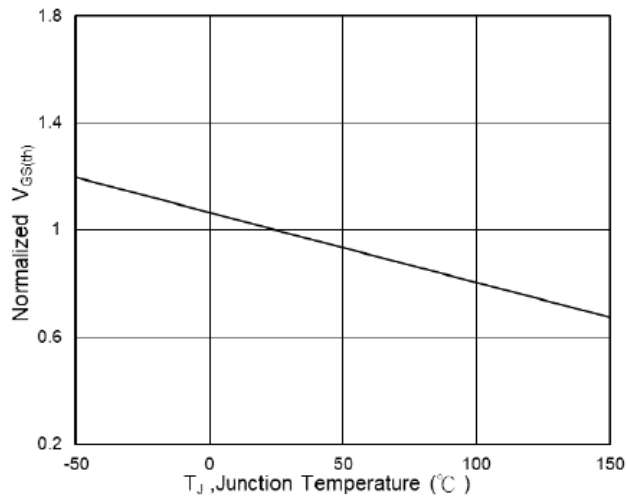


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

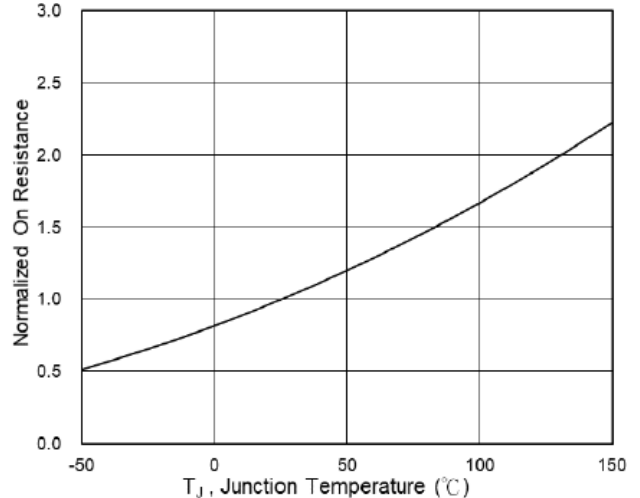


Fig.6 Normalized $R_{DS(on)}$ v.s T_J



N-Ch and P-Ch Fast Switching MOSFETs

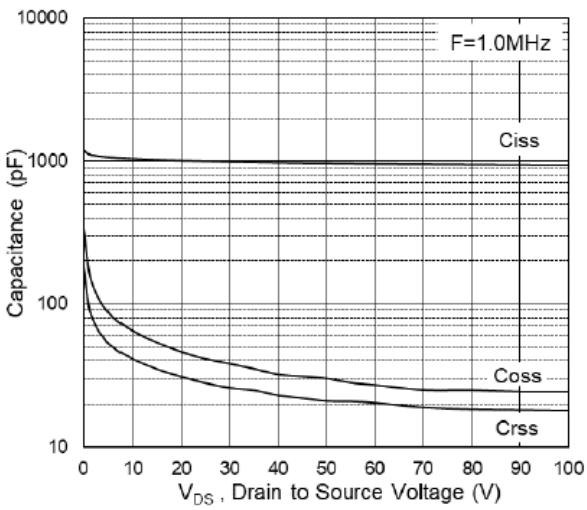


Fig.7 Capacitance

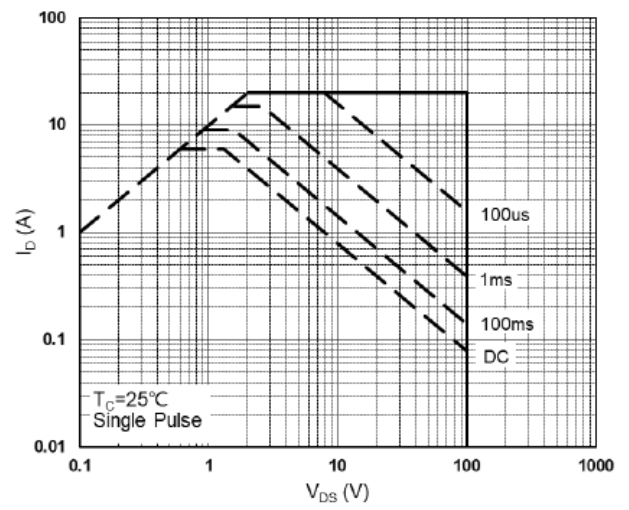


Fig.8 Safe Operating Area

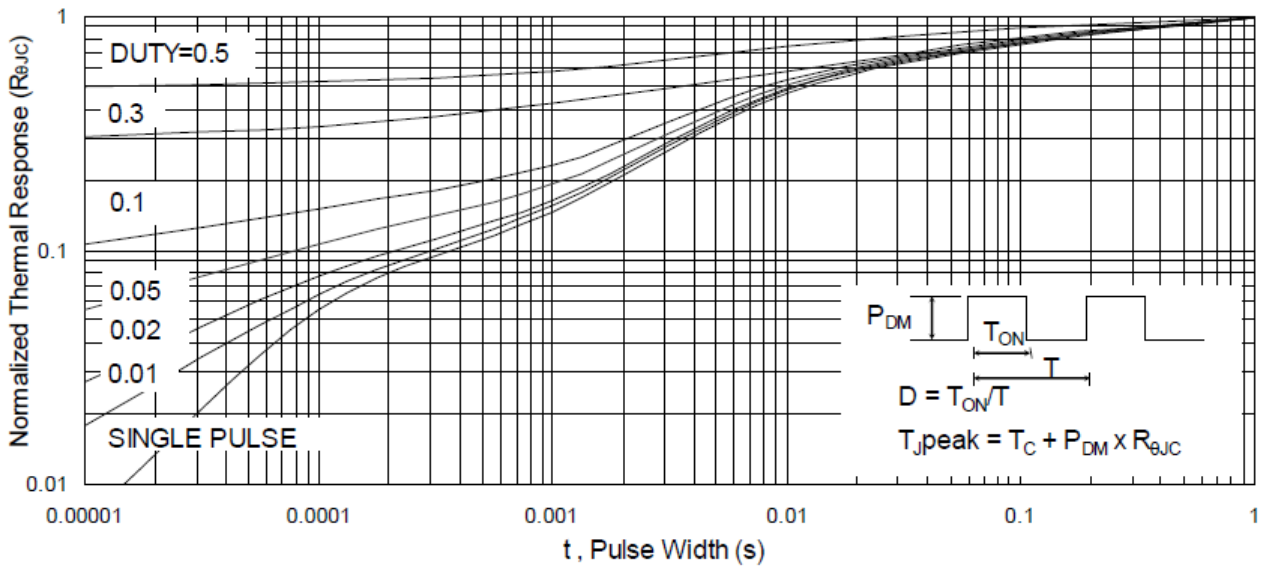


Fig.9 Normalized Maximum Transient Thermal Impedance

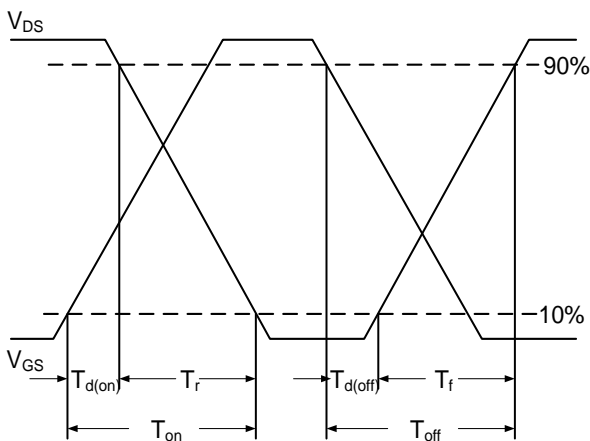


Fig.10 Switching Time Waveform

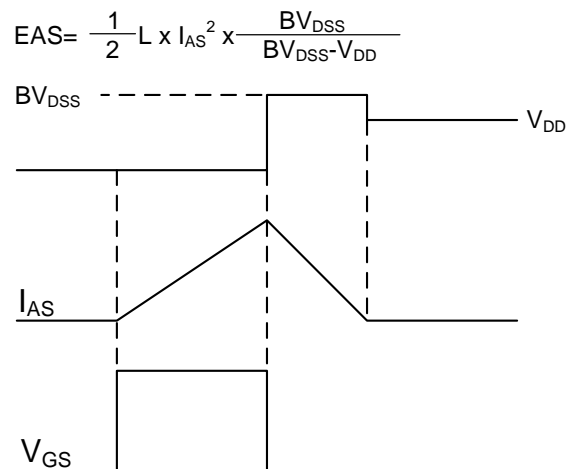


Fig.11 Unclamped Inductive Waveform

N-Ch and P-Ch Fast Switching MOSFETs

P-Channel Typical Characteristics

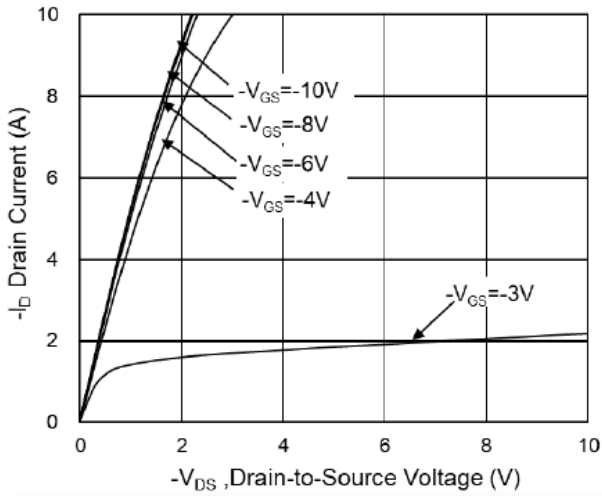


Fig.1 Typical Output Characteristics

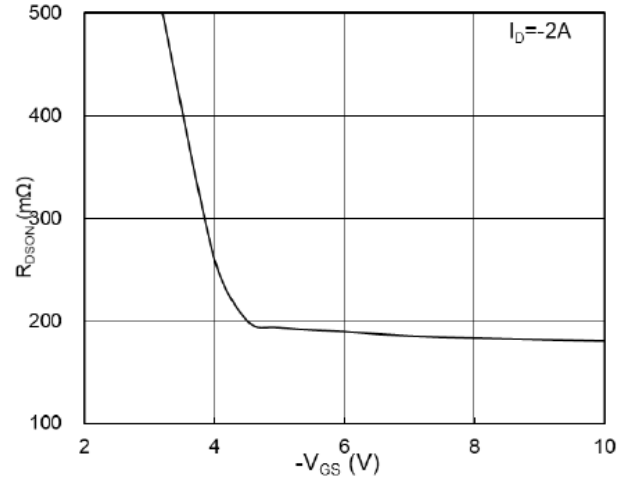


Fig.2 On-Resistance v.s Gate-Source

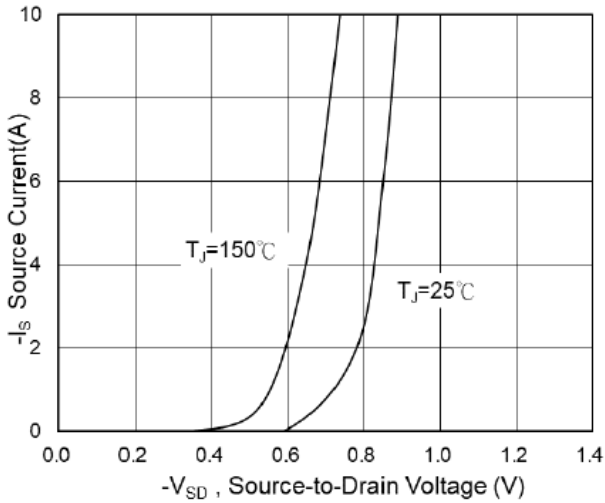


Fig.3 Forward Characteristics of Reverse

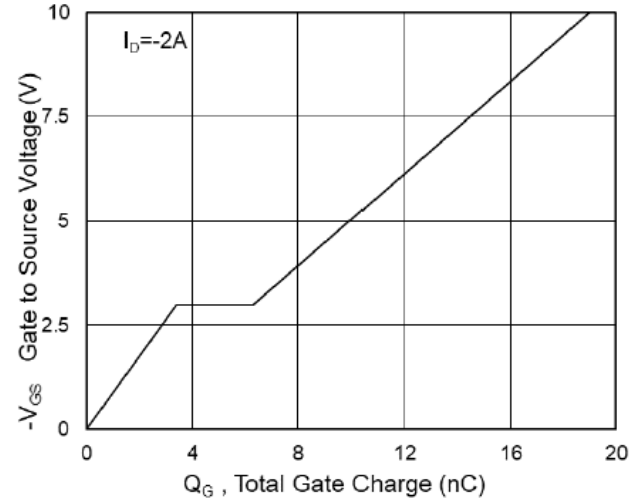


Fig.4 Gate-Charge Characteristics

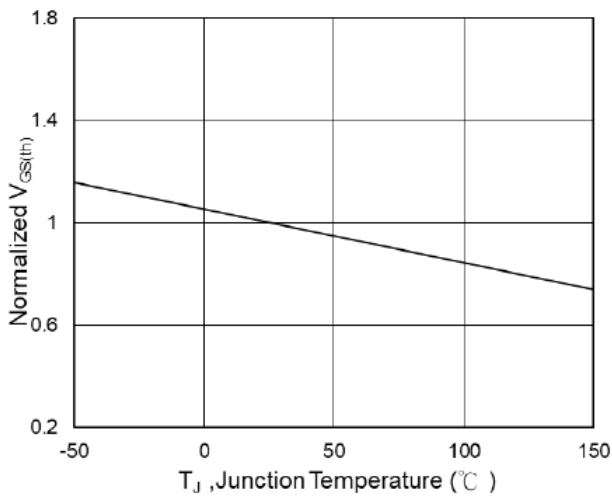


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

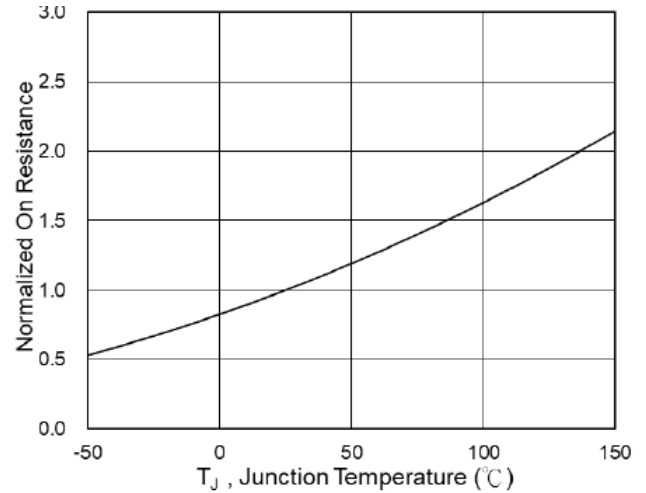


Fig.6 Normalized $R_{DS(on)}$ v.s T_J



N-Ch and P-Ch Fast Switching MOSFETs

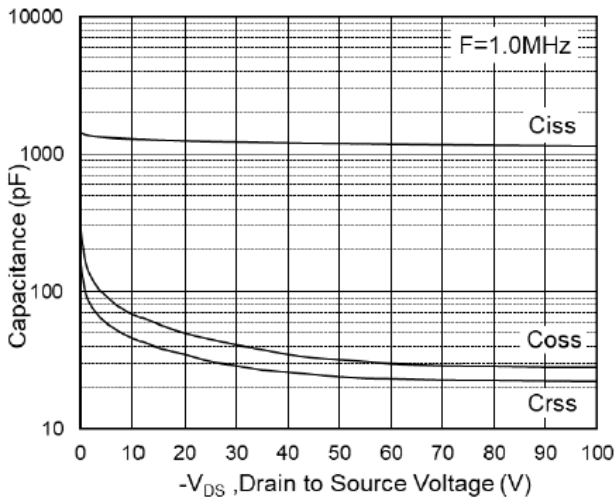


Fig.7 Capacitance

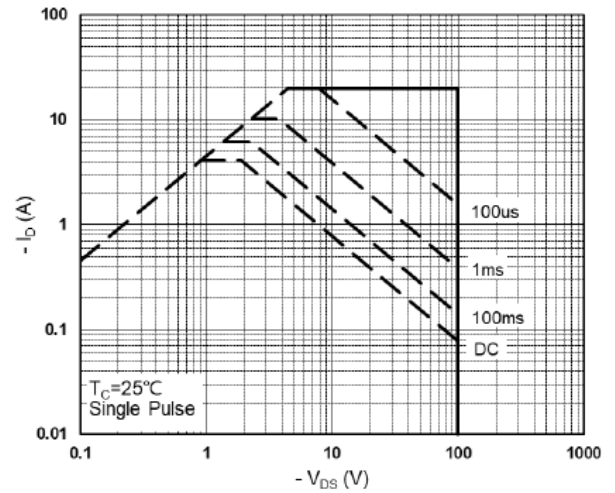


Fig.8 Safe Operating Area

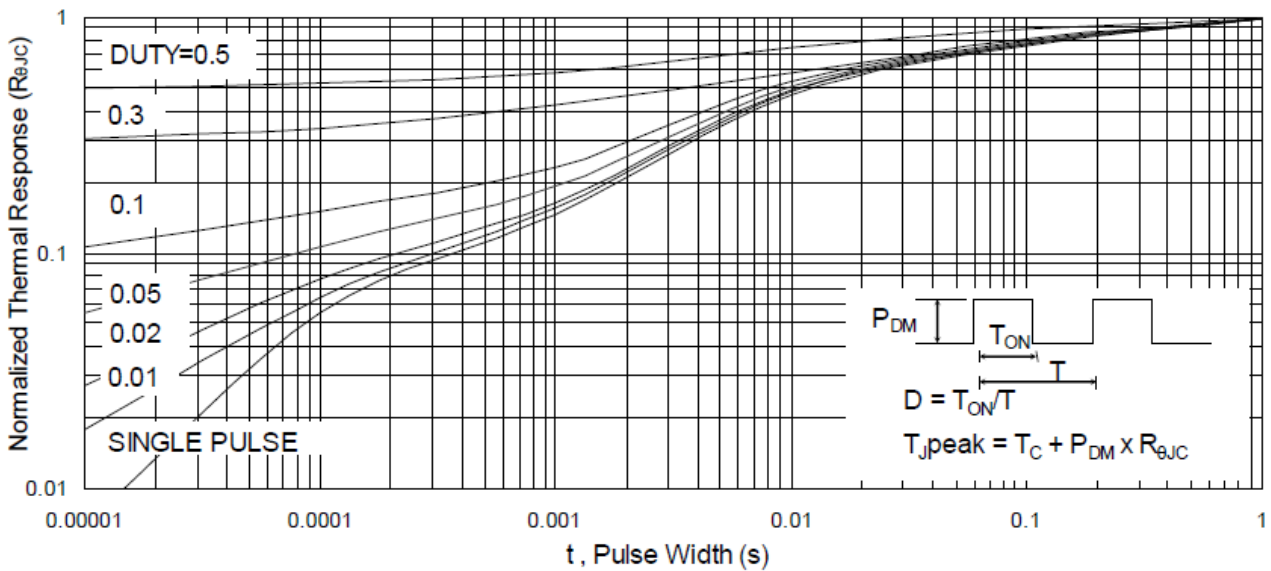


Fig.9 Normalized Maximum Transient Thermal Impedance

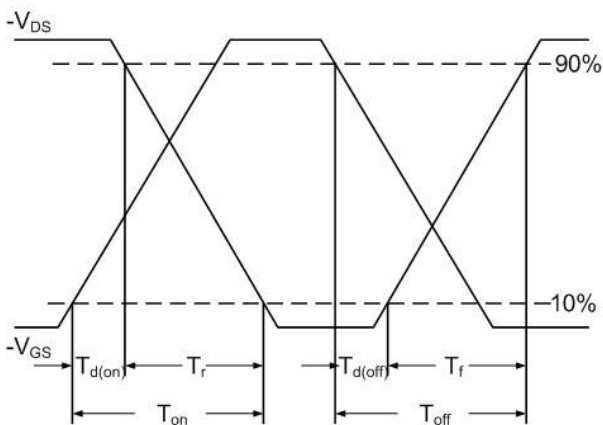


Fig.10 Switching Time Waveform

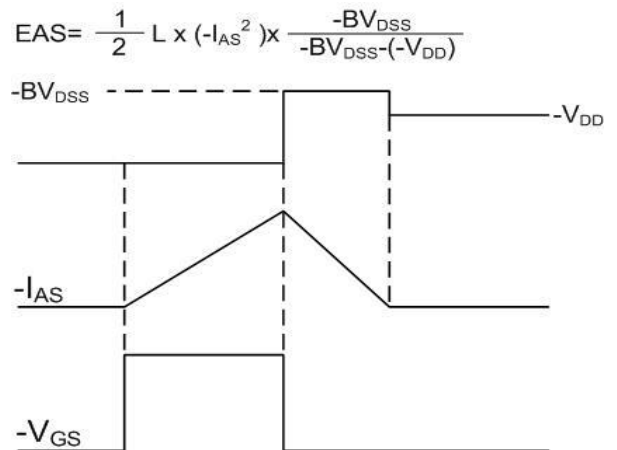


Fig.11 Unclamped Inductive Waveform

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