



## Description

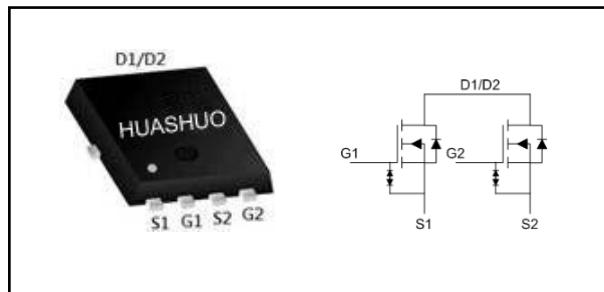
The HSBE2730 is the low RDSON trenched N-CH MOSFETs with robust ESD protection. This product is suitable for Lithium-ion battery pack applications.

The HSBE2730 meet the RoHS and Green Product requirement with full function reliability approved.

## Product Summary

$V_{DS}$	20	V
$R_{DS(ON),max}$	17	mΩ
$I_D$	7	A

## PRPAK3X3 NEP Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current <sup>1</sup>	7	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current <sup>1</sup>	5.8	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	43	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation <sup>3</sup>	1.47	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Max.	Unit
$R_{θJA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	85	°C/W



**N-Ch 20V Fast Switching MOSFETs**

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	20	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_{\text{D}}=1\text{mA}$	---	0.014	---	$\text{V}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=3\text{A}$	12	14.5	17	$\text{m}\Omega$
		$V_{\text{GS}}=4.0\text{V}$ , $I_{\text{D}}=3\text{A}$	12.5	15	18.5	$\text{m}\Omega$
		$V_{\text{GS}}=3.1\text{V}$ , $I_{\text{D}}=3\text{A}$	13	16.5	22	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$ , $I_{\text{D}}=3\text{A}$	14.5	18.5	24.5	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_{\text{D}}=250\mu\text{A}$	0.5	---	1.2	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	-2.09	---	$\text{mV}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	25	$\text{uA}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 8\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 10$	$\text{uA}$
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1.83	---	$\Omega$
$Q_g$	Total Gate Charge (4.5V)	$V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=3\text{A}$	---	9.86	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	1.41	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	2.48	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=10\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $R_g=3.3\Omega$ , $I_{\text{D}}=3\text{A}$	---	7	---	ns
$T_r$	Rise Time		---	36	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	46.5	---	
$T_f$	Fall Time		---	15	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	735	---	pF
$C_{\text{oss}}$	Output Capacitance		---	83	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	81	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Max.	Unit
$I_s$	Continuous Source Current <sup>1,6</sup>	$V_G=V_D=0\text{V}$ , Force Current	7	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=7\text{A}$ , $T_J=25^\circ\text{C}$	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature.
- 4.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.



### Typical Characteristics

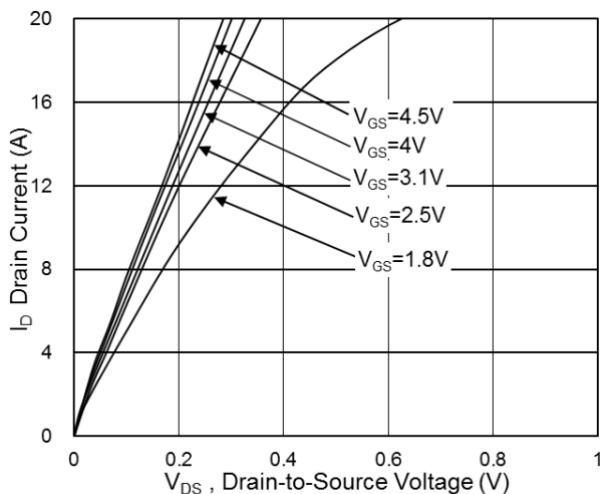


Fig.1 Typical Output Characteristics

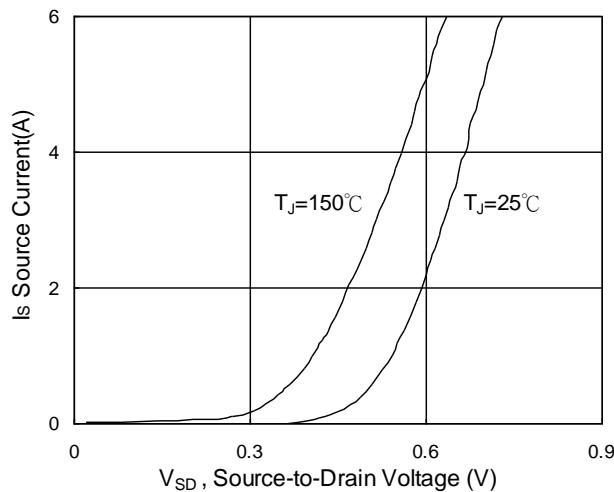


Fig.3 Forward Characteristics of Reverse

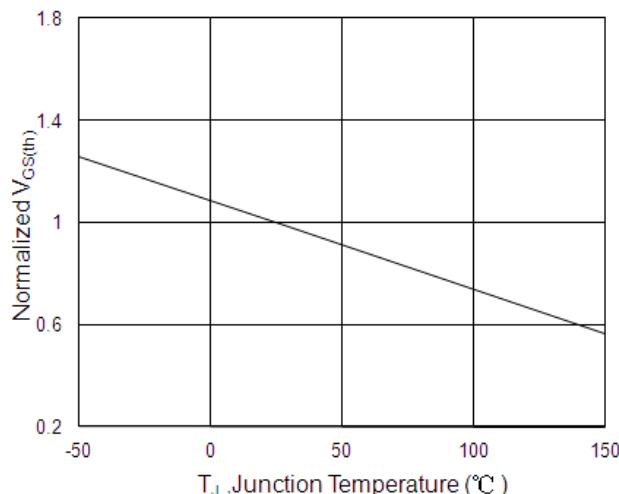


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

### N-Ch 20V Fast Switching MOSFETs

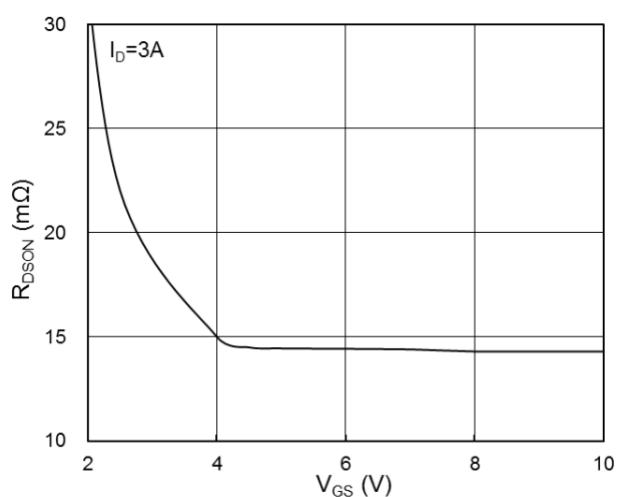


Fig.2 On-Resistance vs. Gate-Source Voltage

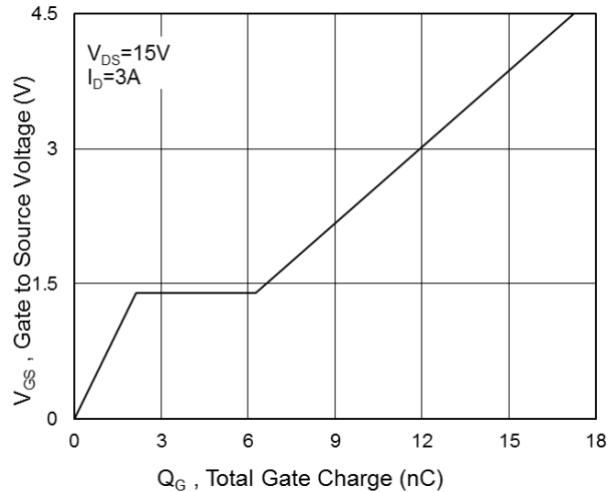


Fig.4 Gate-Charge Characteristics

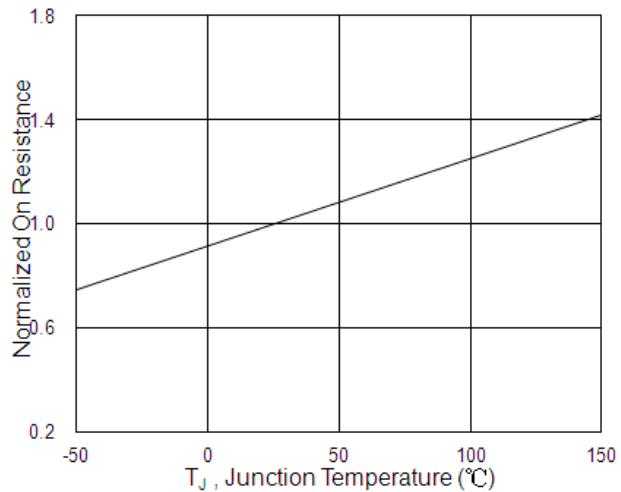


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$

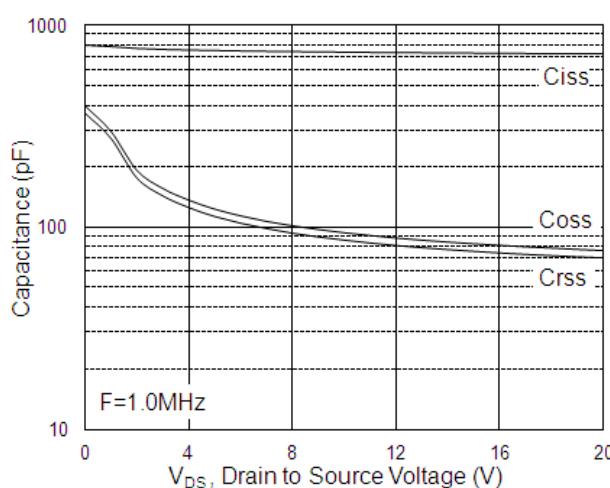


Fig.7 Capacitance

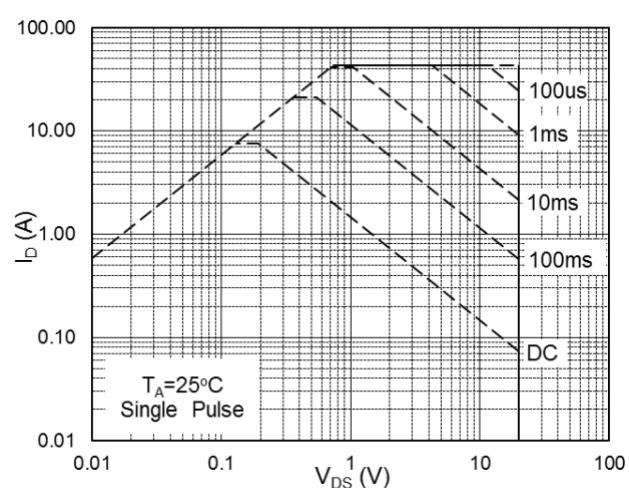


Fig.8 Safe Operating Area

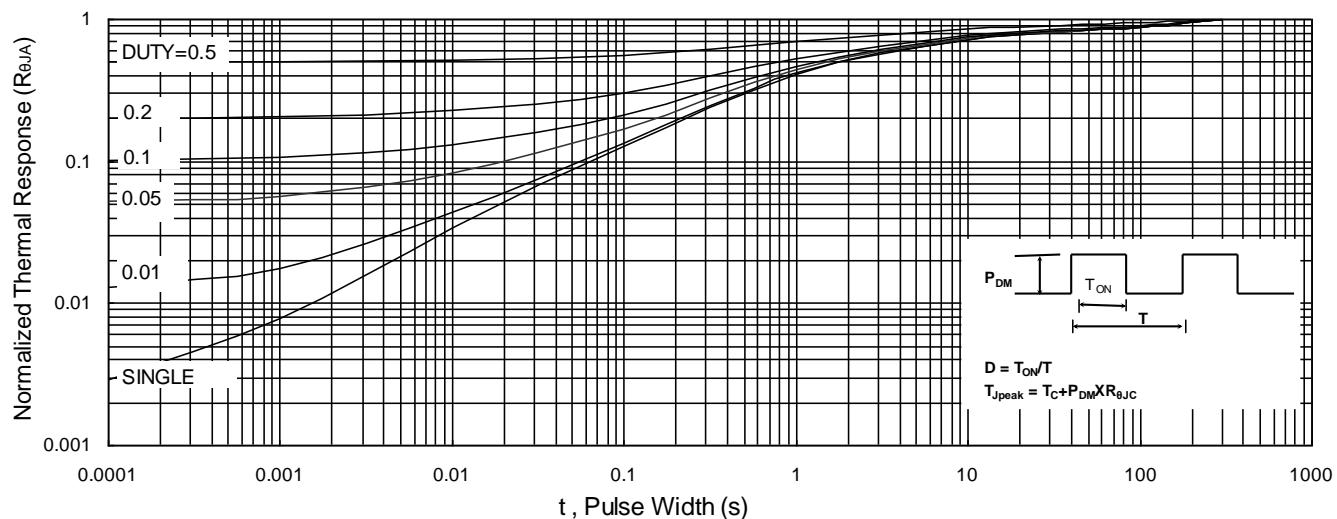


Fig.9 Normalized Maximum Transient Thermal Impedance

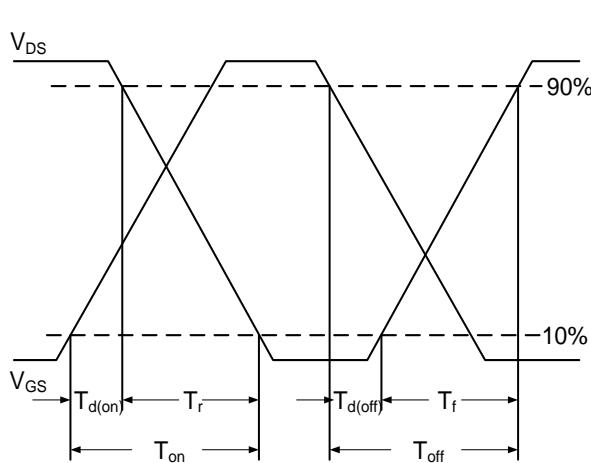


Fig.10 Switching Time Waveform

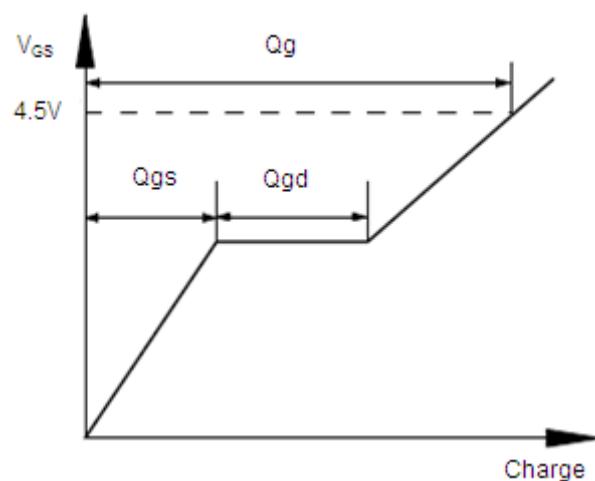
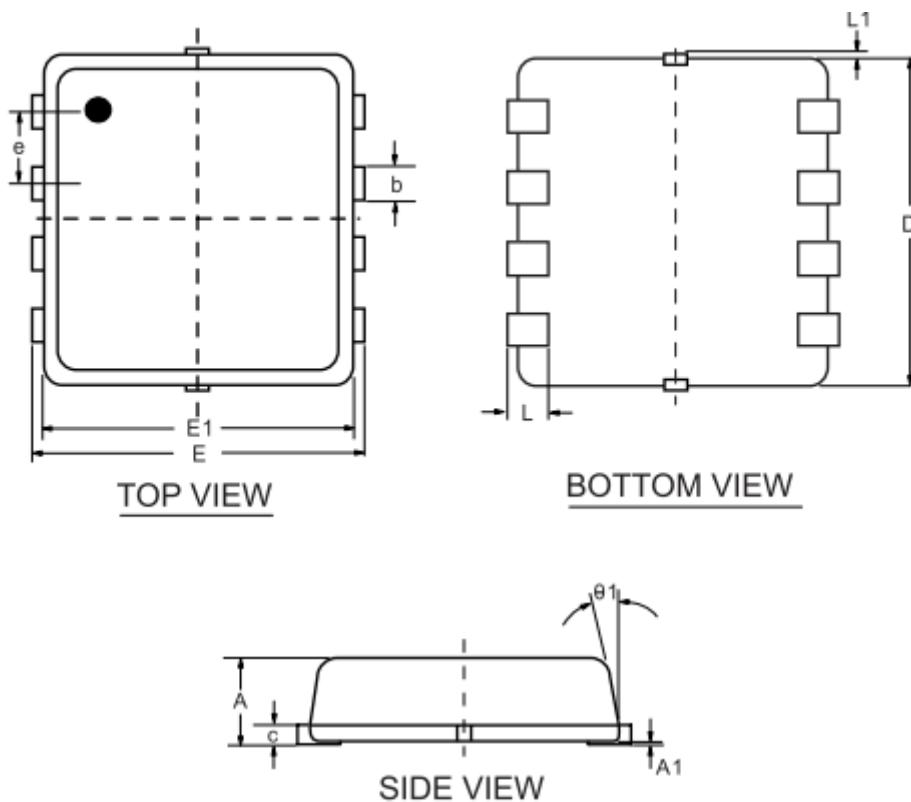


Fig.11 Gate Charge Waveform



N-Ch 20V Fast Switching MOSFETs

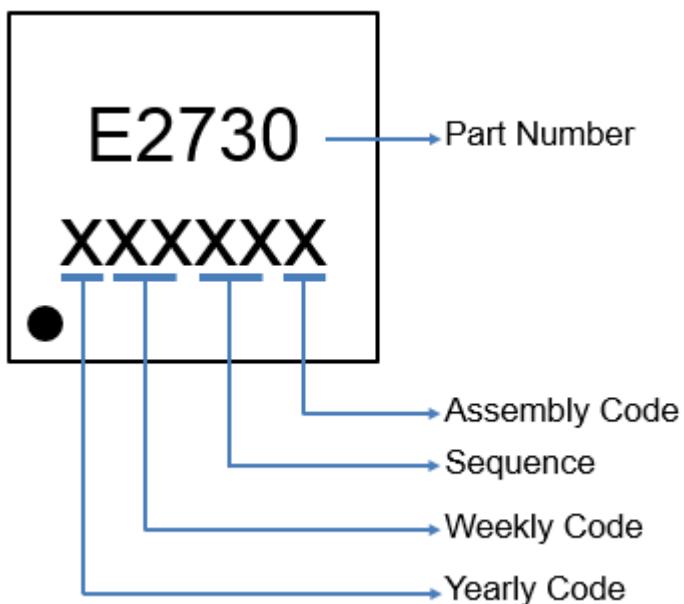
## PRPAK3X3 NEP Package Outline Dimensions



SYMBOLS	MILLIMETERS		
	MIN	NOM	MAX
A	0.700	0.800	0.900
A1	0.000	—	0.050
b	0.240	0.300	0.350
c	0.080	0.152	0.250
D	2.800	2.900	3.000
E	2.700	2.800	2.900
E1	2.200	2.300	2.400
e	0.650 BSC		
L	0.200	0.375	0.450
L1	0.000	—	0.100
θ1	0°	10°	12°



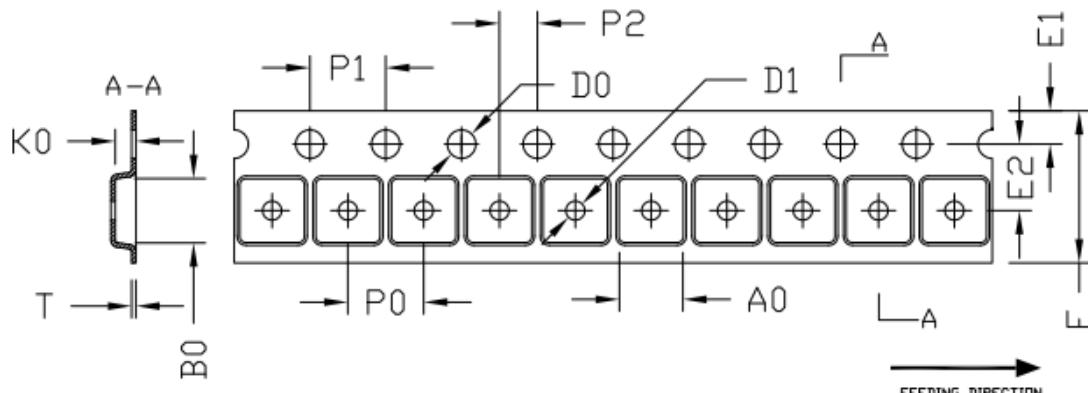
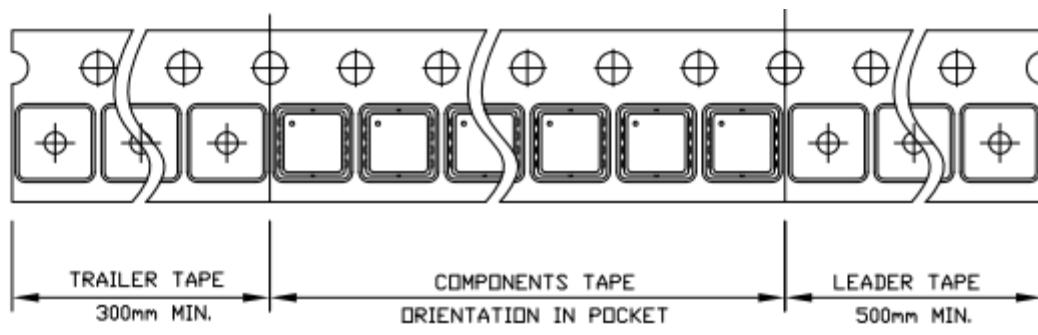
## Marking Instruction





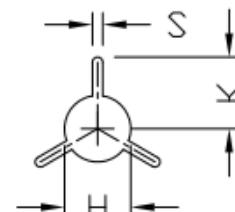
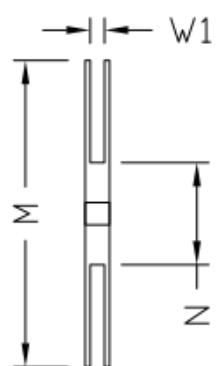
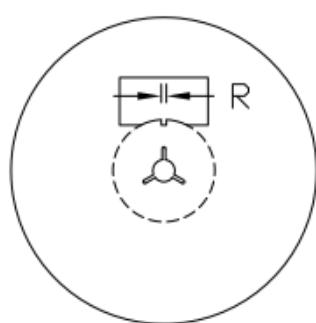
## PRPAK3X3 NEP Tape and Reel Data

Leader / Trailer  
& Orientation



UNIT: MM

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
DFN3x3	3.35	3.20	1.10	1.50	1.00	8.00	1.75	3.50	4.00	4.00	2.00	0.23
DFN3x2	$\pm 0.10$	$\pm 0.10$	$\pm 0.10$	$+0.10$ $-0.00$	$+0.25$ $-0.00$	$+0.30$ $-0.10$	$\pm 0.10$	$\pm 0.05$	$\pm 0.10$	$\pm 0.10$	$\pm 0.05$	$\pm 0.020$
DFN2x3												



UNIT: MM

TAPE SIZE	REEL SIZE	M	N	W1	H	S	K	R
8	$\varnothing 180$ $\pm 0.50$	$\varnothing 180.0$ $\pm 0.50$	60.0 $\pm 0.50$	$8.4$ $^{+1.5}_{-0}$	13.0 $\pm 0.20$	1.5 MIN.	13.5 MIN.	3.0 $\pm 0.50$

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