

Description

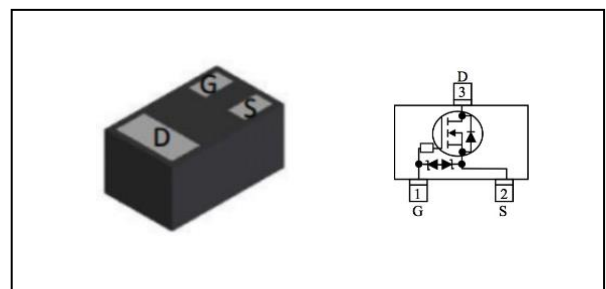
The HSBG2024 is the high cell density trenched N-ch MOSFETs, which provides excellent R_{DS(ON)} and efficiency for most of the small power switching and load switch applications. The HSBG2024 meets the RoHS and Green Product requirement with full function reliability approved.

- Small Single Switch
- Load Switch
- ESD Protected Gate
- Low Gate Threshold Voltage

Product Summary

V _{DS}	20	V
R _{DS(ON),typ}	190	mΩ
I _D	1.4	A

DFN1006 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	±8	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 4.5V ₁	1.4	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 4.5V ₁	1.11	A
I _{DM}	Pulsed Drain Current ₂	3.5	A
P _D @T _A =25°C	Total Power Dissipation ₃	0.7	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ₁	---	200	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =550mA	---	190	230	mΩ
		V _{GS} =2.5V, I _D =450mA	---	234	305	
		V _{GS} =1.8V, I _D =350mA	---	303	455	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	0.5	---	1	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =16V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±8V, V _{DS} =0V	---	---	±10	uA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =550mA	1	---	---	S
Q _g	Total Gate Charge (4.5V)	V _{DS} =10V, V _{GS} =4.5V, I _D =1000mA	---	1.2	---	nC
Q _{gs}	Gate-Source Charge		---	2.1	---	
Q _{gd}	Gate-Drain Charge		---	0.31	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =10V, V _{GS} =4.5V, R _G =6Ω, I _D =500mA	---	1.2	---	ns
T _r	Rise Time		---	24	---	
T _{d(off)}	Turn-Off Delay Time		---	15	---	
T _f	Fall Time		---	14	---	
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1MHz	---	44	---	pF
C _{oss}	Output Capacitance		---	9	---	
C _{rss}	Reverse Transfer Capacitance		---	5	---	

Diode Characteristics

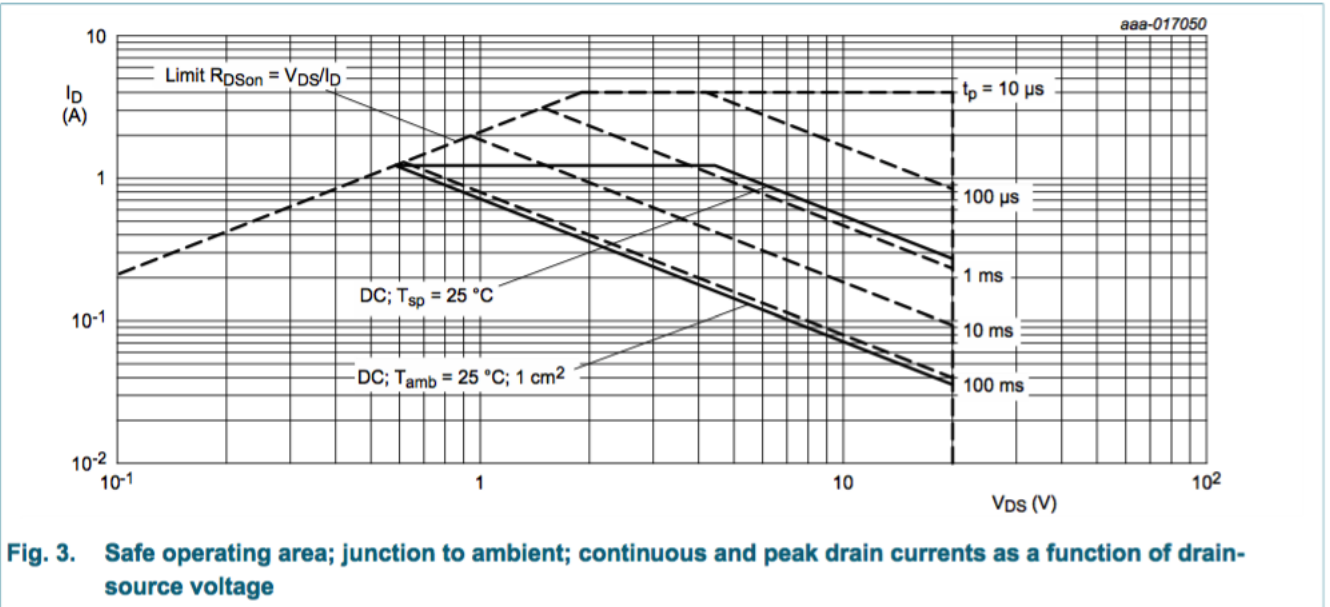
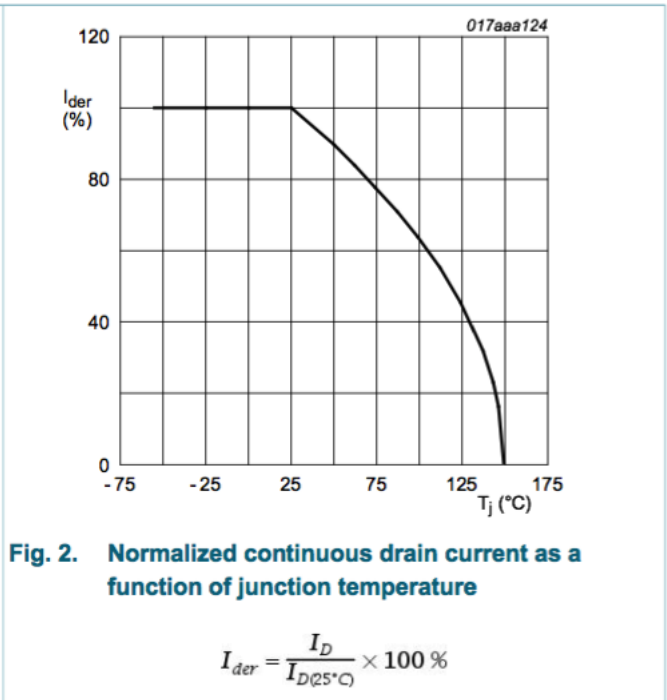
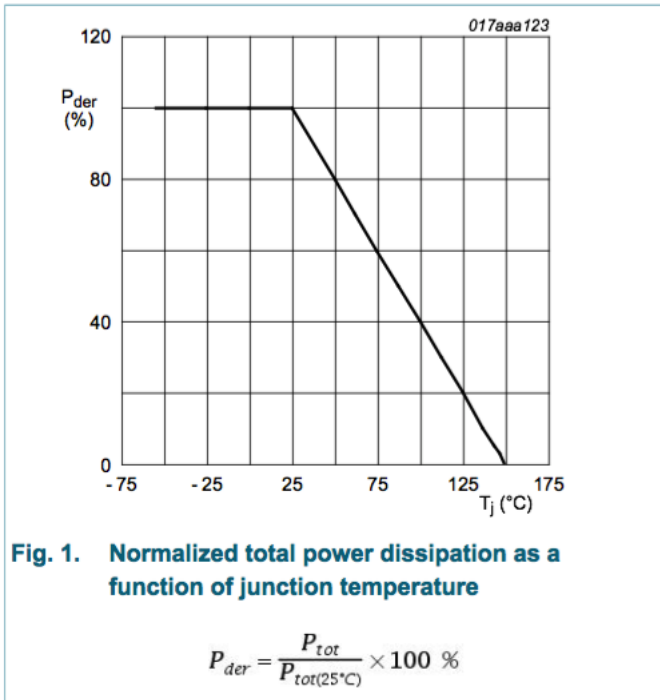
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	1.4	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.1	V

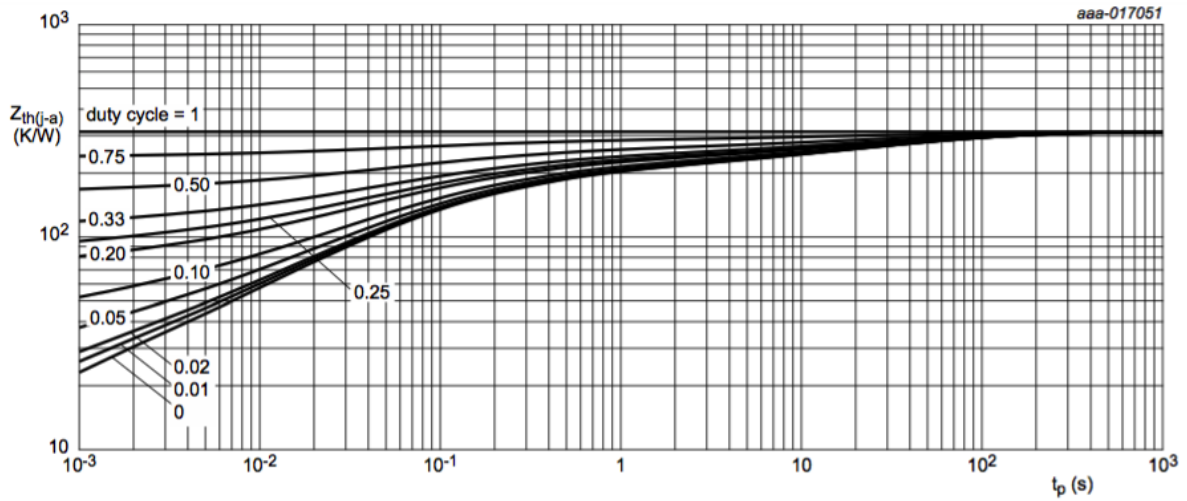
Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



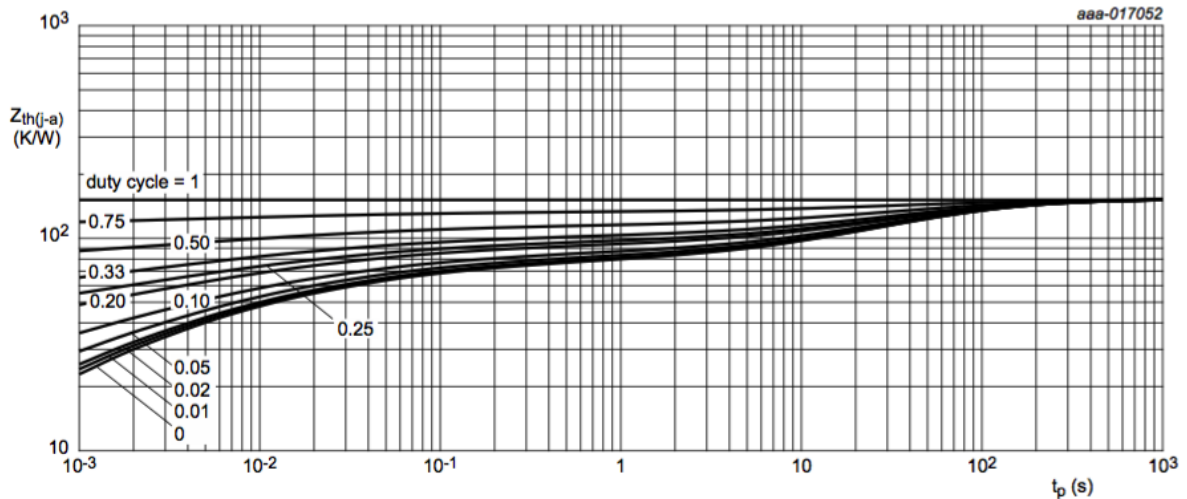
Typical Characteristics





FR4 PCB, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain = 1 cm²

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

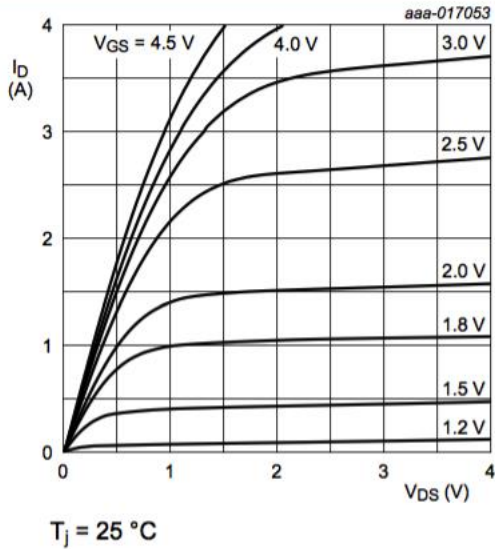


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

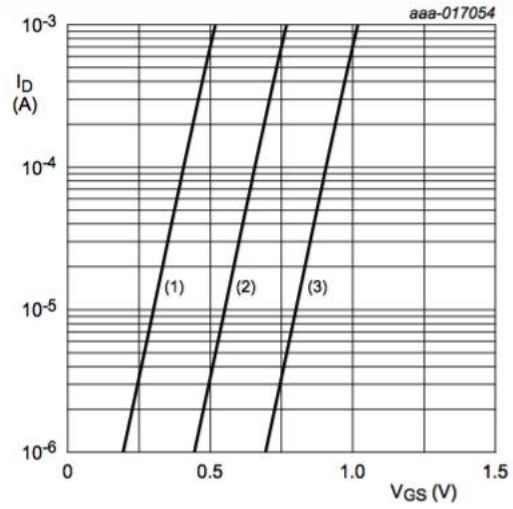


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

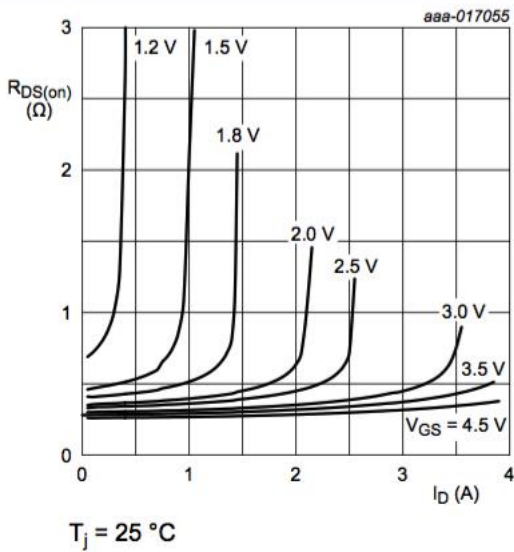


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

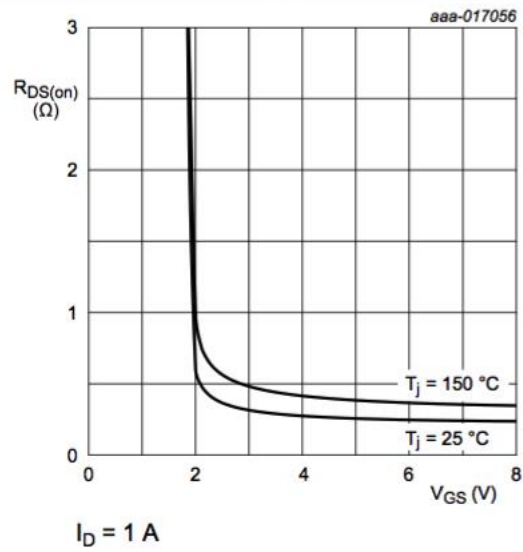


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

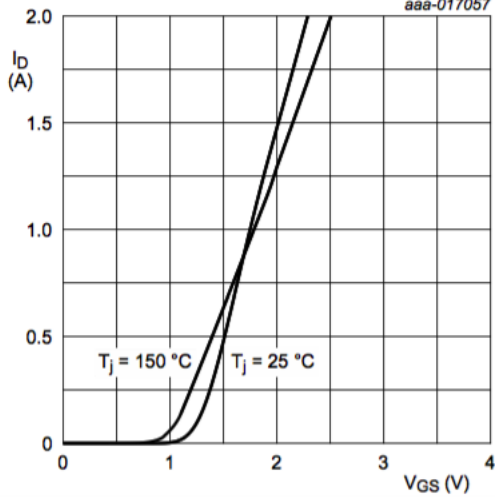


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

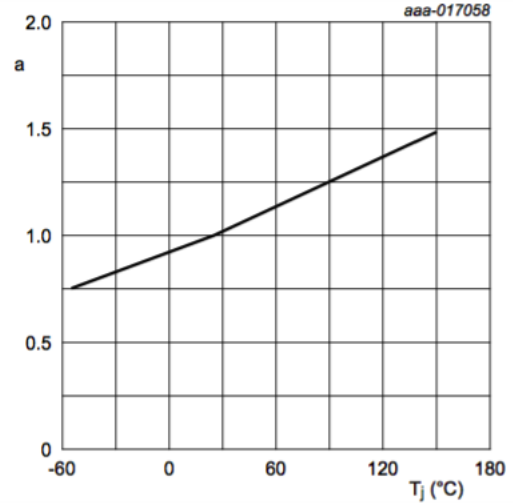
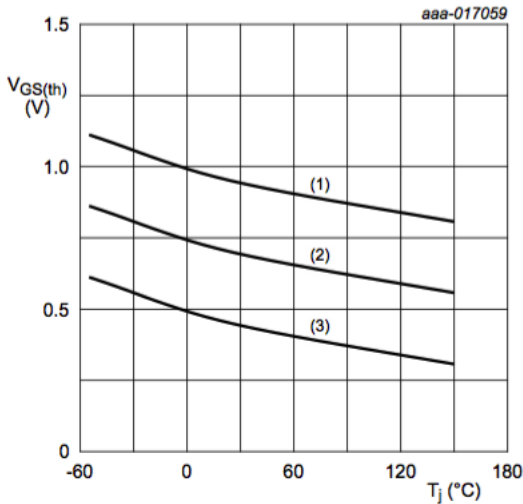


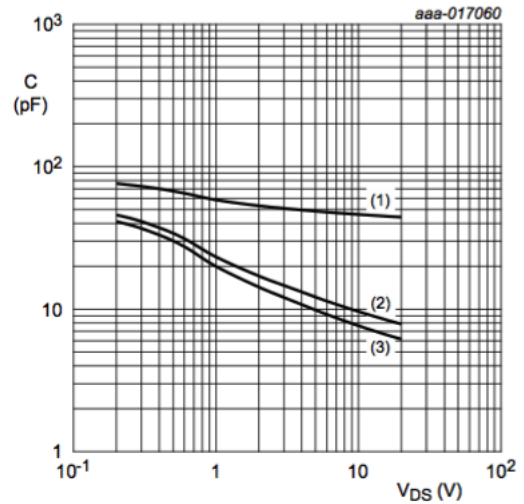
Fig. 11. Normalized drain-source on-state resistance as a function of ambient temperature; typical values

$$a = \frac{R_{DSon}}{R_{DSon,25^{\circ}C}}$$



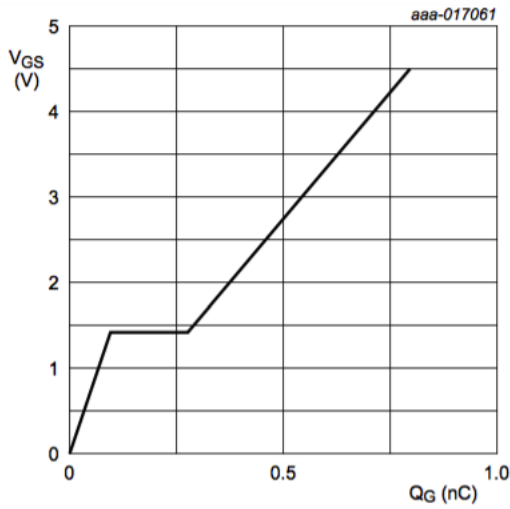
$I_D = 250 \mu A; V_{DS} = V_{GS}$
 (1) maximum values
 (2) typical values
 (3) minimum values

Fig. 12. Gate-source threshold voltage as a function of ambient temperature



$f = 1 \text{ MHz}; V_{GS} = 0 \text{ V}$
 (1) C_{iss}
 (2) C_{oss}
 (3) C_{rss}

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



$V_{DS} = 10\text{ V}; I_D = 0.5\text{ A}$
 $T_{amb} = 25\text{ }^\circ\text{C}$

Fig. 14. Gate-source voltage as a function of gate charge; typical values

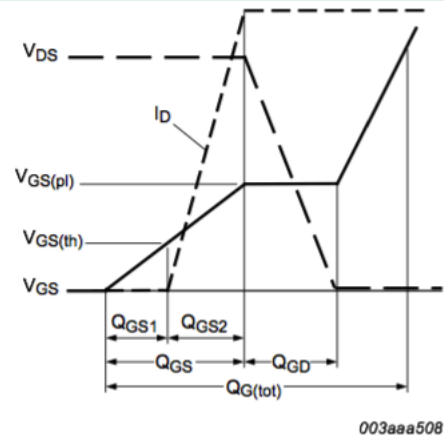
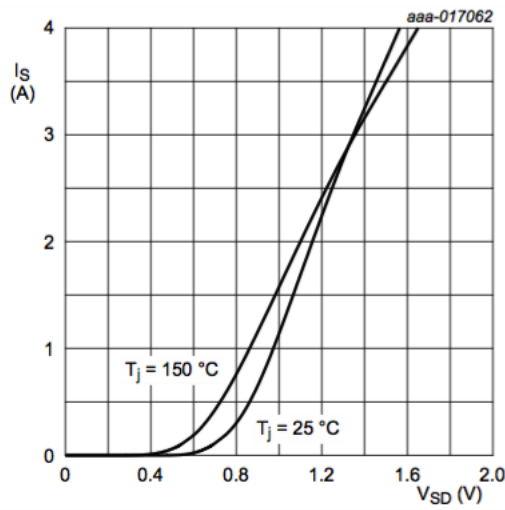


Fig. 15. Gate charge waveform definitions



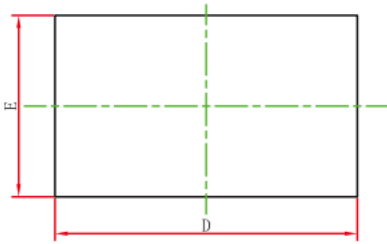
$V_{GS} = 0\text{ V}$

Fig. 16. Source current as a function of source-drain voltage; typical values

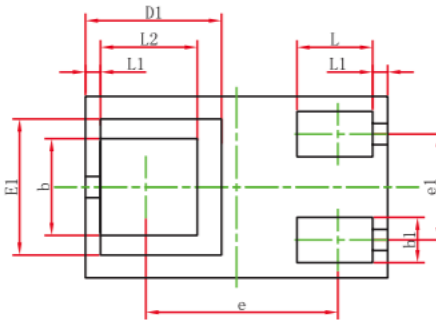


Ordering Information

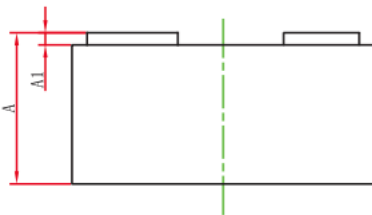
Part Number	Package code	Packaging
HSBG2024	DFN1006-3	10000/Tape&Reel



TOP VIEW



BOTTOM VIEW



SIDE VIEW

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.450	0.550	0.018	0.022
A1	0.010	0.100	0.000	0.004
D	0.950	1.050	0.037	0.041
E	0.550	0.650	0.022	0.026
D1	0.450REF.		0.018REF.	
E1	0.450REF.		0.018REF.	
b	0.270	0.370	0.011	0.015
b1	0.100	0.200	0.004	0.008
e	0.635REF.		0.025REF.	
e1	0.300	0.400	0.012	0.016
L	0.200	0.300	0.008	0.012
L1	0.050REF.		0.002REF.	
L2	0.270	0.370	0.011	0.015

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