



## 650V 4A N-Channel Planar MOSFET

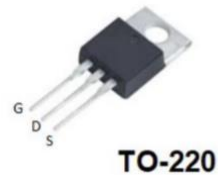
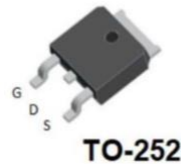
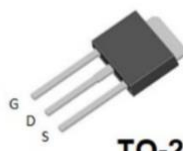
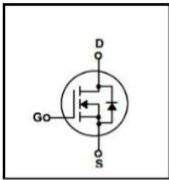
### General Description

HS4N65 is Fortunatus high voltage MOSFET family based on advanced planar stripe DMOS technology. This advanced MOSFET family has optimized on-state resistance, and also provides superior switching performance and higher avalanche energy strength. This device family is suitable for high efficiency switch mode power supplies.

### FEATURES

- $R_{DS(on)} \leq 3.0\Omega$  @  $V_{GS}=10V, I_D=2A$
- Ultra Low gate Charge (typical 13 nC)
- Low Crss (typical 5pF)
- Fast switching capability
- 100% avalanche tested
- Improved dv/dt capability

### SYMBOL



### ASSEMBLY MESSAGE

Product Name	Marking	Package	Halogen Free	Packaging
HSD4N65	4N65UF	TO-251	YES	Tube
HSU4N65	4N65DF	TO-252	YES	Tube/Reel
HSP4N65	4N65CF	TO-220	YES	Tube
HSF4N65	4N65FF	TO-220F	YES	Tube

### ABSOLUTE MAXIMUM RATINGS (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Rating			Unit
		HSD/U4N65	HSP4N65	HSF4N65	
Drain-Source Voltage	V <sub>DSS</sub>	650			V
Drain Current	Continuous (T <sub>c</sub> = 25°C)	4			A
	Continuous (T <sub>c</sub> = 100°C)	2.7			A
Drain Current	Pulsed (Note1)	16			A
Gate-Source Voltage	V <sub>GSS</sub>	±30			V
Avalanche Energy	Single Pulse (Note2)	232			mJ
	Repetitive (None1)	15			mJ
Avalanche Current (None1)	I <sub>AR</sub>	4			A
Peak Diode Recovery dv/dt (Note3)	dv/dt	5			V/ns
Power Dissipation (Note 2)	T <sub>c</sub> = 25°C	80	102	38	W
	Derate above 25°C	0.64	0.82	0.3	W/°C
Maximum Junction Temperature	T <sub>J</sub>	150			°C
Storage Temperature Range	T <sub>STG</sub>	-55 to 150			°C

- Note:**
1. Repetitive Rating: Pulse width limited by maximum junction temperature
  2. L=29mH, I<sub>AS</sub>=4.0A, V<sub>DD</sub>=50V, R<sub>G</sub>=25 Ω, Starting T<sub>J</sub> = 25°C
  3. I<sub>SD</sub> ≤ 4.0A, di/dt ≤ 300A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25°C



**THERMAL CHARACTERISTICS**

Parameter	Symbol	Max.			Unit
		HSD/U4N65	HSP4N65	HSF4N65	
Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	1.56	1.23	3.29	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	110	62	120	°C/W

**ELECTRICAL CHARACTERISTICS** (T = 25<sub>J</sub> °C, unless otherwise Noted)

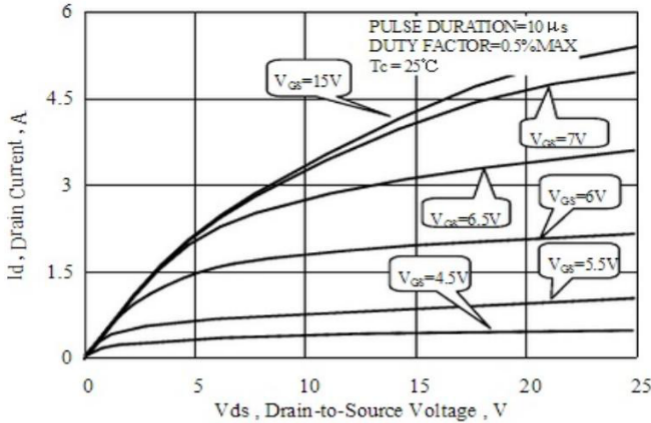
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	VGS=0V, ID=250μA	650			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	VDS=650V, VGS=0V			1	uA
		VDS=520V, TC = 125°C			100	uA
Gate-Body Leakage Current, Forward	I <sub>GSS</sub>	VGS=30V			100	nA
Gate-Body Leakage Current, Reverse		VGS=-30V			-100	nA
Breakdown Voltage Temperature Coefficient	$\frac{\Delta BV_{DSS}}{\Delta T_J}$	ID = 250 μA		0.62		V/°C
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	VGS(TH)	VDS=VGS, ID=250μA	2		4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	VGS=10V, ID=2A		2.6	3.0	Ω
Forward Transconductance (Note4)	g <sub>FS</sub>	VDS = 50V, ID = 2A		2.5		S
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	VDS=25V, VGS=0V, f=1.0MHz		545		pF
Output Capacitance	C <sub>OSS</sub>			54		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			5		pF
<b>SWITCHING PARAMETERS</b>						
Turn-ON Delay Time	t <sub>D(ON)</sub>	VDD=325V, ID=4 A, VGS = 10V ,RG=10Ω (Note4,5)		11		ns
Turn-ON Rise Time	t <sub>r</sub>			25		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			32.5		ns
Turn-OFF Fall-Time	t <sub>f</sub>			7		ns
Total Gate Charge(Note)	Q <sub>G</sub>	VDS =520V, VGS =10V, ID =4A (Note4,5)		13		nC
Gate Source Charge	Q <sub>GS</sub>			3.4		nC
Gate Drain Charge	Q <sub>GD</sub>			7		nC
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	IS=4A, VGS=0V			1.4	V
Diode Continuous Forward Current	I <sub>S</sub>				4	A
Pulsed Drain-Source Current	I <sub>SM</sub>				16	A
Reverse Recovery Time	t <sub>RR</sub>	VGS=0V, ISD=4A		510		ns
Reverse Recovery Charge	Q <sub>RR</sub>	di/dt=100 A/μs (Note4,5)		2.5		uC

Note: 4. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%

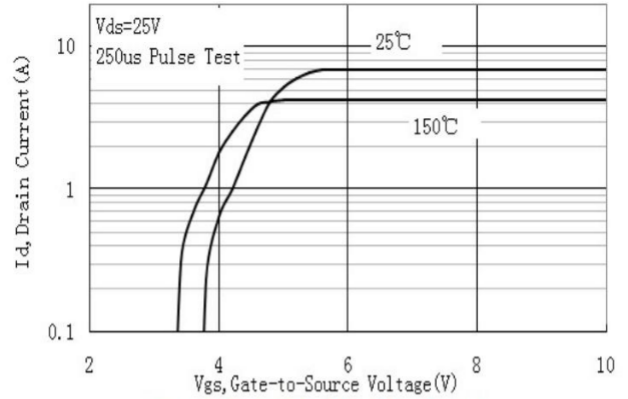
5. Essentially independent of operating temperature



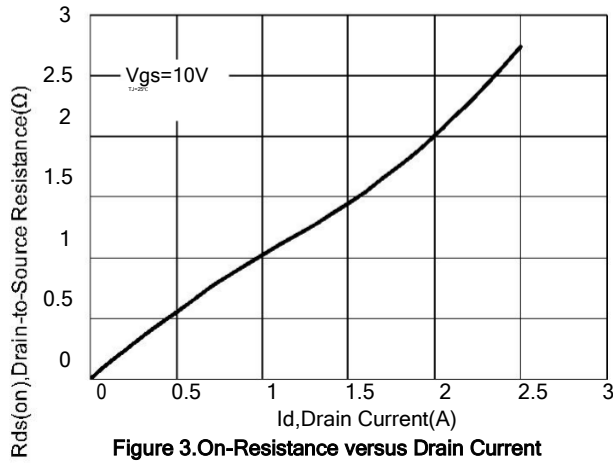
**TYPICAL CHARACTERISTICS**



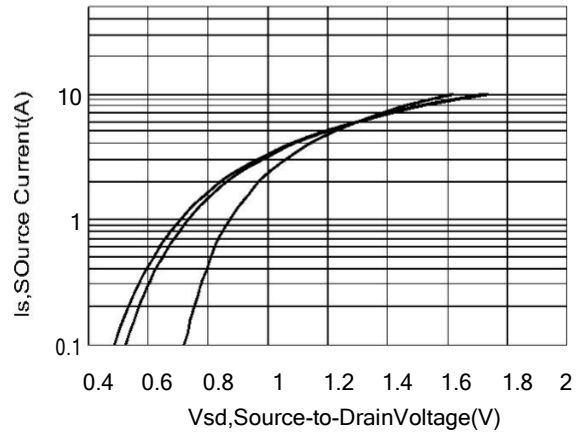
**Figure 1. Typical Output Characteristics**



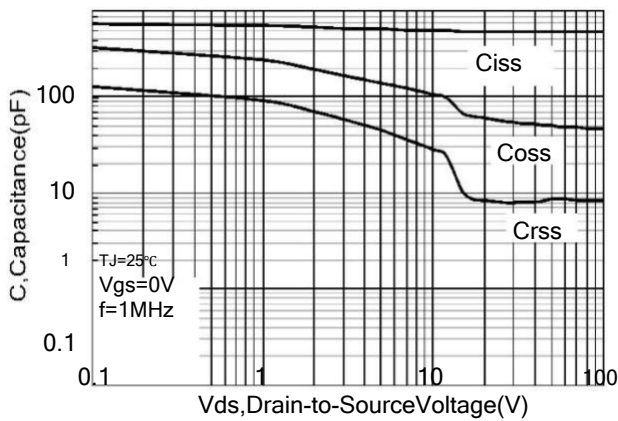
**Figure 2. Typical Transfer Characteristics**



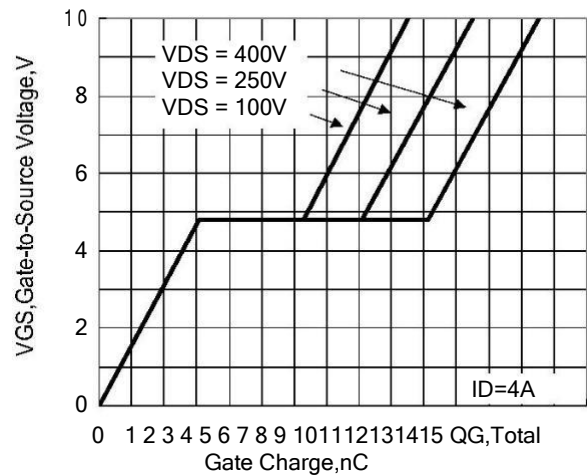
**Figure 3. On-Resistance versus Drain Current**



**Figure 4. Diode Forward Voltage versus Current**



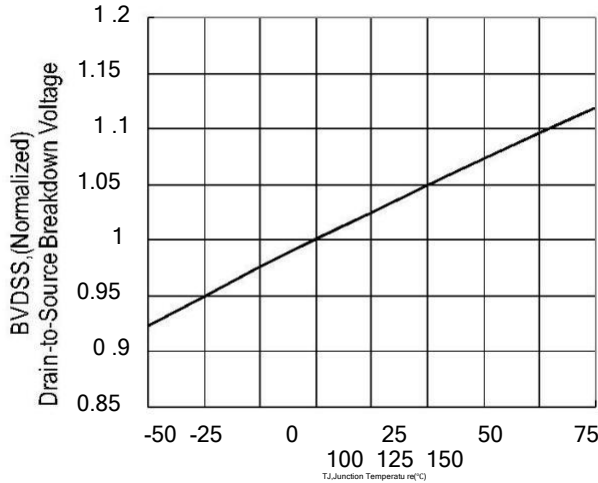
**Figure 5. Typical Capacitance vs. Drain-to-Source Voltage**



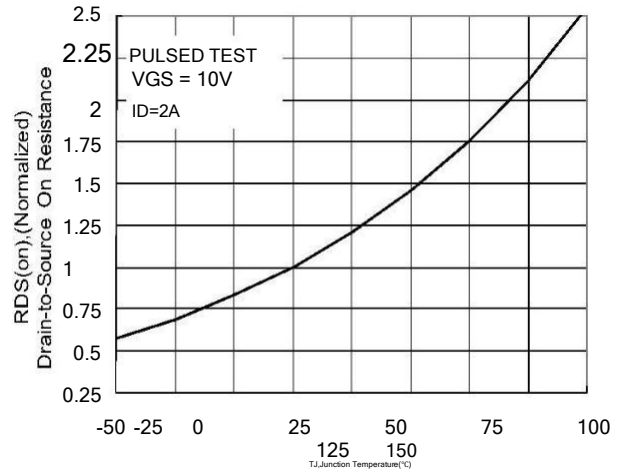
**Figure 6. Typical Gate Charge vs. Vgs**



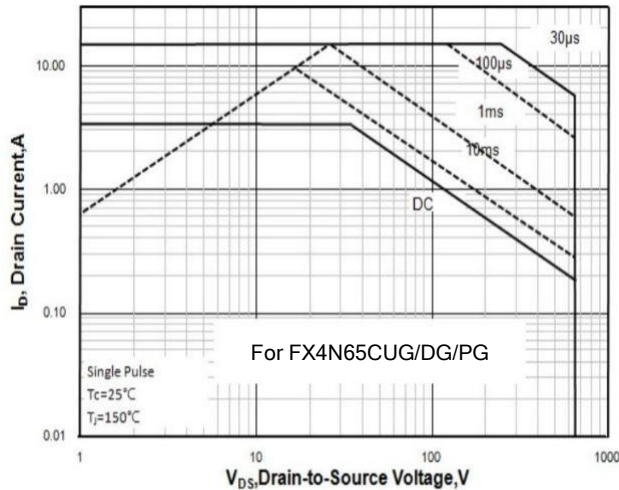
**TYPICAL CHARACTERISTICS(Cont.)**



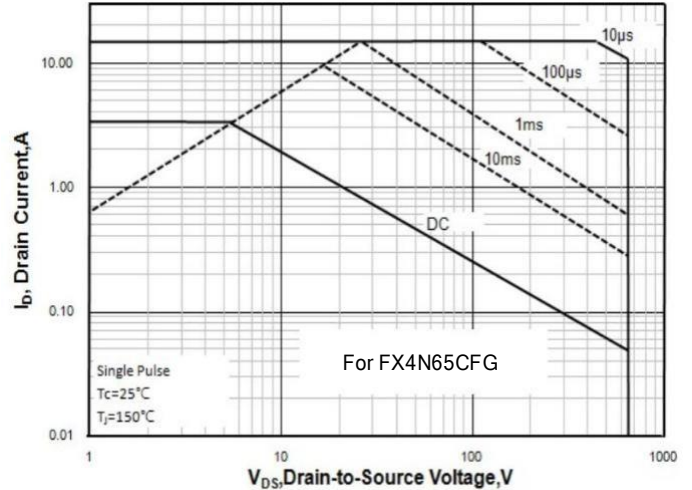
**Figure 7. BvDSS Variation with Temperature**



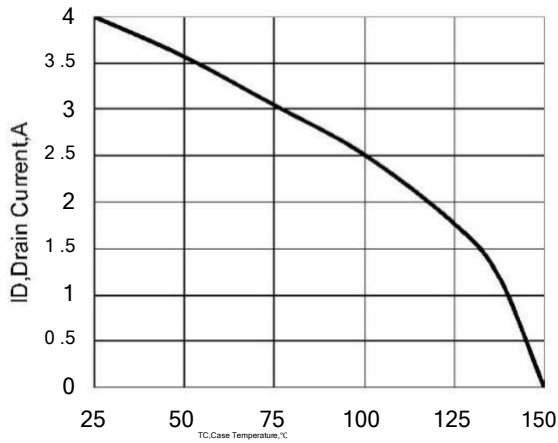
**Figure 8. On-Resistance Variation with Temperature**



**Figure 9. Maximum Safe Operating Area**

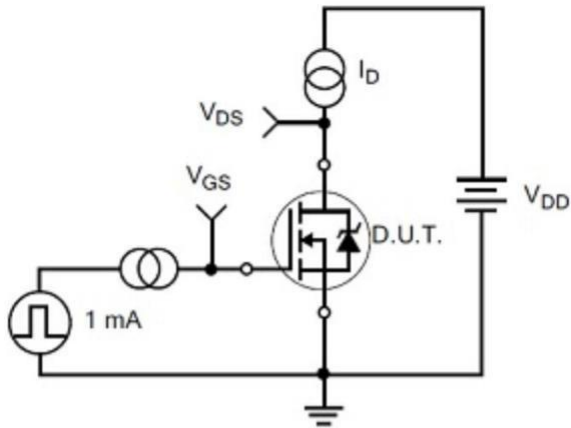


**Figure 9. Maximum Safe Operating Area**

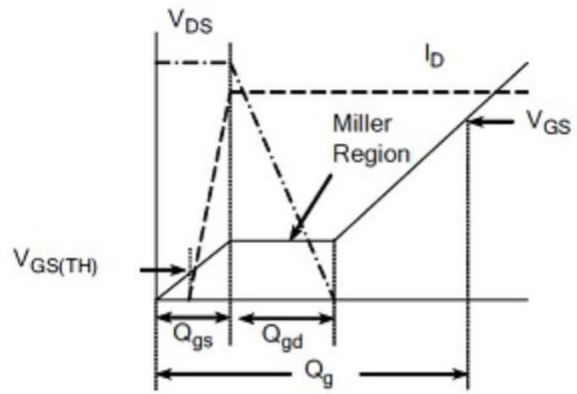


**Figure 10. Maximum Continuous Drain Current vs Case Temperature**

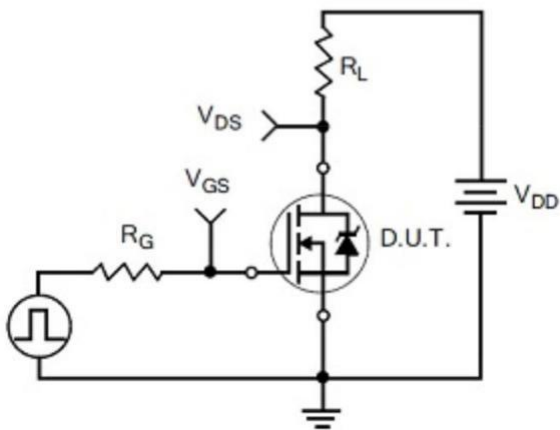
TEST CIRCUITS AND WAVEFORMS



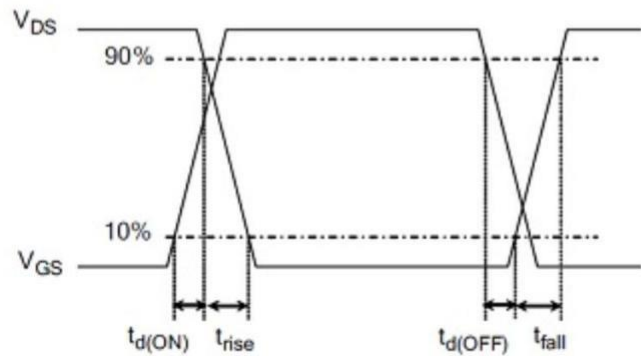
Gate Charge Test Circuit



Gate Charge Waveform

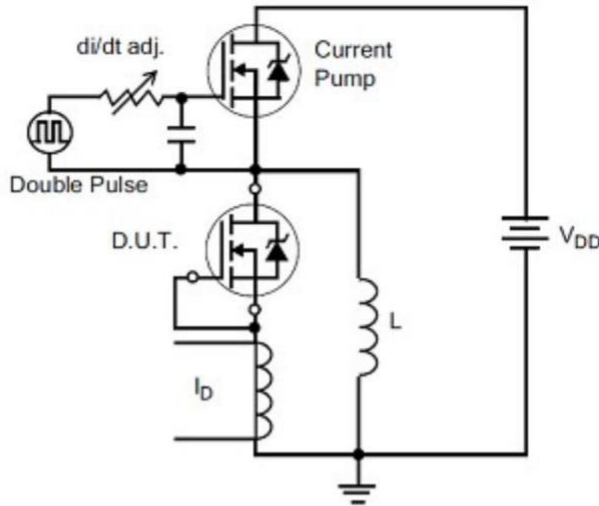


Resistive Switching Test Circuit

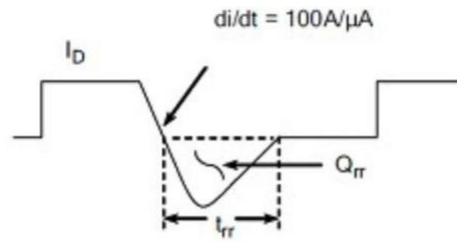


Resistive Switching Waveforms

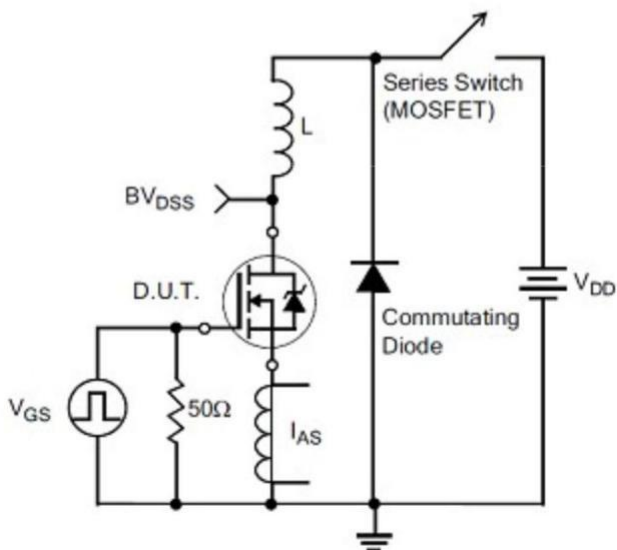
TEST CIRCUITS AND WAVEFORMS(Cont.)



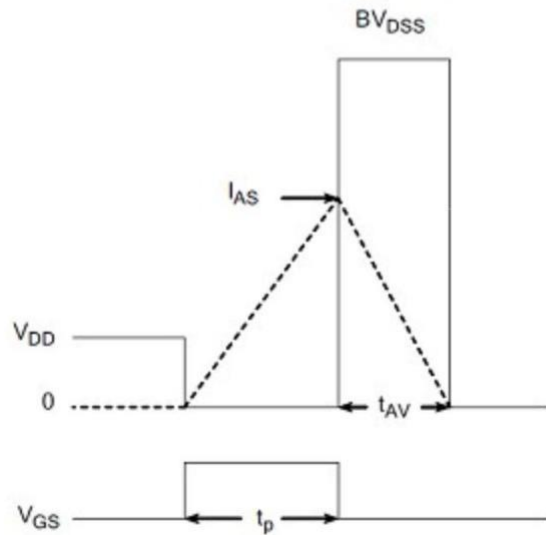
Diode Reverse Recovery Test Circuit



Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit



$$E_{AS} = \frac{I_{AS}^2 L}{2}$$

Unclamped Inductive Switching Waveforms

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