

## Description

The HSM24P03 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

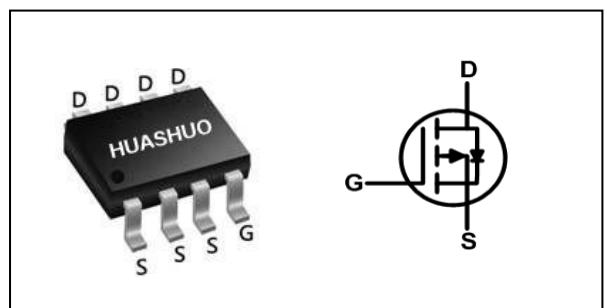
The HSM24P03 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

## Product Summary

V <sub>DS</sub>	-30	V
R <sub>DSON,typ</sub>	3.8	mΩ
I <sub>D</sub>	-24	A

## SOP8 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-24	A
I <sub>D</sub> @T <sub>c</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	-96	A
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>4</sup>	2.1	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup> (t≤10S)	---	-	°C/W
	Thermal Resistance Junction-ambient <sup>1</sup> (Steady State)	---	60	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-case <sup>1</sup>	---	24	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_D=-250\mu\text{A}$	-30	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$\text{V}_{\text{GS}}=-10\text{V}$ , $\text{I}_D=-17\text{A}$	---	3.8	4.8	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-4.5\text{V}$ , $\text{I}_D=-10\text{A}$	---	5.8	7.8	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$ , $\text{I}_D=-250\mu\text{A}$	-1.2	-1.6	-2.2	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=-24\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{T}_J=25^\circ\text{C}$	---	---	-1	$\text{uA}$
		$\text{V}_{\text{DS}}=-24\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{T}_J=55^\circ\text{C}$	---	---	-10	
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$\text{Q}_g$	Total Gate Charge (-10V)	$\text{V}_{\text{DS}}=-15\text{V}$ , $\text{V}_{\text{GS}}=-10\text{V}$ , $\text{I}_D=-10\text{A}$	---	110	---	$\text{nC}$
$\text{Q}_{\text{gs}}$	Gate-Source Charge		---	15	---	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		---	18	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=-15\text{V}$ , $\text{V}_{\text{GS}}=-10\text{V}$ , $\text{R}_G=6\Omega$ , $\text{I}_D=-1\text{A}$	---	28	---	$\text{ns}$
$\text{T}_r$	Rise Time		---	17	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	180	---	
$\text{T}_f$	Fall Time		---	73	---	
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{DS}}=-25\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	6240	---	$\text{pF}$
$\text{C}_{\text{oss}}$	Output Capacitance		---	780	---	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	410	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{I}_s$	Continuous Source Current <sup>1,5</sup>	$\text{V}_G=\text{V}_D=0\text{V}$ , Force Current	---	---	-24	A
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_s=-1\text{A}$ , $\text{T}_J=25^\circ\text{C}$	---	---	-1	V

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 4.The data is theoretically the same as  $\text{I}_D$  and  $\text{I}_{\text{DM}}$  , in real applications , should be limited by total power dissipation.



### Typical Characteristics

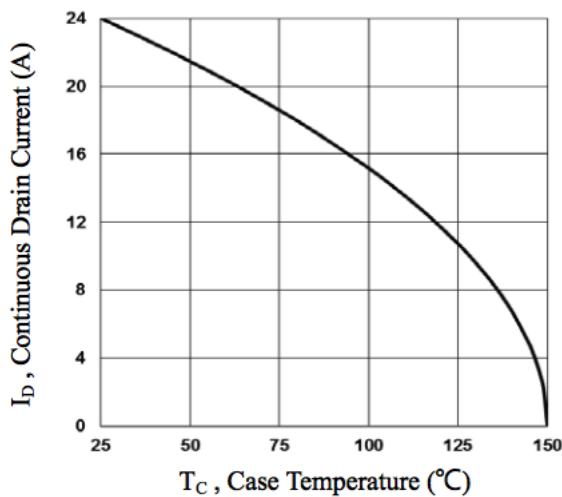


Fig.1 Continuous Drain Current vs.  $T_C$

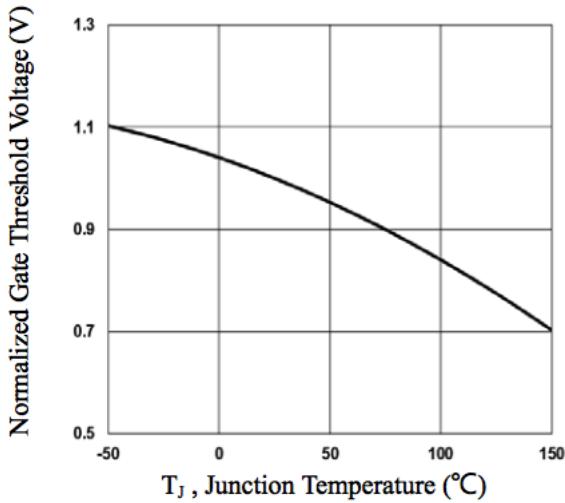


Fig.3 Normalized  $V_{th}$  vs.  $T_J$

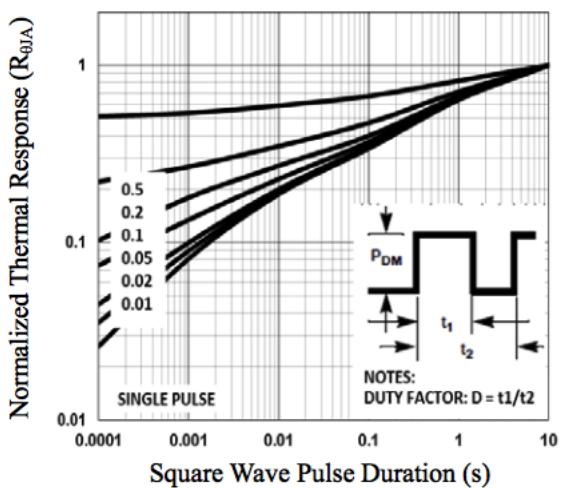


Fig.5 Normalized Transient Impedance

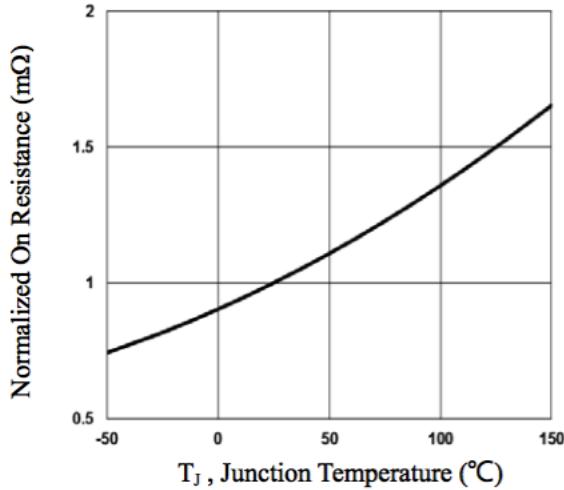


Fig.2 Normalized RDSON vs.  $T_J$

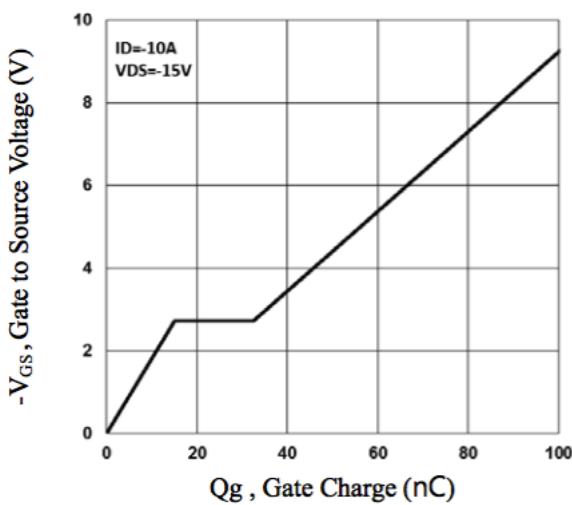


Fig.4 Gate Charge Waveform

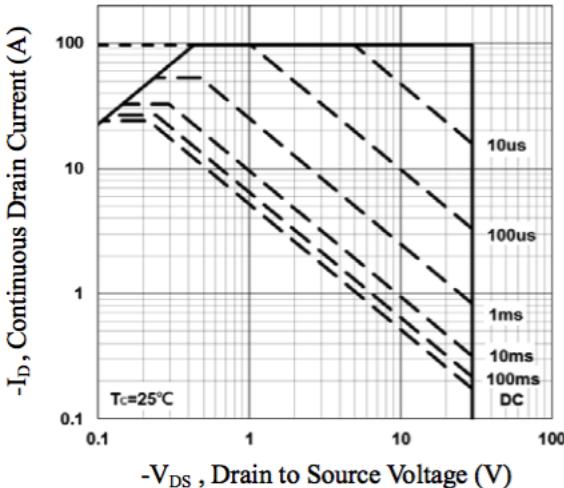


Fig.6 Maximum Safe Operation Area

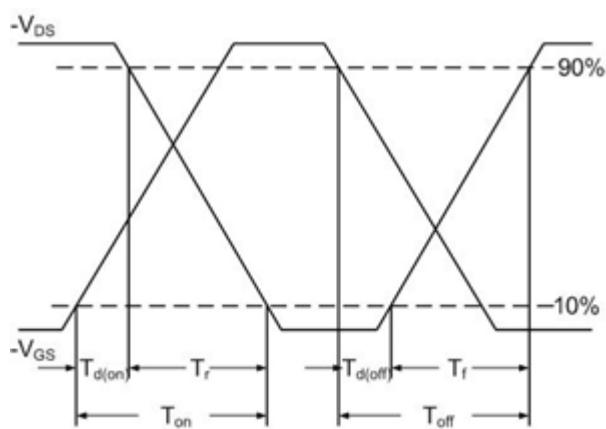


Fig.7 Switching Time Waveform

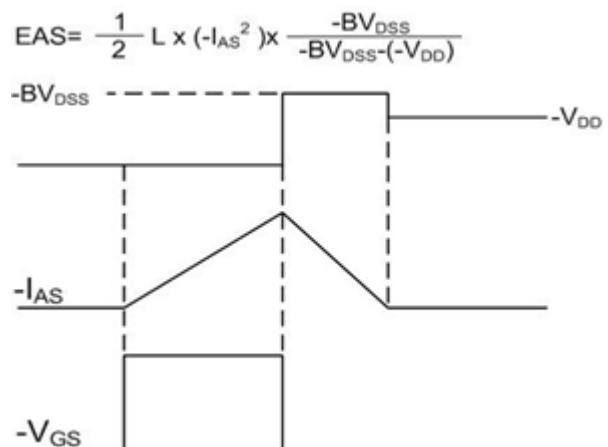
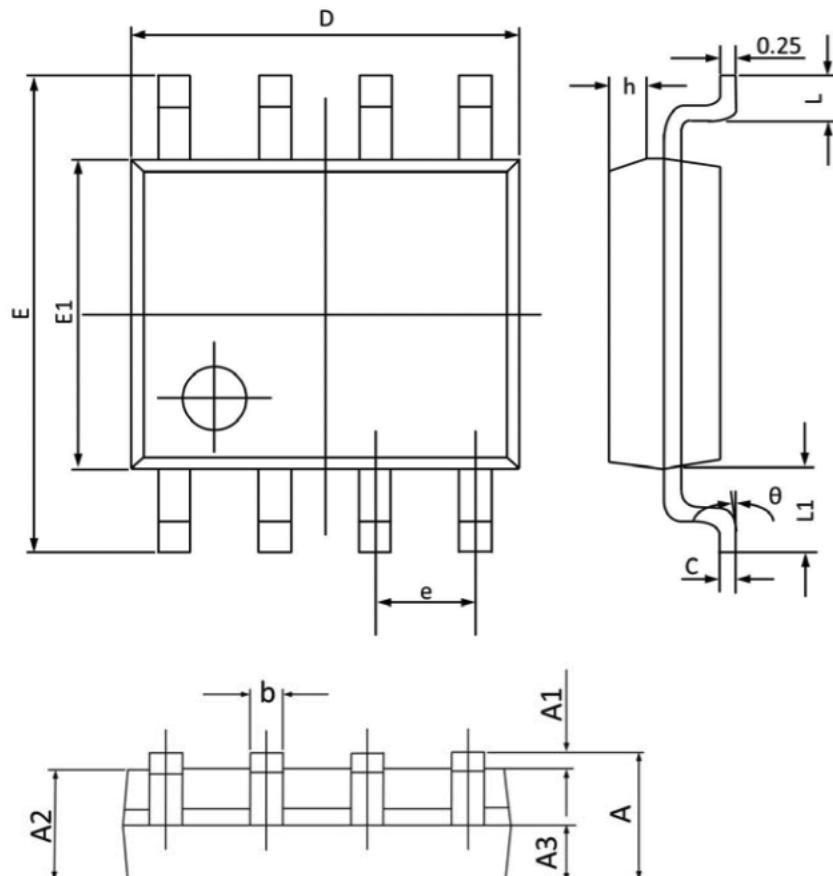


Fig.8 Unclamped Inductive Switching Waveform



## SOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050(BSC)		0.041(BSC)	
θ	0°	8°	0°	8°

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