

Description

The HSP8004 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

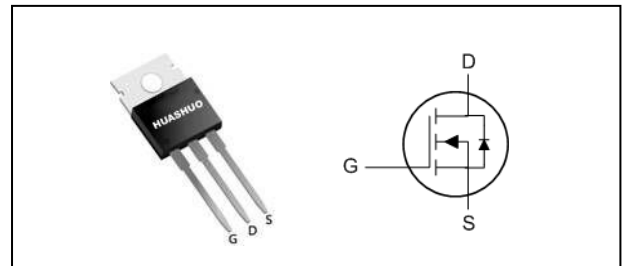
The HSP8004 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

Product Summary

V_{DS}	80	V
$R_{DS(ON),TYP}$	3.1	m Ω
I_D	175	A

TO-220 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	80	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	175	A
$I_D @ T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	123	A
I_{DM}	Pulsed Drain Current ²	440	A
EAS	Single Pulse Avalanche Energy ³	378	mJ
$P_D @ T_C=25^\circ C$	Total Power Dissipation ⁴	192	W
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	0.65	$^\circ C/W$



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B _{VDS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	80	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	3.1	3.5	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.8	2.5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =64V, V _{GS} =0V, T _J =25°C	---	---	100	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =30A	---	50	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.2	---	Ω
Q _g	Total Gate Charge (10V)	V _{DD} =40V, V _{GS} =10V, I _D =20A	---	80	---	nC
Q _{gs}	Gate-Source Charge		---	15	---	
Q _{gd}	Gate-Drain Charge		---	16	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =40V, V _{GS} =10V, R _G =6Ω, I _D =20A	---	17	---	ns
T _r	Rise Time		---	32	---	
T _{d(off)}	Turn-Off Delay Time		---	50	---	
T _f	Fall Time		---	18	---	
C _{iss}	Input Capacitance	V _{DS} =40V, V _{GS} =0V, f=1MHz	---	4450	---	pF
C _{oss}	Output Capacitance		---	800	---	
C _{rss}	Reverse Transfer Capacitance		---	50	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	175	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =A, T _J =25°C	---	---	1.5	V
t _{rr}	Reverse Recovery Time	I _F =20A, dI/dt=100A/μs, T _J =25°C	---	40	---	nS
Q _{rr}	Reverse Recovery Charge		---	45	---	nC



Electrical Characteristic Diagrams

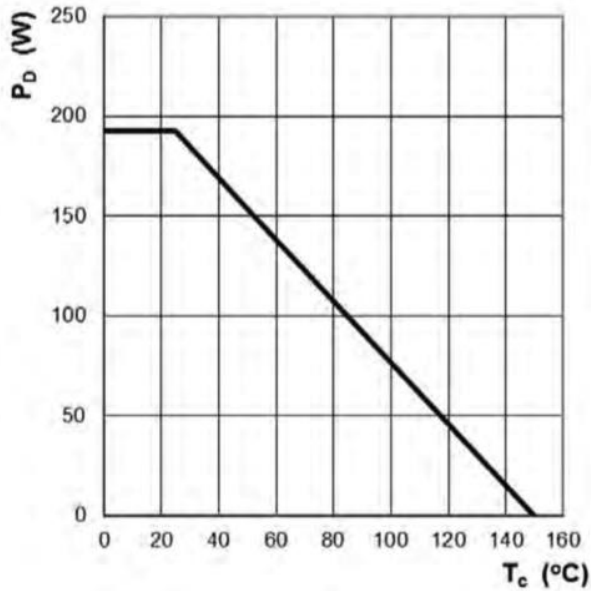


Figure 1: Power Dissipation

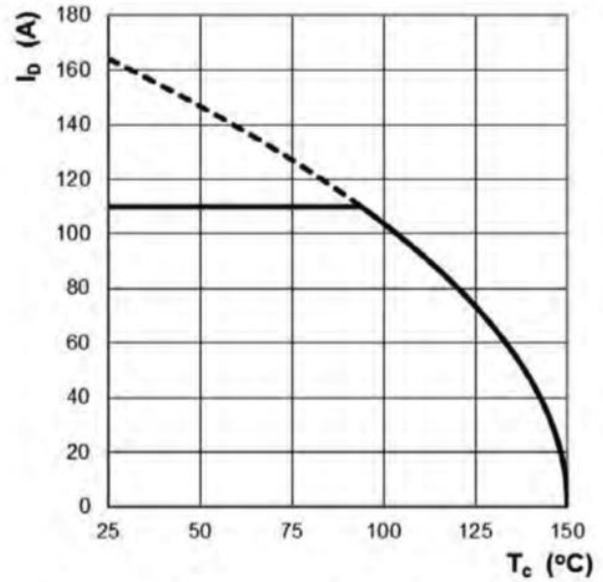


Figure 2: Continuous Drain Current vs. T_c

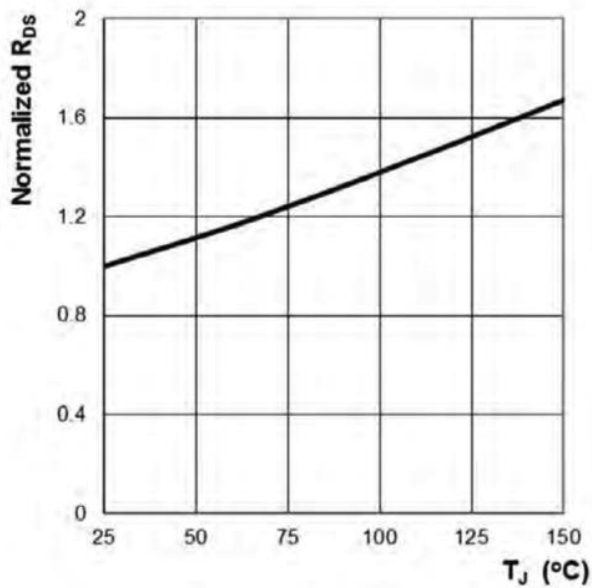


Figure 3: Normalized $R_{DS(ON)}$ vs. T_J

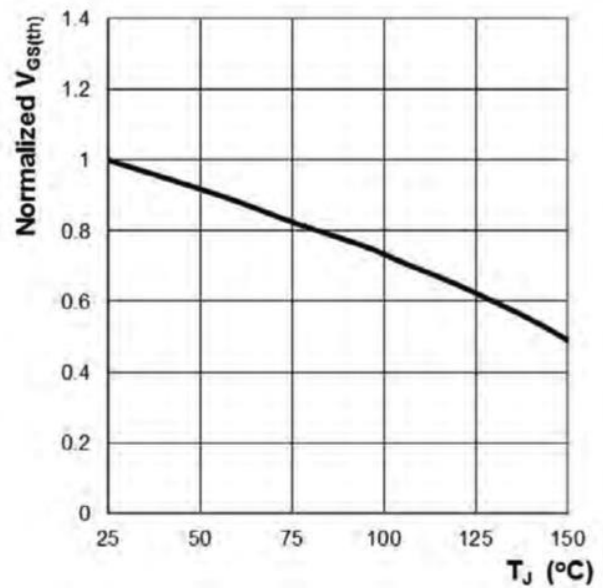


Figure 4: Normalized BV_{DSS} vs. T_J

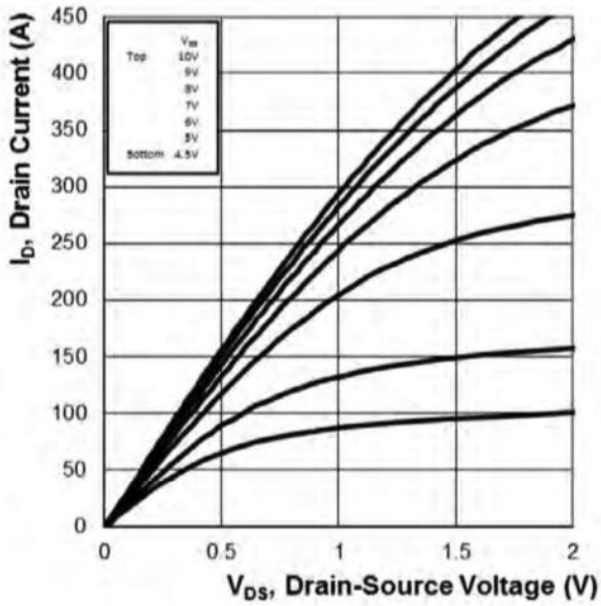


Figure 5: On-Region Characteristics

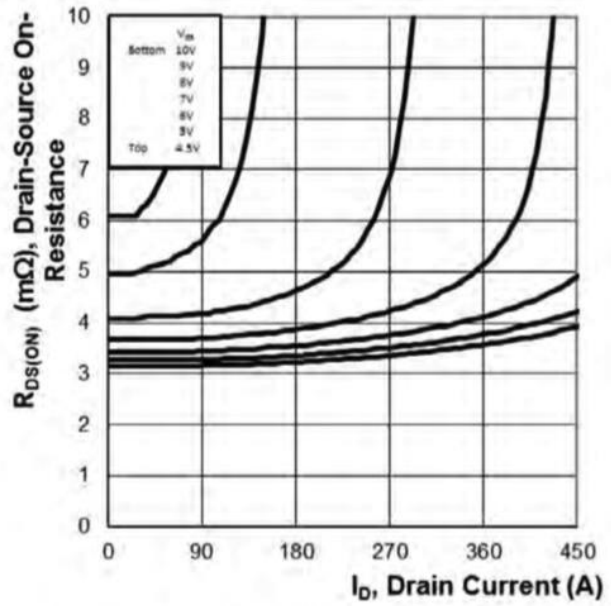


Figure 6: Typ. R_{DS} Variation vs. I_D and V_{GS}

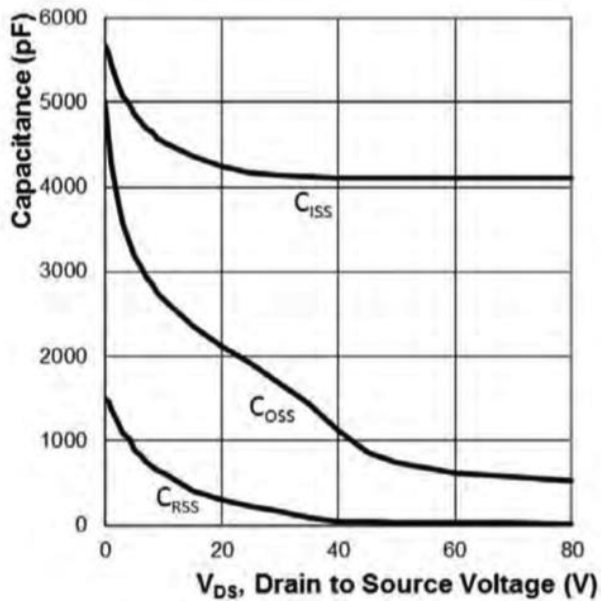


Figure 7: Typ. Capacitance Characteristics

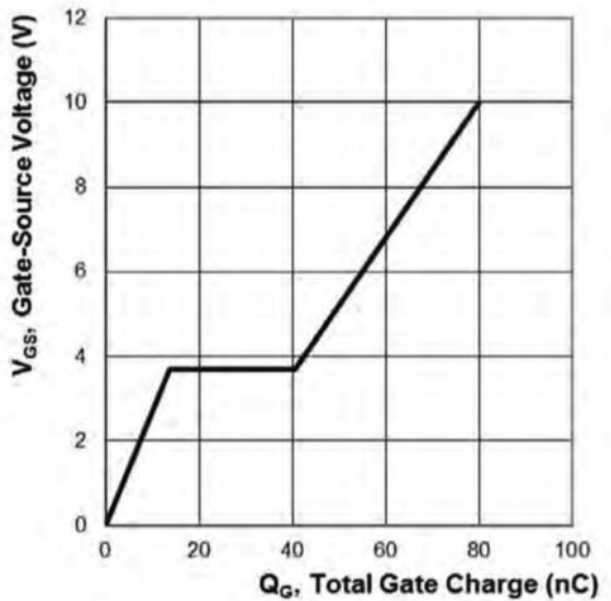


Figure 8: Typ. Gate Charge Characteristics

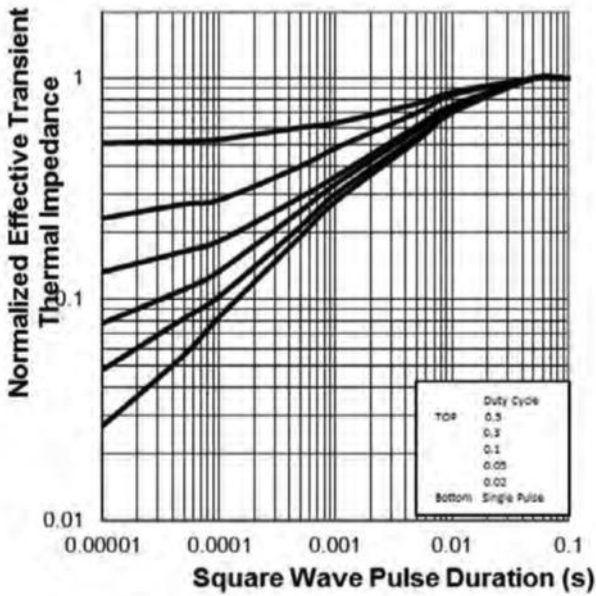


Figure 9: Normalized Thermal Transient Impedance, Junction-to-Case

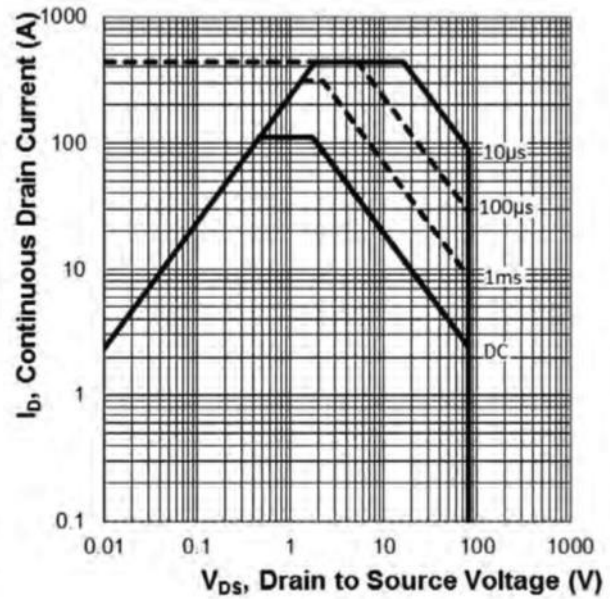


Figure 10: Maximum Safe Operation Area

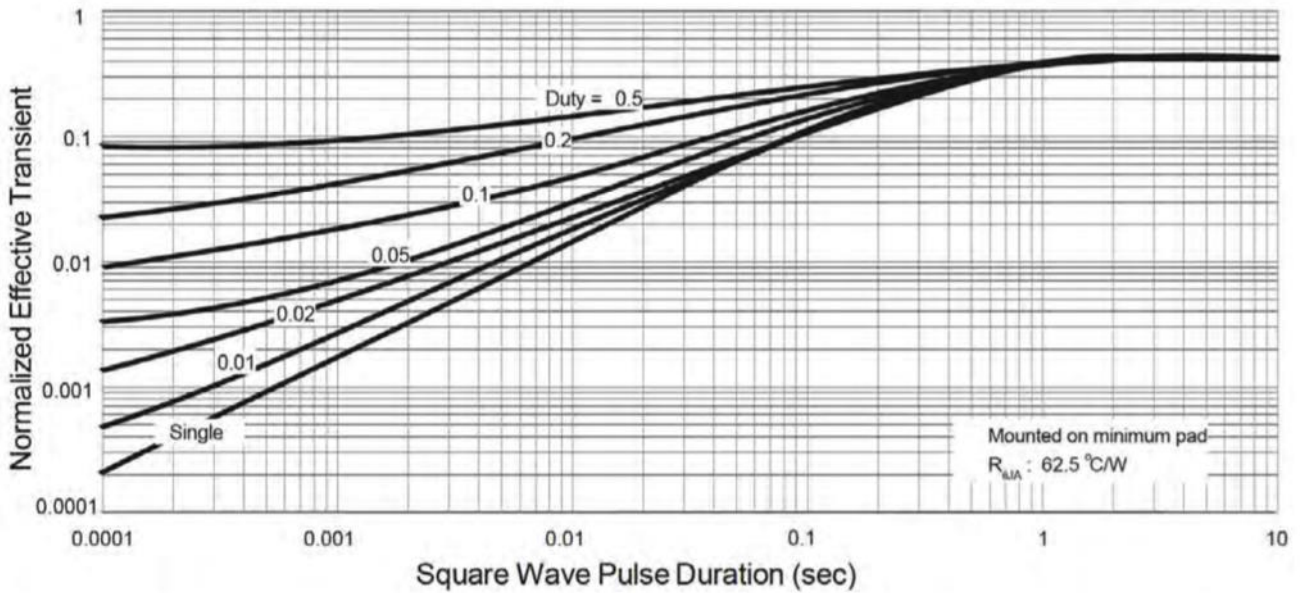


Fig 7 Normalized Maximum Transient Thermal Impedance



Parameter Test Circuits

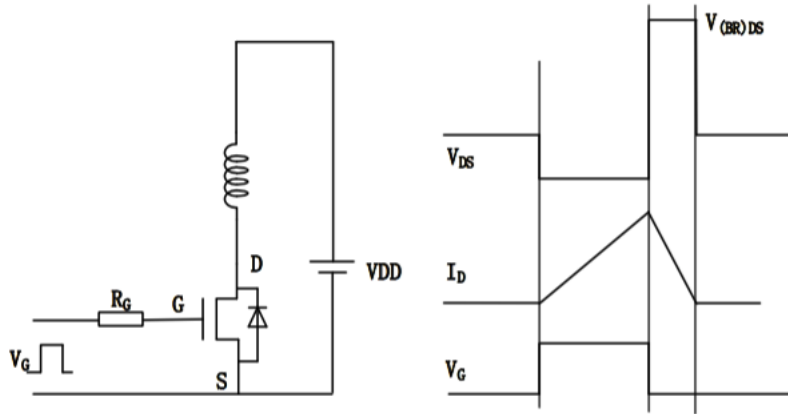


Figure 10 Unclamped Inductive Switching (UIS) Test circuit and waveforms

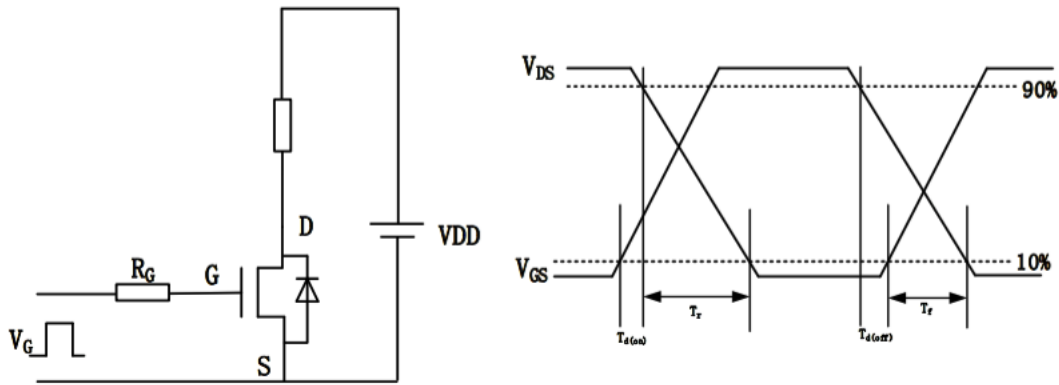


Figure 11 Resistive Switching time Test circuit and waveforms

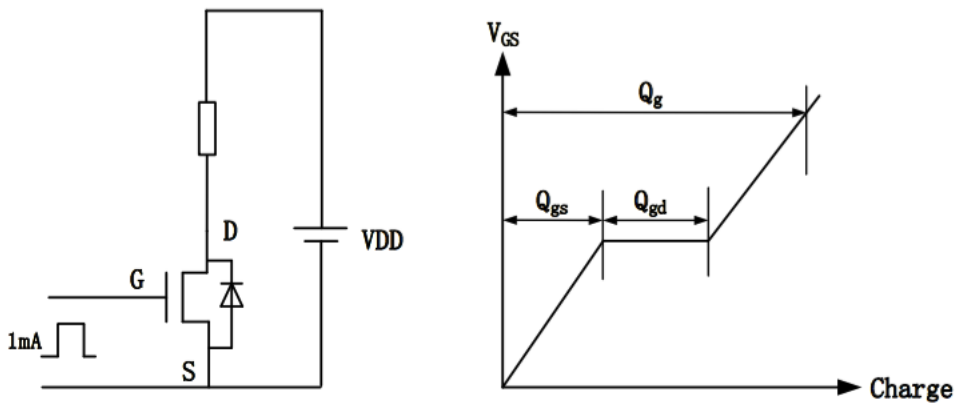


Figure 12 Gate charge Test circuit and waveforms

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