

Description

The HSS2333 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

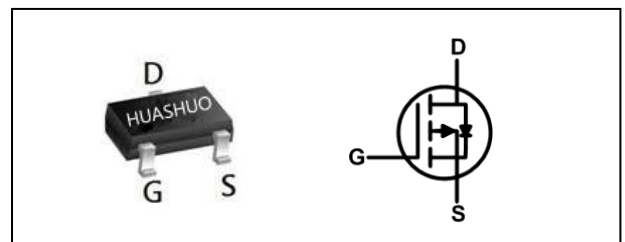
The HSS2333 meet the RoHS and Green Product requirement with full function reliability approved.

- Super Low Gate Charge
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

Product Summary

V _{DS}	-12	V
R _{DS(ON),typ}	15.3	mΩ
I _D	-8	A

SOT 23 Pin Configurations



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-12	V
V _{GS}	Gate-Source Voltage	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -4.5V ₁	-8	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -4.5V ₁	-6.8	A
I _{DM}	Pulsed Drain Current ₂	-32	A
P _D @T _A =25°C	Total Power Dissipation ₃	1.2	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ₁	---	100	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-12	---	---	V
ΔB _V DSS/ΔT _J	B _V DSS Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.014	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V, I _D =-8A	---	15.3	18	mΩ
		V _{GS} =-2.5V, I _D =-6A	---	20.8	28	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.4	-0.68	-1.2	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	3.95	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-12V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} =-12V, V _{GS} =0V, T _J =55°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-5V, I _D =-8A	---	5	---	S
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-6V, V _{GS} =-4.5V, I _D =-5A	---	35	---	nC
Q _{gs}	Gate-Source Charge		---	5	---	
Q _{gd}	Gate-Drain Charge		---	10	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-6V, V _{GS} =-4.5V, R _G =3.3Ω, I _D =-5A	---	11	---	ns
T _r	Rise Time		---	33	---	
T _{d(off)}	Turn-Off Delay Time		---	31	---	
T _f	Fall Time		---	10	---	
C _{iss}	Input Capacitance	V _{DS} =-6V, V _{GS} =0V, f=1MHz	---	2700	---	pF
C _{oss}	Output Capacitance		---	680	---	
C _{rss}	Reverse Transfer Capacitance		---	589	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	-8	A
I _{SM}	Pulsed Source Current ^{2,4}		---	---	-32	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

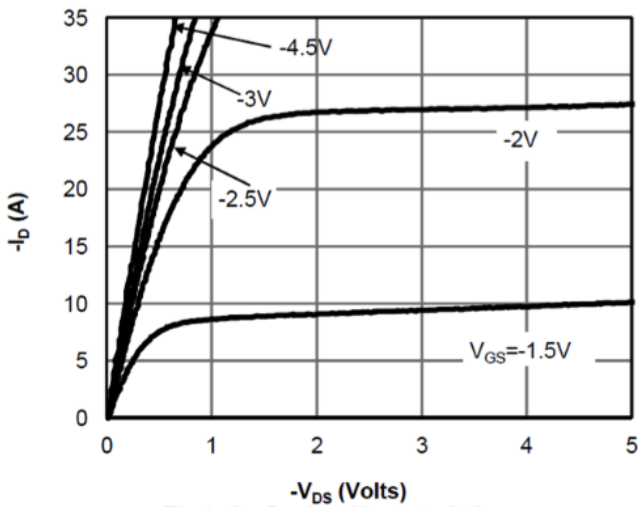


Fig 1: On-Region Characteristics

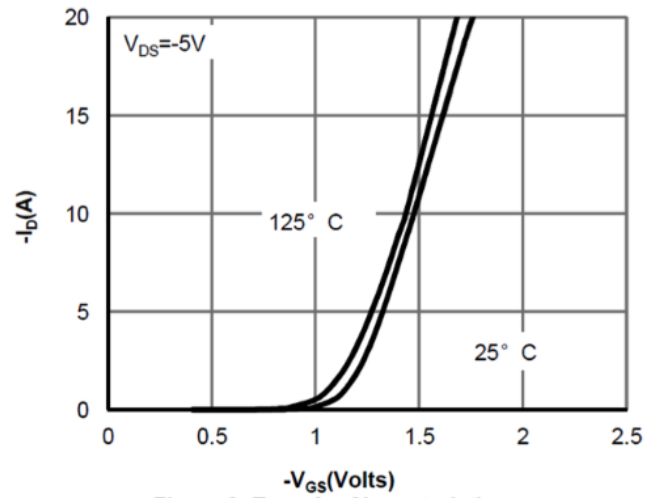


Figure 2: Transfer Characteristics

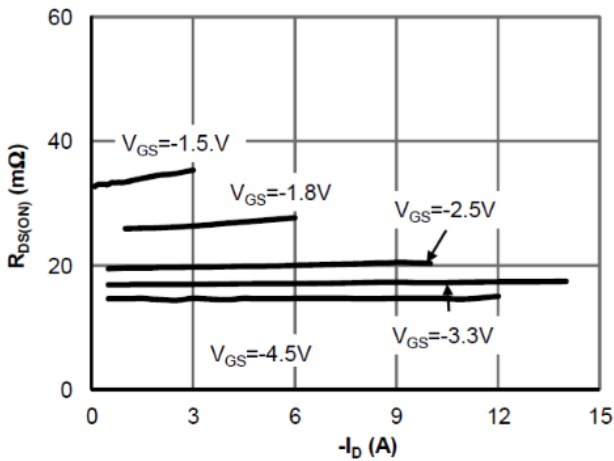


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

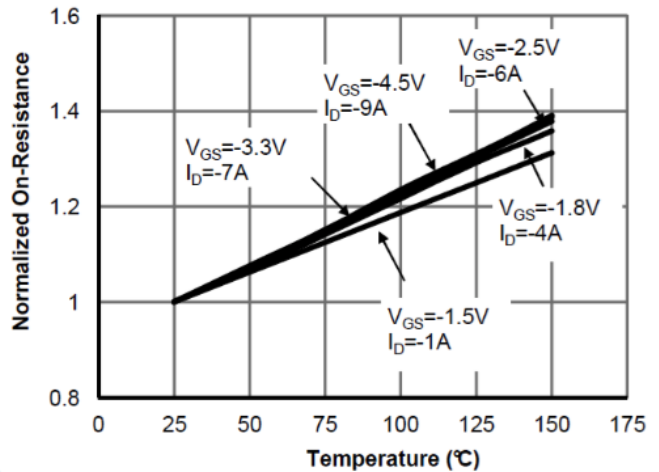


Figure 4: On-Resistance vs. Junction Temperature

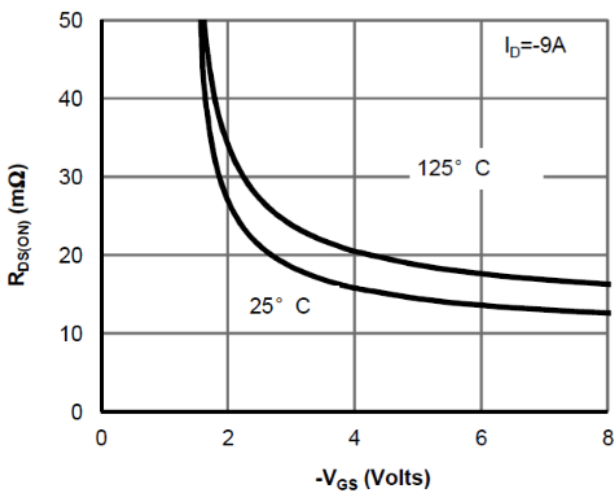


Figure 5: On-Resistance vs. Gate-Source Voltage

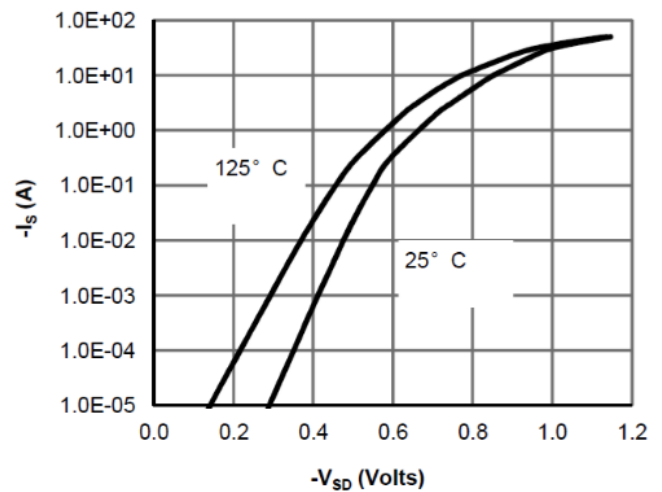


Figure 6: Body-Diode Characteristics



P-Ch 12V Fast Switching MOSFETs

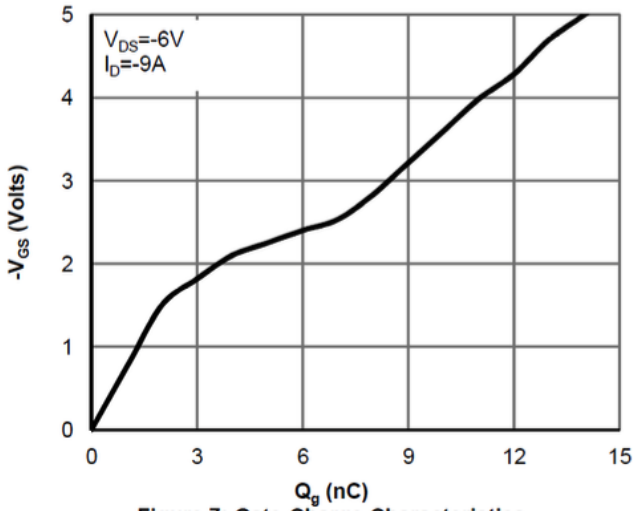


Figure 7: Gate-Charge Characteristics

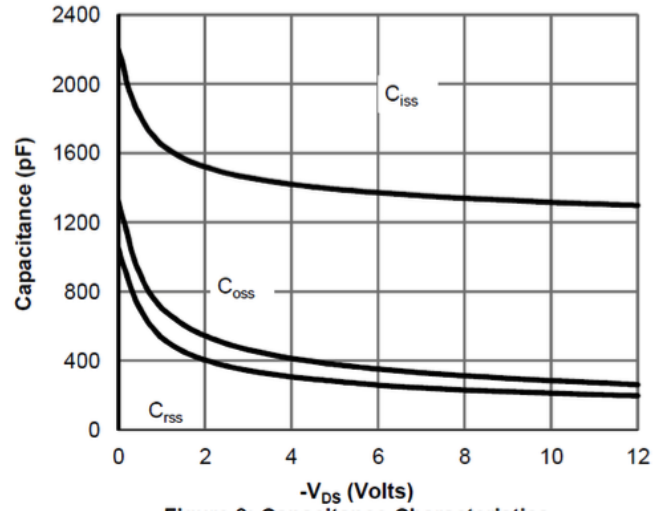


Figure 8: Capacitance Characteristics

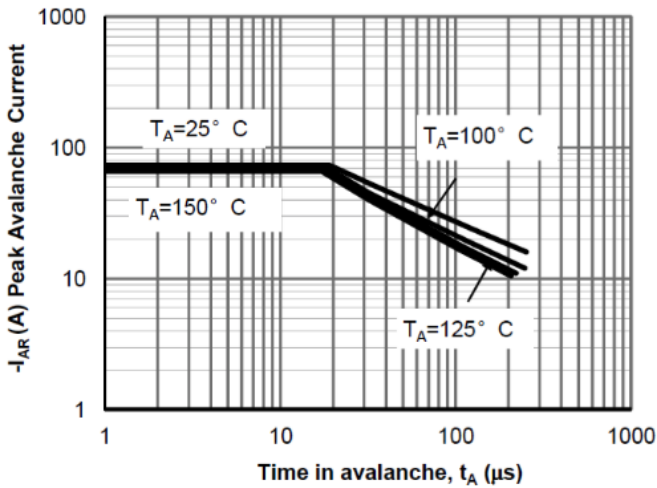


Figure 9: Single Pulse Avalanche capability

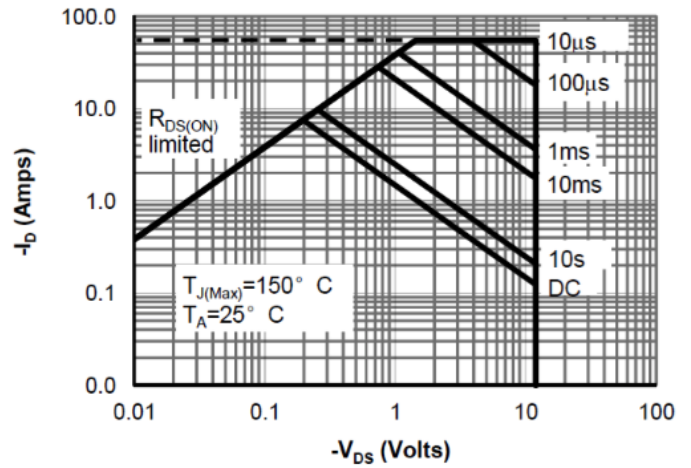


Figure 10: Maximum Forward Biased Safe Operating Area

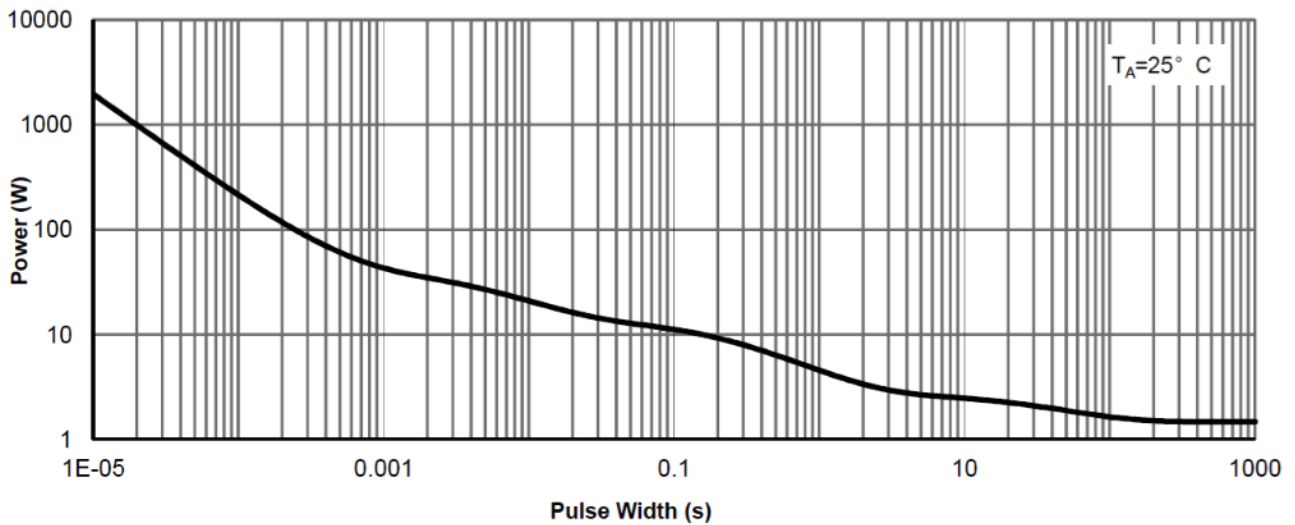
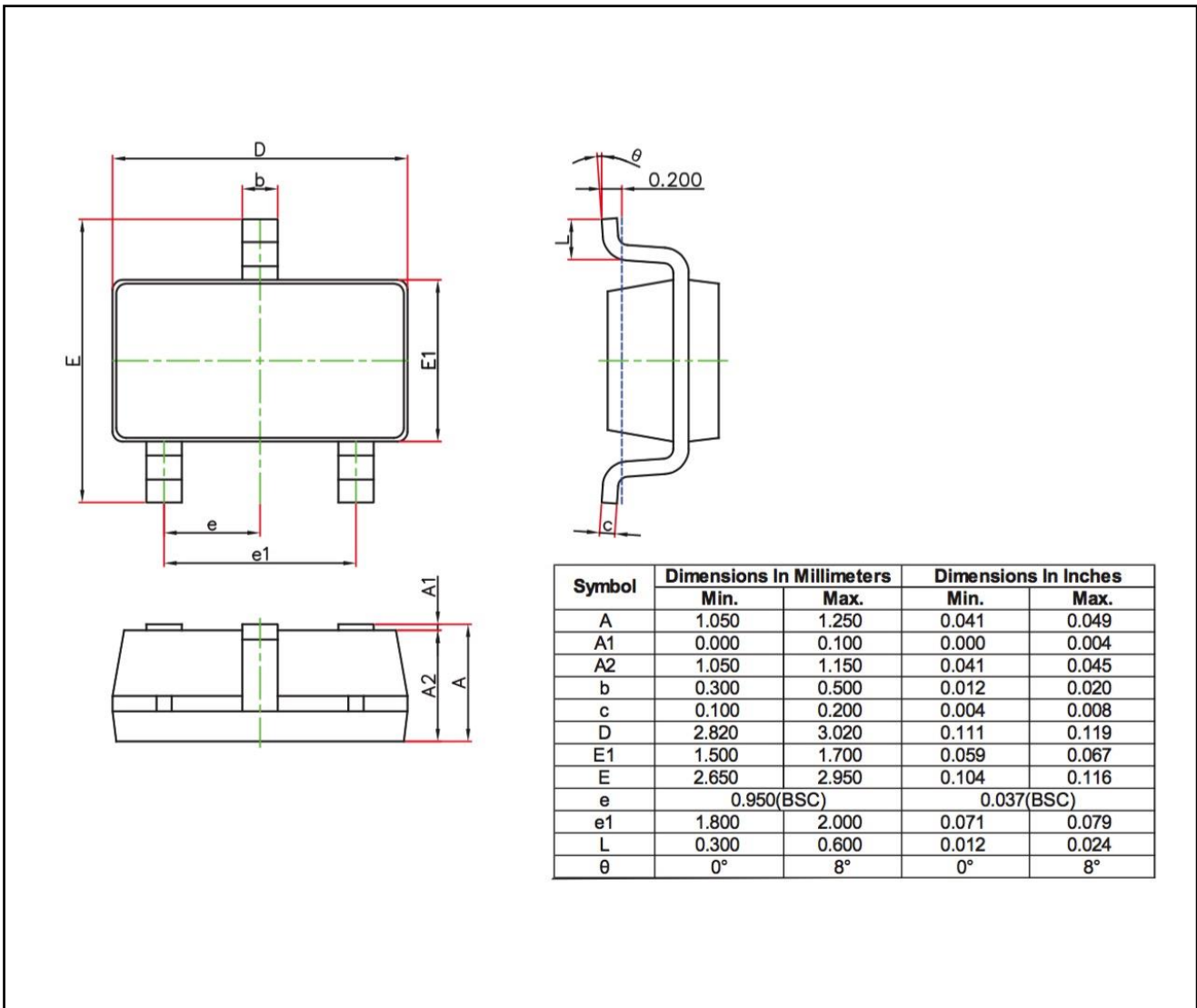


Figure 11: Single Pulse Power Rating Junction-to-Ambient

Ordering Information

Part Number	Package code	Packaging
HSS2333	SOT-23L	3000/Tape&Reel



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