

## Description

The HSU120P03 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

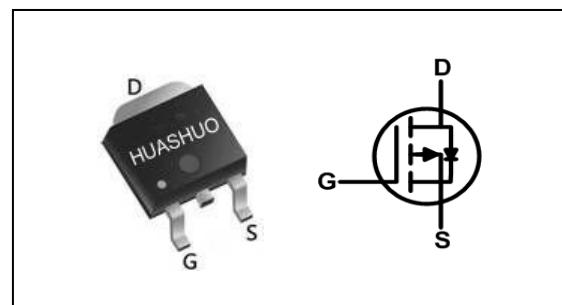
The HSU120P03 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

## Product Summary

V <sub>DS</sub>	-30	V
R <sub>DSON,typ</sub>	3.1	mΩ
I <sub>D</sub>	-120	A

## TO252 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1,6</sup>	-120	A
I <sub>D</sub> @T <sub>c</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1,6</sup>	-89	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	-504	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	576	mJ
P <sub>D</sub> @T <sub>c</sub> =25°C	Total Power Dissipation <sup>4</sup>	107	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup> (t≤10S)	---	20	°C/W
	Thermal Resistance Junction-ambient <sup>1</sup> (Steady State)	---	50	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-case <sup>1</sup>	---	1.45	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_{\text{D}}=-250\mu\text{A}$	-30	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$\text{V}_{\text{GS}}=-10\text{V}$ , $\text{I}_{\text{D}}=-20\text{A}$	---	3.1	3.9	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-4.5\text{V}$ , $\text{I}_{\text{D}}=-20\text{A}$	---	4.6	6	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$ , $\text{I}_{\text{D}}=-250\mu\text{A}$	-1.0	---	-2.5	V
$\text{I}_{\text{bss}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=-24\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{T}_J=25^\circ\text{C}$	---	---	-1	$\text{uA}$
		$\text{V}_{\text{DS}}=-24\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{T}_J=55^\circ\text{C}$	---	---	-5	
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$\text{Q}_g$	Total Gate Charge (-10V)	$\text{V}_{\text{DS}}=-15\text{V}$ , $\text{V}_{\text{GS}}=-10\text{V}$ , $\text{I}_{\text{D}}=-20\text{A}$	---	125	---	$\text{nC}$
$\text{Q}_{\text{gs}}$	Gate-Source Charge		---	12	---	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		---	31	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=-15\text{V}$ , $\text{V}_{\text{GS}}=-10\text{V}$ , $\text{R}_g=3.3\Omega$ , $\text{I}_{\text{D}}=20\text{A}$	---	14	---	$\text{ns}$
$\text{T}_r$	Rise Time		---	12	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	66	---	
$\text{T}_f$	Fall Time		---	35	---	
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{DS}}=-15\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	7300	---	$\text{pF}$
$\text{C}_{\text{oss}}$	Output Capacitance		---	819	---	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	530	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{I}_s$	Continuous Source Current <sup>1,5</sup>	$\text{V}_{\text{G}}=\text{V}_{\text{D}}=0\text{V}$ , Force Current	---	---	-120	A
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_{\text{S}}=-1\text{A}$ , $\text{T}_J=25^\circ\text{C}$	---	---	-1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $\text{V}_{\text{DD}}=-15\text{V}$ , $\text{V}_{\text{GS}}=-10\text{V}$ , $\text{L}=0.5\text{mH}$ , $\text{R}_g=25\Omega$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5.The data is theoretically the same as  $\text{I}_{\text{D}}$  and  $\text{I}_{\text{DM}}$  , in real applications , should be limited by total power dissipation
- 6.The maximum current rating is package limited.



Typical Characteristics

Figure 1. Output Characteristics

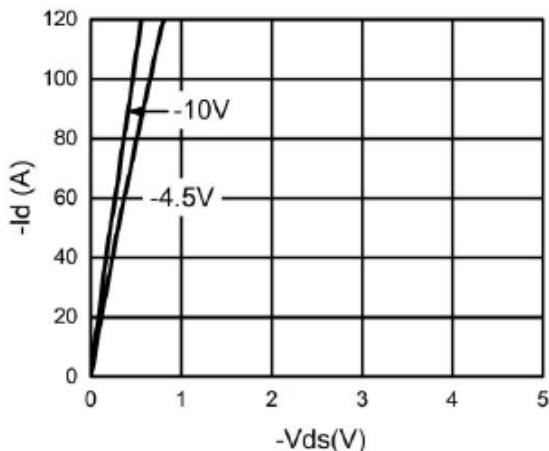


Figure 2. Transfer Characteristics

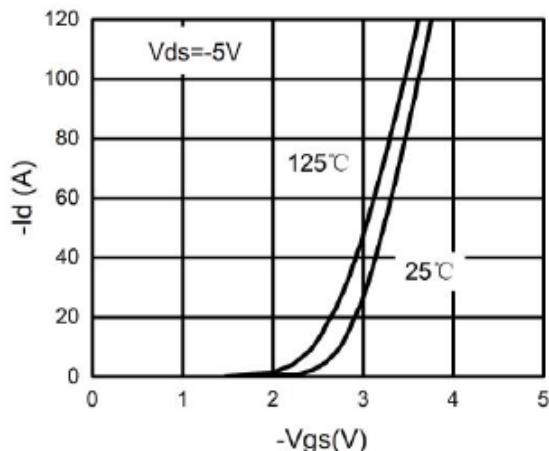


Figure 3. Power Dissipation

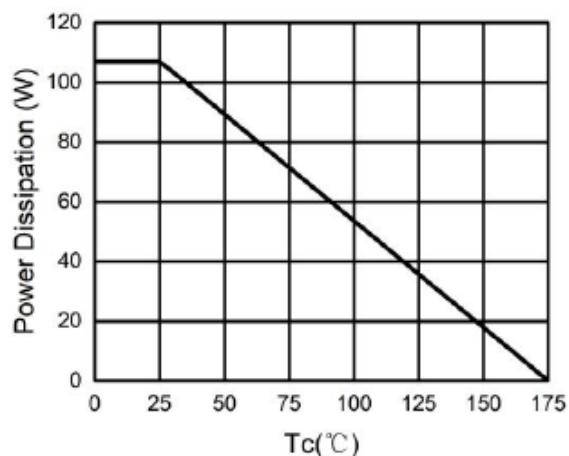


Figure 4. Drain Current

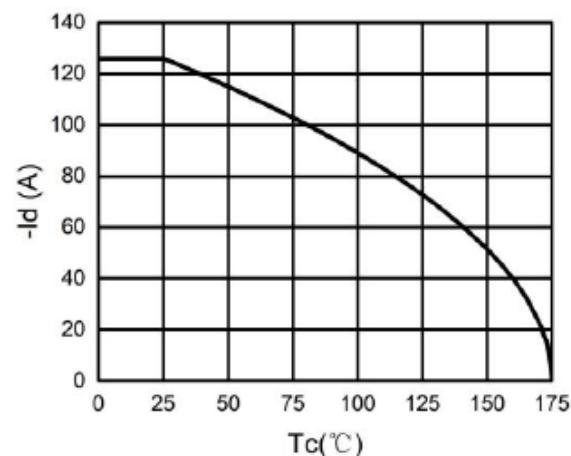


Figure 5.  $\text{BV}_{\text{DSS}}$  vs Junction Temperature

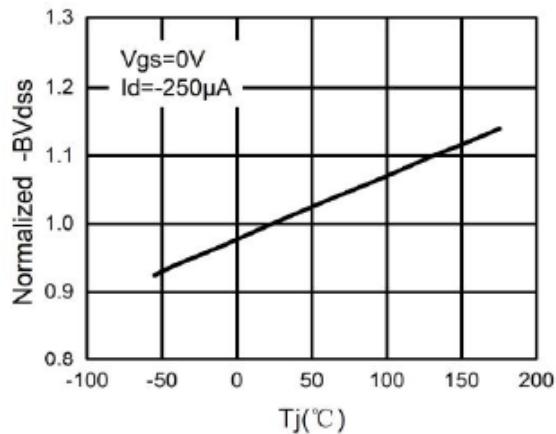


Figure 6.  $R_{\text{DS(ON)}}$  vs Junction Temperature

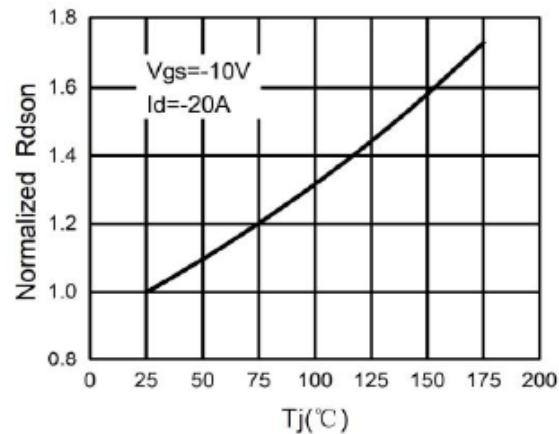




Figure 7. Gate Charge Waveforms

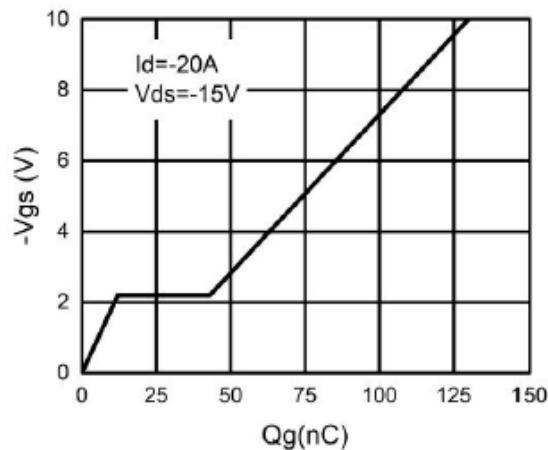


Figure 8. Capacitance

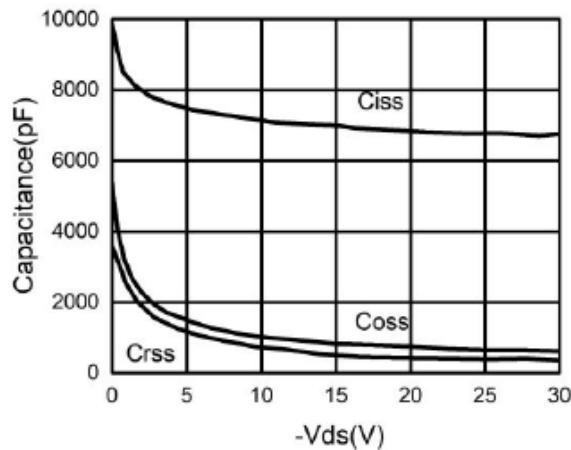


Figure 9. Body-Diode Characteristics

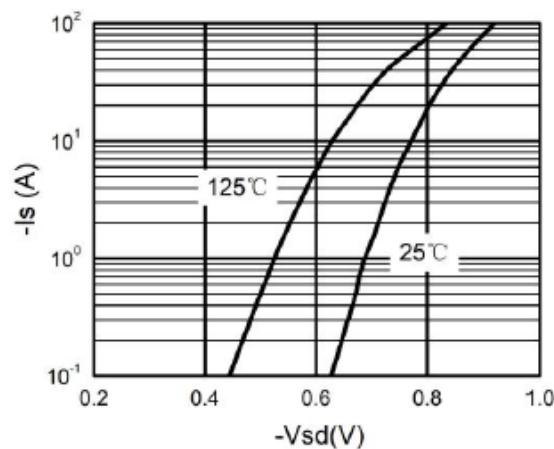
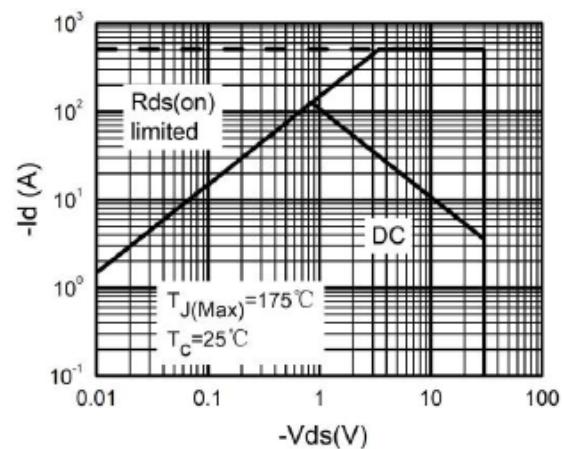


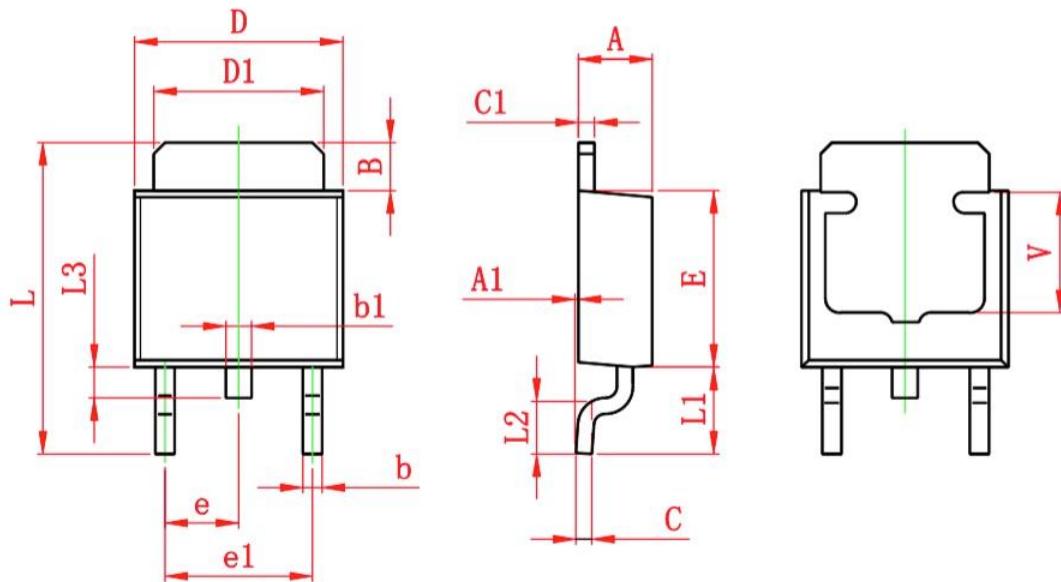
Figure 10. Maximum Safe Operating Area





## Ordering Information

Part Number	Package code	Packaging
HSU120P03	TO252-2	2500/Tape&Reel



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.800 REF.		0.150 REF.	

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