

### Description

The HSU3018B is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

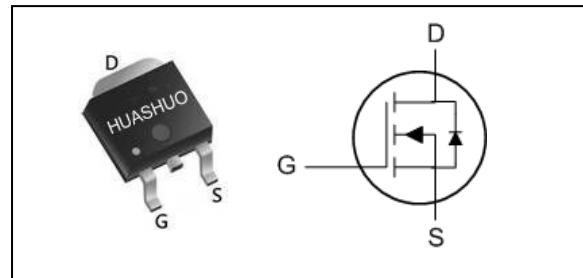
The HSU3018B meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

### Product Summary

V <sub>DS</sub>	30	V
R <sub>DS(ON),max</sub>	2.4	mΩ
I <sub>D</sub>	110	A

### TO252 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sub>1,6</sub>	110	A
I <sub>D</sub> @T <sub>c</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sub>1,6</sub>	54	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	280	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	180	mJ
I <sub>AS</sub>	Avalanche Current	60	A
P <sub>D</sub> @T <sub>c</sub> =25°C	Total Power Dissipation <sup>4</sup>	60	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>	---	62	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	2	°C/W

**Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_D=250\mu\text{A}$	30	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=30\text{A}$	---	2	2.4	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}$ , $\text{I}_D=15\text{A}$	---	2.6	3.2	
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$ , $\text{I}_D=250\mu\text{A}$	1.2	---	2.5	V
$\text{I}_{\text{bss}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=24\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{T}_J=25^{\circ}\text{C}$	---	---	1	$\text{uA}$
		$\text{V}_{\text{DS}}=24\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $\text{T}_J=55^{\circ}\text{C}$	---	---	5	
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
$\text{g}_{\text{fs}}$	Forward Transconductance	$\text{V}_{\text{DS}}=5\text{V}$ , $\text{I}_D=30\text{A}$	---	50	---	S
$\text{R}_{\text{g}}$	Gate Resistance	$\text{V}_{\text{DS}}=0\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1.7	---	$\Omega$
$\text{Q}_{\text{g}}$	Total Gate Charge (4.5V)	$\text{V}_{\text{DS}}=15\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{I}_D=15\text{A}$	---	56.9	---	$\text{nC}$
$\text{Q}_{\text{gs}}$	Gate-Source Charge		---	13.8	---	
$\text{Q}_{\text{gd}}$	Gate-Drain Charge		---	23.5	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=15\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{R}_{\text{g}}=3.3\Omega$ , $\text{I}_D=1\text{A}$	---	20.1	---	$\text{ns}$
$\text{T}_{\text{r}}$	Rise Time		---	6.3	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	124.6	---	
$\text{T}_{\text{f}}$	Fall Time		---	15.8	---	
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{DS}}=15\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	4345	---	$\text{pF}$
$\text{C}_{\text{oss}}$	Output Capacitance		---	340	---	
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance		---	225	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{I}_{\text{s}}$	Continuous Source Current <sup>1,6</sup>	$\text{V}_{\text{G}}=\text{V}_{\text{D}}=0\text{V}$ , Force Current	---	---	70	A
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_{\text{s}}=1\text{A}$ , $\text{T}_J=25^{\circ}\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $\text{V}_{\text{DD}}=25\text{V}$ , $\text{V}_{\text{GS}}=10\text{V}$ , $\text{L}=0.1\text{mH}$ , $\text{I}_{\text{AS}}=60\text{A}$
- 4.The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature
- 5.The data is theoretically the same as  $\text{I}_{\text{D}}$  and  $\text{I}_{\text{DM}}$  , in real applications , should be limited by total power dissipation.
- 6.The maximum current rating is package limited.



### Typical Characteristics

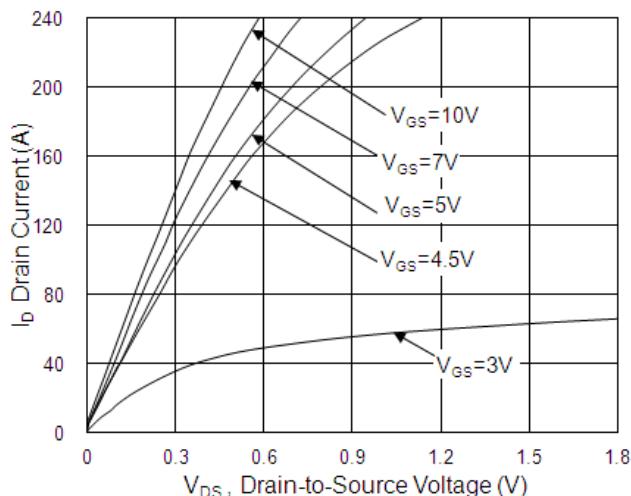


Fig.1 Typical Output Characteristics

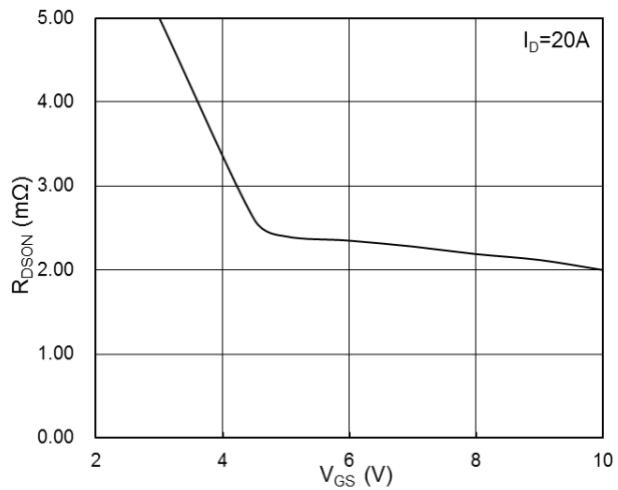


Fig.2 On-Resistance vs. G-S Voltage

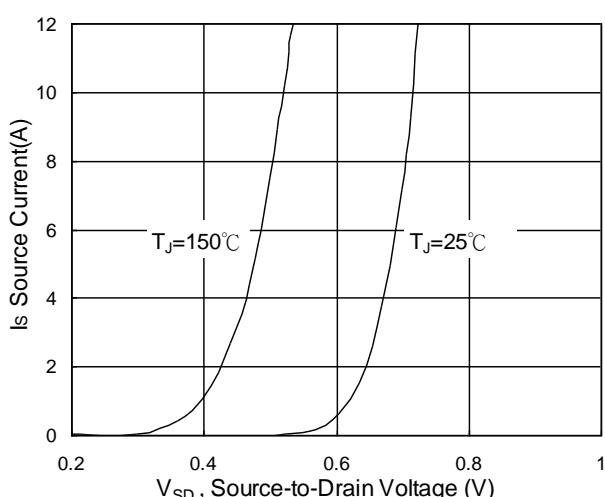


Fig.3 Source Drain Forward Characteristics

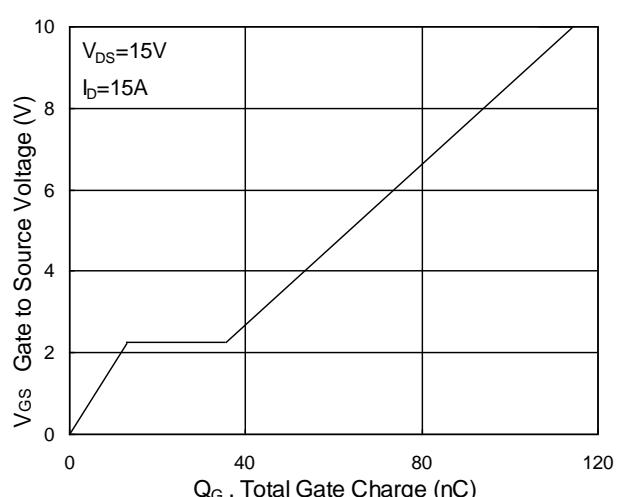


Fig.4 Gate-Charge Characteristics

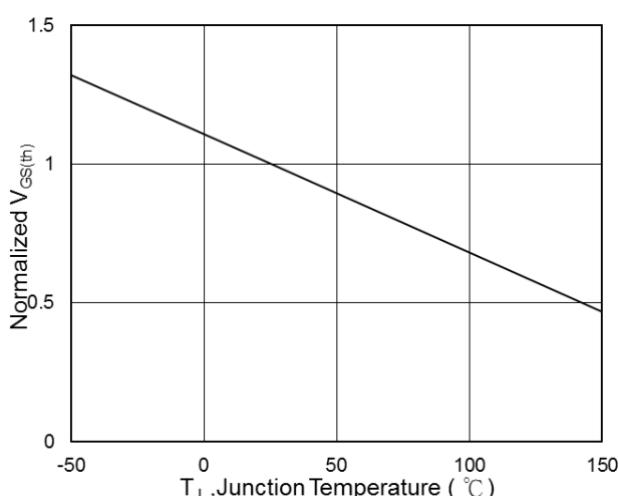


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

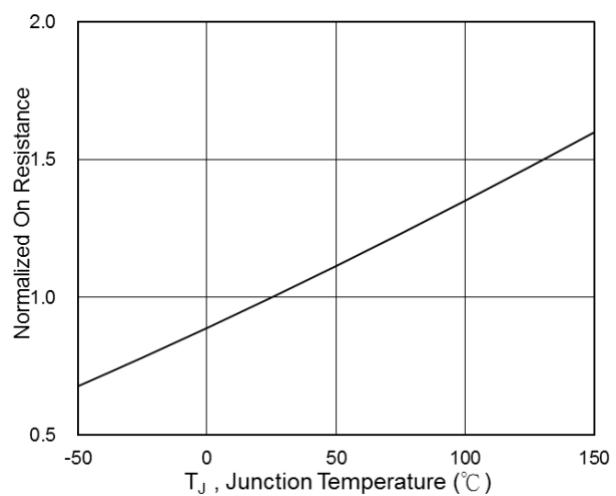


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$



HUASHUO  
SEMICONDUCTOR

HSU3018B

N-Ch 30V Fast Switching MOSFETs

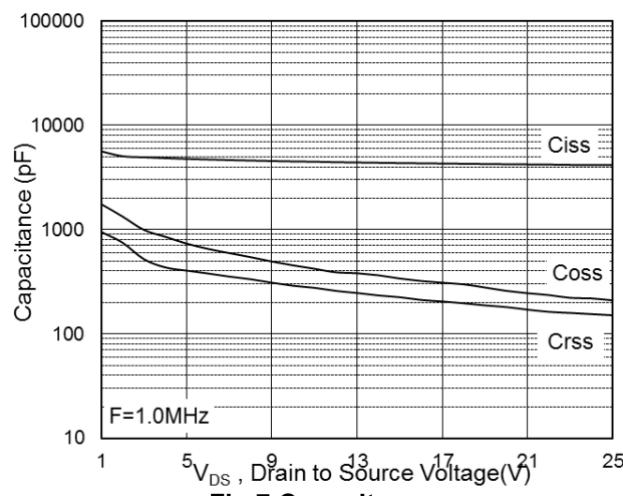


Fig.7 Capacitance

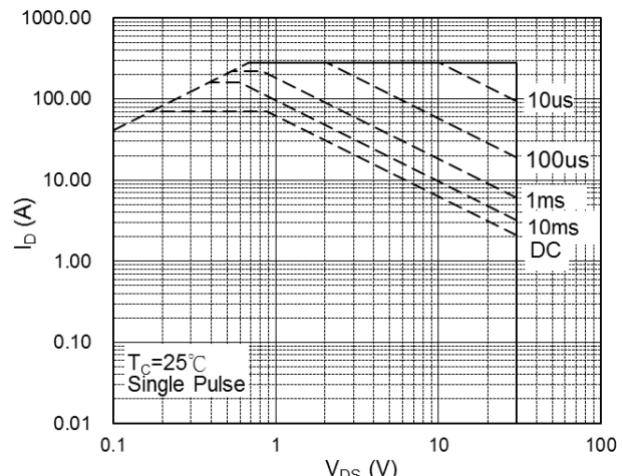


Fig.8 Safe Operating Area

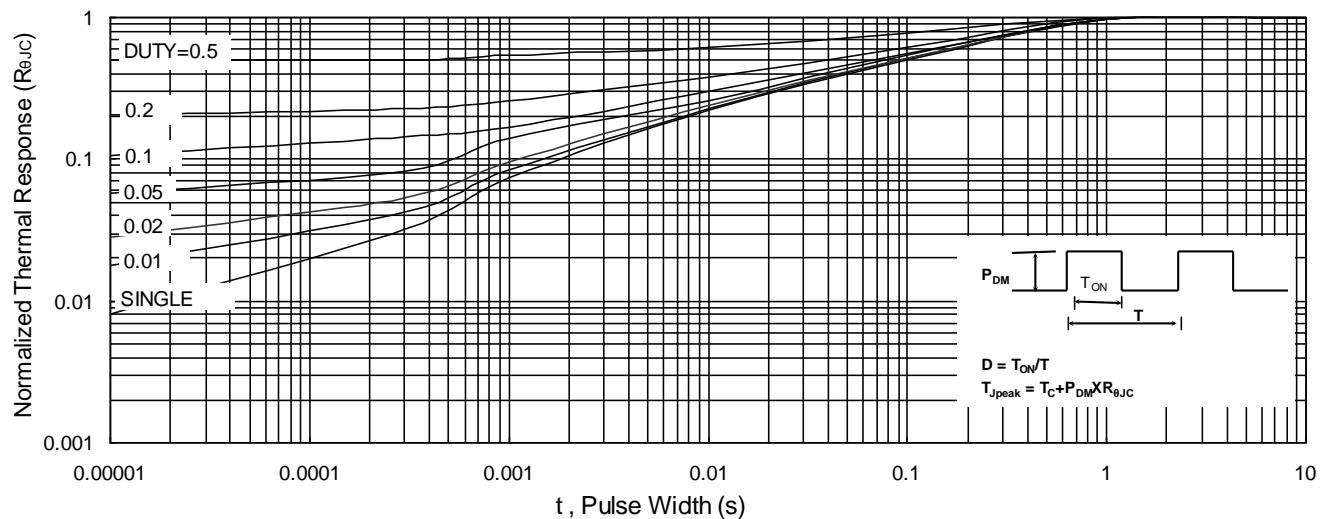


Fig.9 Normalized Maximum Transient Thermal Impedance

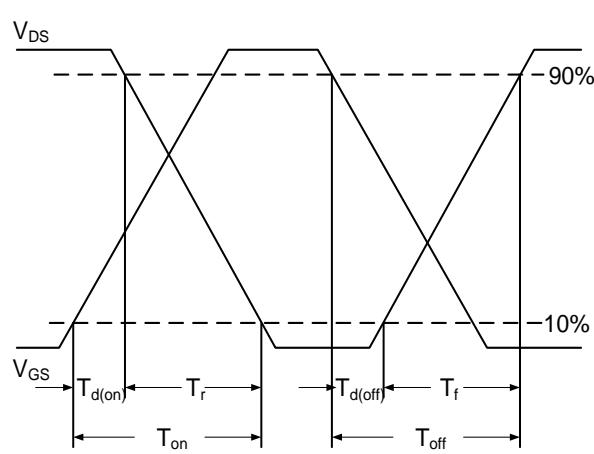


Fig.10 Switching Time Waveform

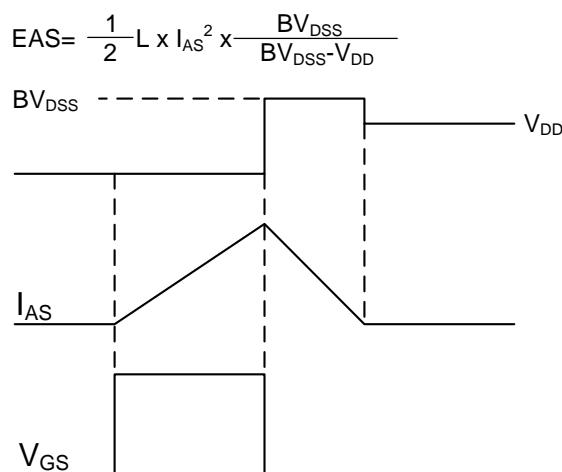
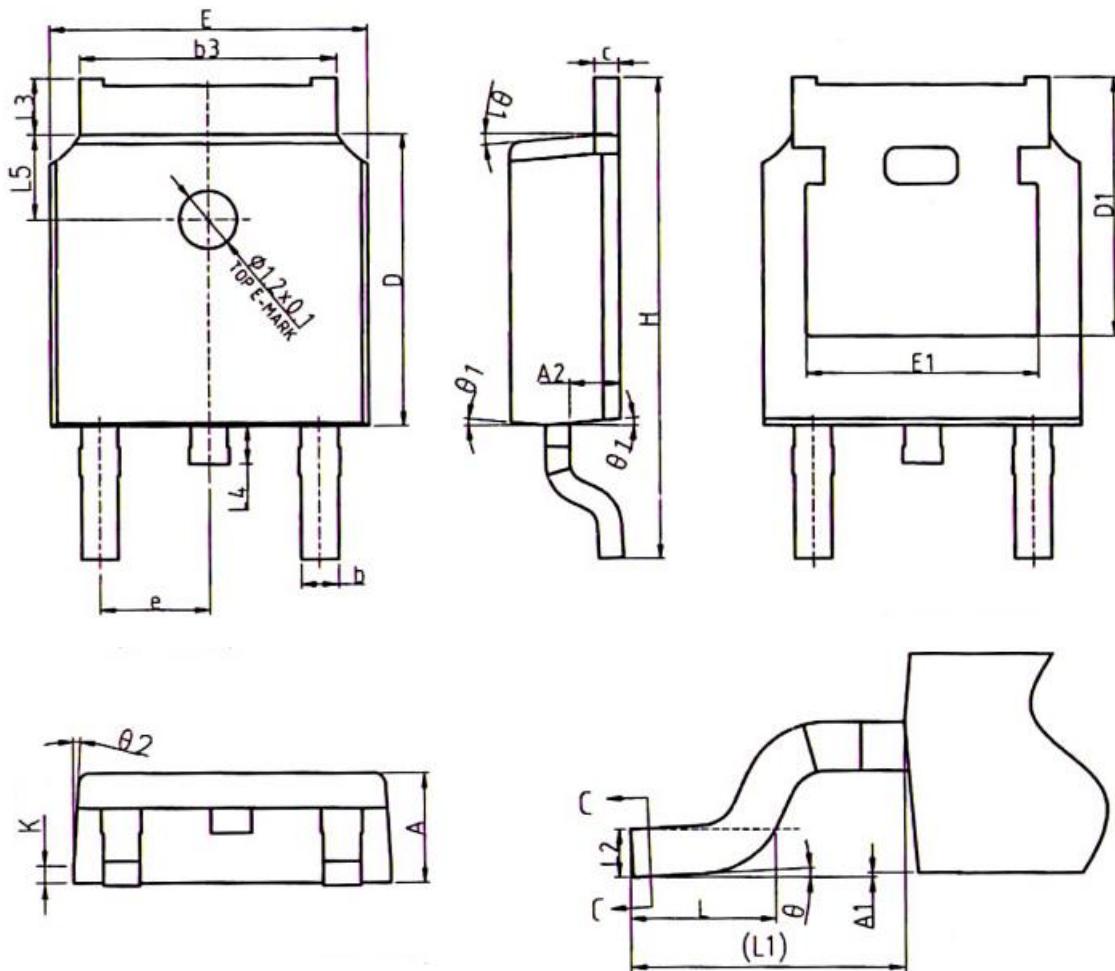


Fig.11 Unclamped Inductive Switching

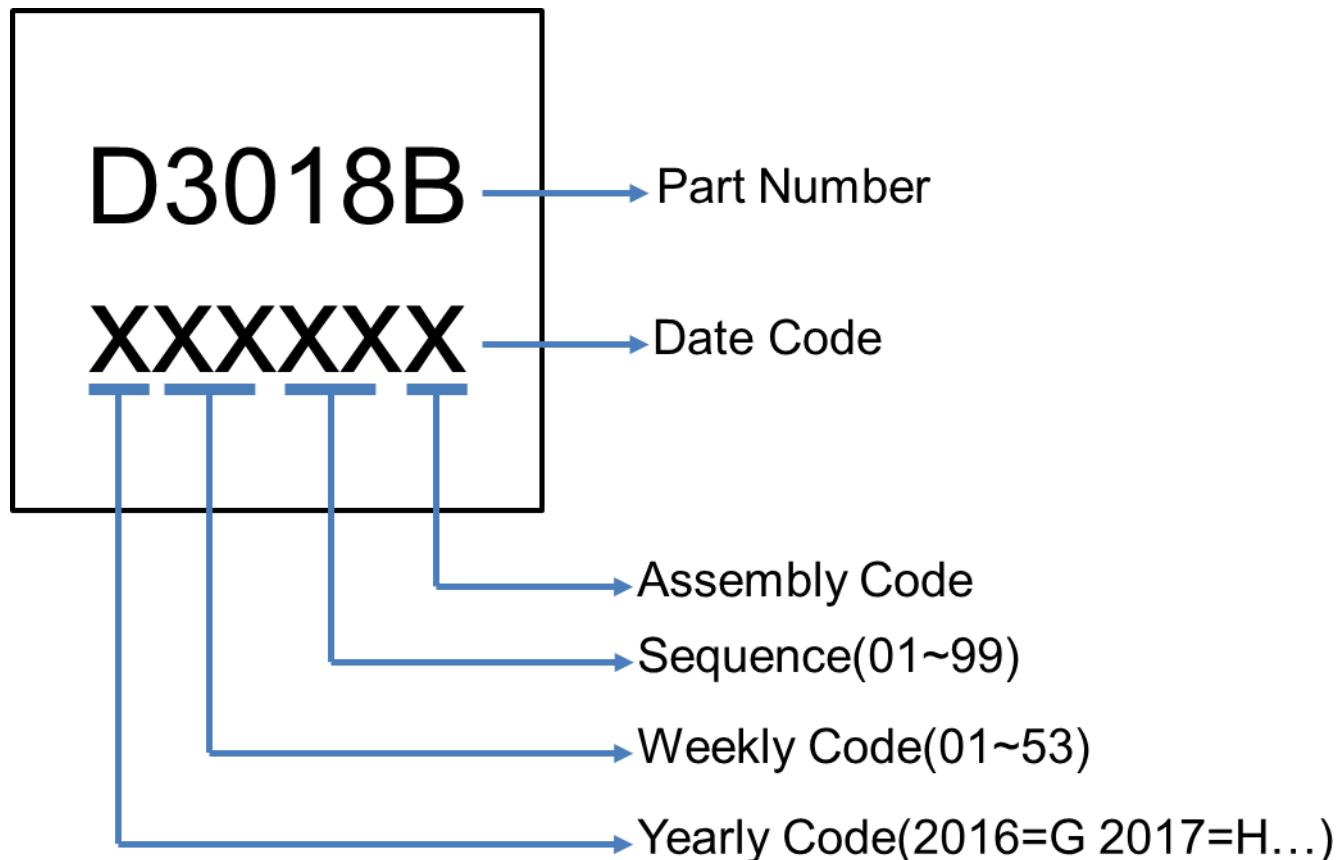


## TO252 Package Outline Dimensions



SYMBOL	MILLIMETERS			SYMBOL	MILLIMETERS		
	MIN	NOM	MAX		MIN	NOM	MAX
A	2.20	2.30	2.38	H	9.90	10.10	10.30
A1	0.00	---	0.10	L	1.40	1.50	1.70
A2	0.97	1.07	1.17	L1	2.90REF		
b	0.72	0.78	0.85	L2	0.51BSC		
b3	5.23	5.33	5.46	L3	0.90	---	1.25
c	0.47	0.53	0.58	L4	0.60	0.80	1.00
D	6.00	6.10	6.20	L5	1.70	1.80	1.90
D1	5.30REF			theta	0°	---	8°
E	6.50	6.60	6.70	theta1	5°	7°	9°
E1	4.70	4.83	4.92	theta2	5°	7°	9°
e	2.286BSC			K	0.40REF		

## TO252 TOP MARKING DEFINITION



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