



## N-Ch and P-Ch Fast Switching MOSFETs

### Description

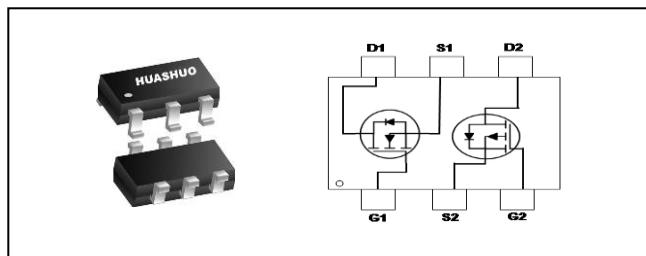
The HSW2004 uses advanced trench technology to provide excellent RDS(ON) and low gate charge. The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

- PWM applications
- Load switch

### Product Summary

| BVDSS | RDSON | ID  |
|-------|-------|-----|
| 20V   | 19mΩ  | 4A  |
| -20V  | 35mΩ  | -4A |

### SOT23-6L Pin Configuration



### Absolute Maximum Ratings

| Symbol                               | Parameter                                                      | Rating       |              | Units |
|--------------------------------------|----------------------------------------------------------------|--------------|--------------|-------|
|                                      |                                                                | N-Channel    | P-Channel    |       |
|                                      |                                                                | Steady State | Steady State |       |
| V <sub>DS</sub>                      | Drain-Source Voltage                                           | 20           | -20          | V     |
| V <sub>GS</sub>                      | Gate-Source Voltage                                            | ±12          | ±12          | V     |
| I <sub>D</sub> @T <sub>A</sub> =25°C | Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup> | 4            | -4           | A     |
| I <sub>D</sub> @T <sub>A</sub> =70°C | Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup> | 2.4          | -2.4         | A     |
| I <sub>DM</sub>                      | Pulsed Drain Current <sup>2</sup>                              | 12           | -12          | A     |
| P <sub>D</sub> @T <sub>A</sub> =25°C | Total Power Dissipation <sup>3</sup>                           | 1.4          | 1.4          | W     |
| T <sub>STG</sub>                     | Storage Temperature Range                                      | -55 to 150   | -55 to 150   | °C    |
| T <sub>J</sub>                       | Operating Junction Temperature Range                           | -55 to 150   | -55 to 150   | °C    |

### Thermal Data

| Symbol           | Parameter                                        | Typ. | Max. | Unit |
|------------------|--------------------------------------------------|------|------|------|
| R <sub>θJA</sub> | Thermal Resistance Junction-ambient <sup>1</sup> | ---  | 125  | °C/W |



**N-Ch and P-Ch Fast Switching MOSFETs**

**N-Channel Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

| Symbol                     | Parameter                                      | Conditions                                                                      | Min. | Typ. | Max.      | Unit             |
|----------------------------|------------------------------------------------|---------------------------------------------------------------------------------|------|------|-----------|------------------|
| $\text{BV}_{\text{DSS}}$   | Drain-Source Breakdown Voltage                 | $V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$                                | 20   | ---  | ---       | V                |
| $R_{\text{DS}(\text{ON})}$ | Static Drain-Source On-Resistance <sup>2</sup> | $V_{\text{GS}}=4.5\text{V}$ , $I_D=4\text{A}$                                   | ---  | 19   | 24        | $\text{m}\Omega$ |
|                            |                                                | $V_{\text{GS}}=2.5\text{V}$ , $I_D=3\text{A}$                                   | ---  | 26   | 34        |                  |
| $V_{\text{GS}(\text{th})}$ | Gate Threshold Voltage                         | $V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$                            | 0.5  | 0.8  | 1.2       | V                |
| $I_{\text{bss}}$           | Drain-Source Leakage Current                   | $V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$ | ---  | ---  | 1         | $\text{uA}$      |
|                            |                                                | $V_{\text{DS}}=16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$ | ---  | ---  | 5         |                  |
| $I_{\text{GSS}}$           | Gate-Source Leakage Current                    | $V_{\text{GS}}=\pm 12\text{V}$ , $V_{\text{DS}}=0\text{V}$                      | ---  | ---  | $\pm 100$ | nA               |
| $g_{\text{fs}}$            | Forward Transconductance                       | $V_{\text{DS}}=5\text{V}$ , $I_D=4\text{A}$                                     | ---  | 10   | ---       | S                |
| $Q_g$                      | Total Gate Charge (4.5V)                       | $V_{\text{DS}}=10\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_D=4\text{A}$      | ---  | 10   | ---       | $\text{nC}$      |
| $Q_{\text{gs}}$            | Gate-Source Charge                             |                                                                                 | ---  | 2.4  | ---       |                  |
| $Q_{\text{gd}}$            | Gate-Drain Charge                              |                                                                                 | ---  | 2.9  | ---       |                  |
| $T_{\text{d}(\text{on})}$  | Turn-On Delay Time                             | $V_{\text{DD}}=10\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $R_G=6\Omega$        | ---  | 13   | ---       | $\text{ns}$      |
| $T_r$                      | Rise Time                                      |                                                                                 | ---  | 31   | ---       |                  |
| $T_{\text{d}(\text{off})}$ | Turn-Off Delay Time                            |                                                                                 | ---  | 34   | ---       |                  |
| $T_f$                      | Fall Time                                      |                                                                                 | ---  | 12   | ---       |                  |
| $C_{\text{iss}}$           | Input Capacitance                              | $V_{\text{DS}}=10\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$        | ---  | 500  | ---       | $\text{pF}$      |
| $C_{\text{oss}}$           | Output Capacitance                             |                                                                                 | ---  | 299  | ---       |                  |
| $C_{\text{rss}}$           | Reverse Transfer Capacitance                   |                                                                                 | ---  | 93   | ---       |                  |

**Diode Characteristics**

| Symbol          | Parameter                          | Conditions                                                           | Min. | Typ. | Max. | Unit |
|-----------------|------------------------------------|----------------------------------------------------------------------|------|------|------|------|
| $V_{\text{SD}}$ | Diode Forward Voltage <sup>2</sup> | $V_{\text{GS}}=0\text{V}$ , $I_S=1\text{A}$ , $T_J=25^\circ\text{C}$ | ---  | ---  | 1.2  | V    |

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.



## N-Ch and P-Ch Fast Switching MOSFETs

### N-Channel Typical Characteristics

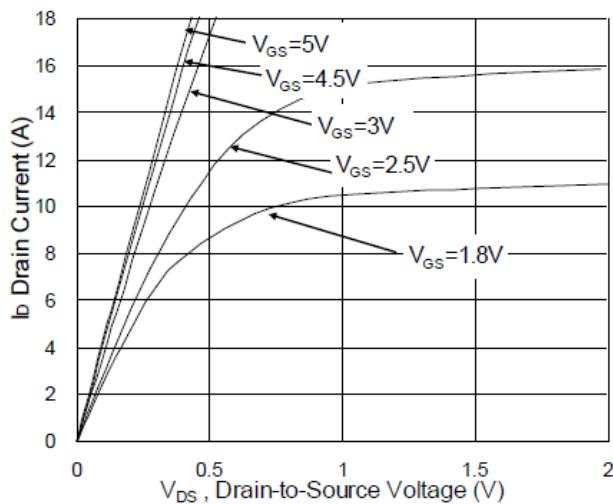


Fig.1 Typical Output Characteristics

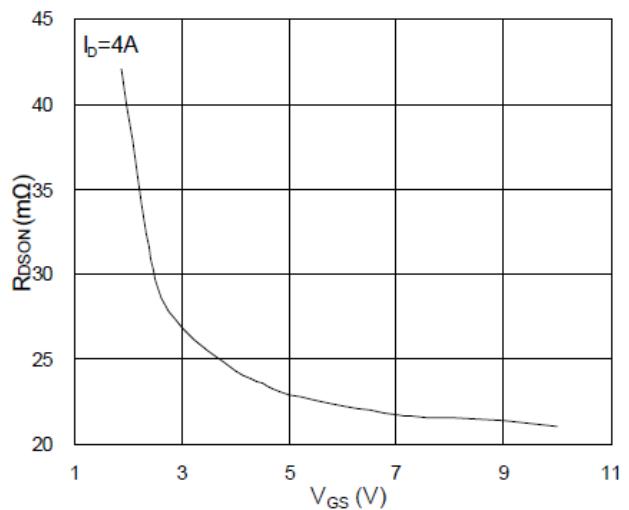


Fig.2 On-Resistance vs G-S Voltage

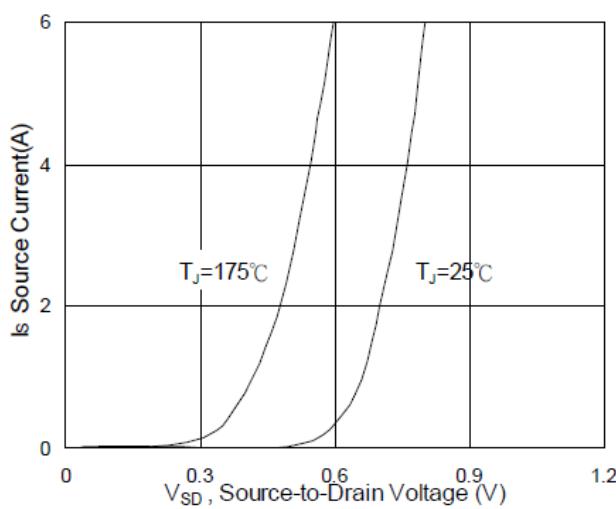


Fig.3 Source Drain Forward Characteristics

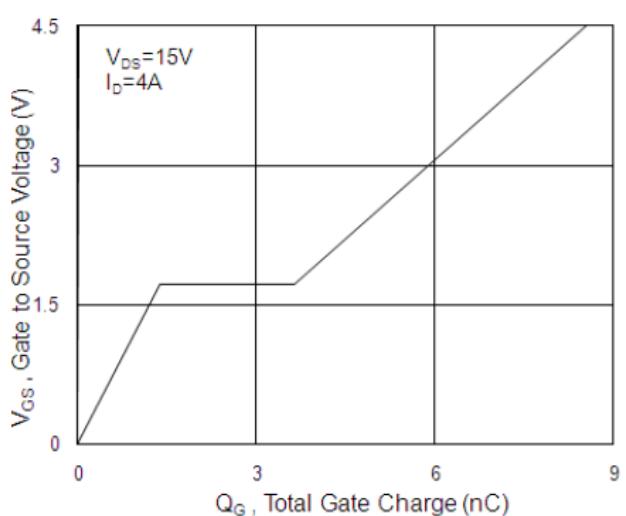


Fig.4 Gate-Charge Characteristics

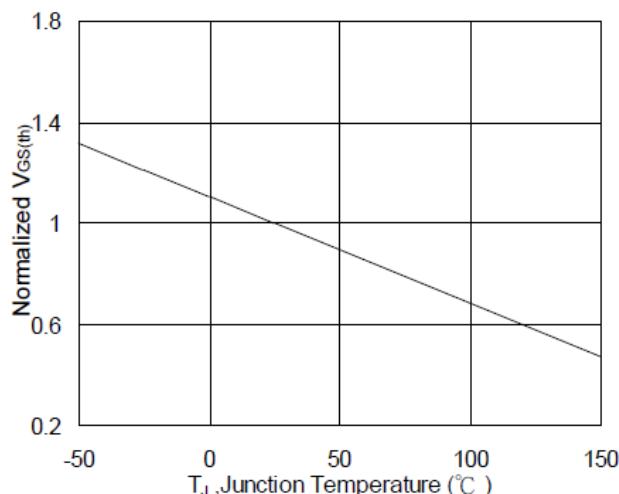


Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$

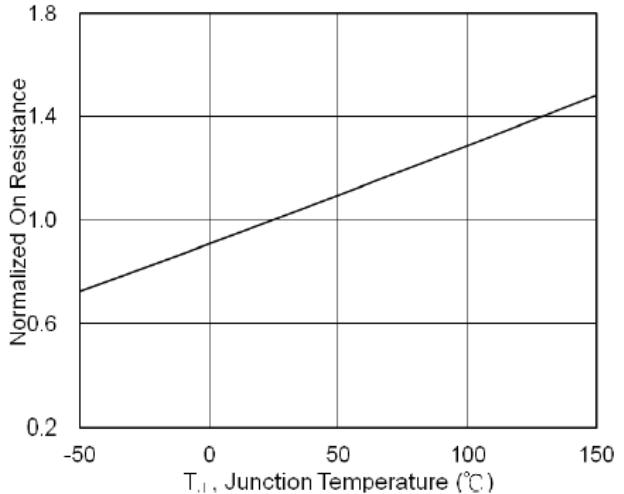


Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$



### N-Ch and P-Ch Fast Switching MOSFETs

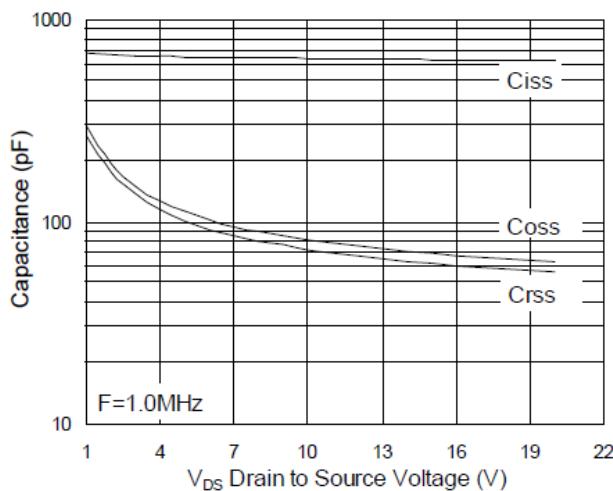


Fig.7 Capacitance

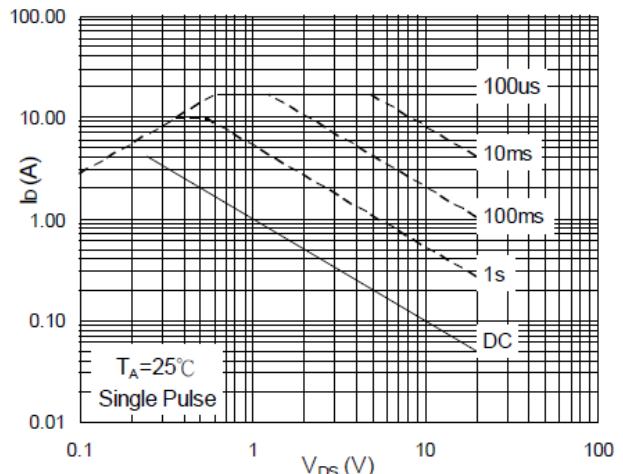


Fig.8 Safe Operating Area

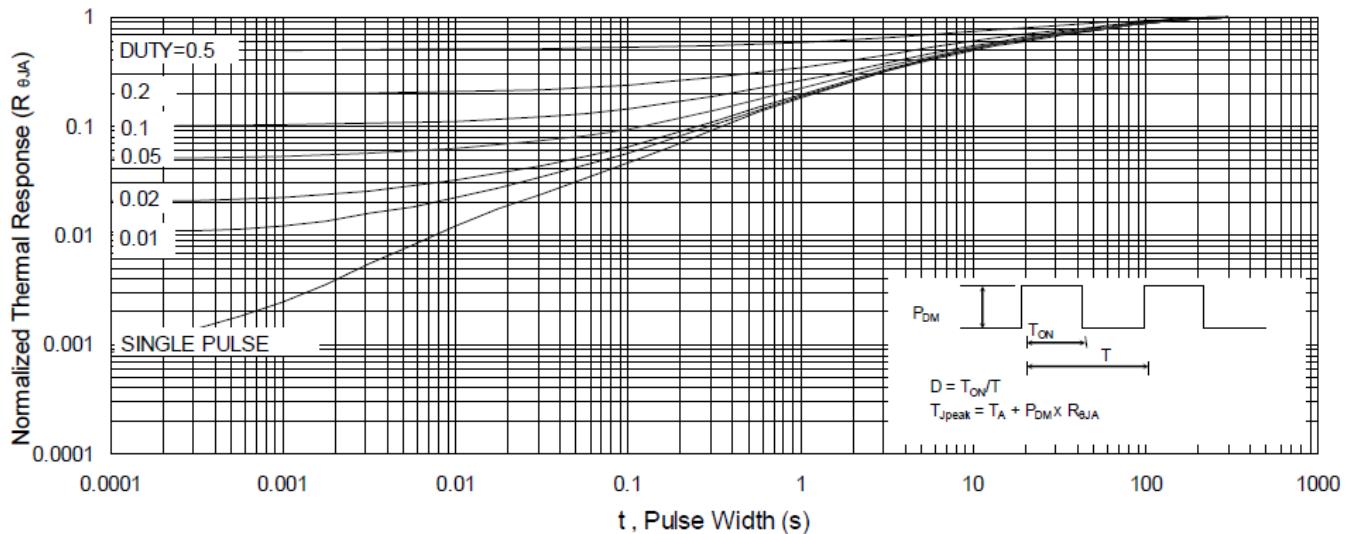


Fig.9 Normalized Maximum Transient Thermal Impedance

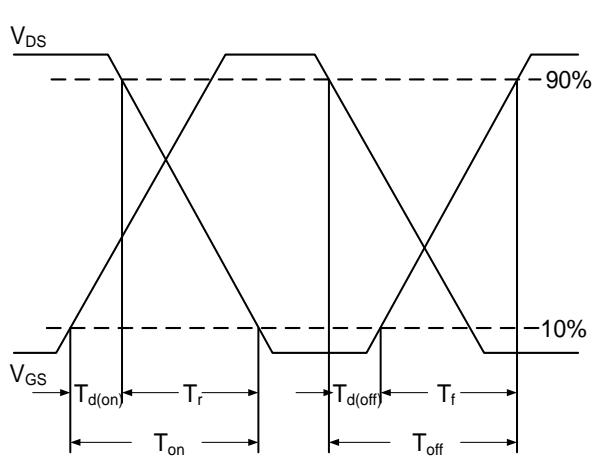


Fig.10 Switching Time Waveform

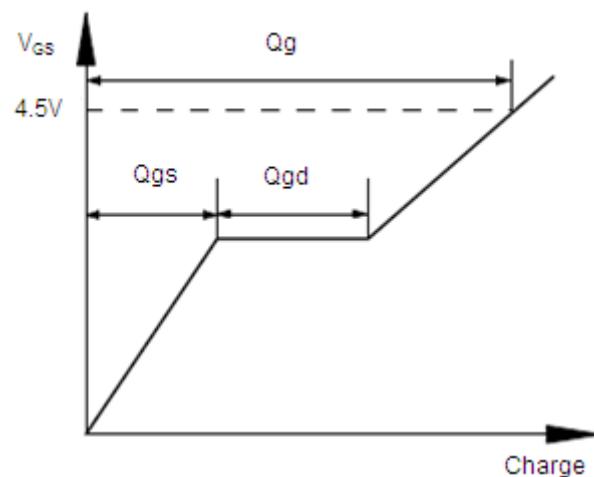


Fig.11 Gate Charge Waveform



**N-Ch and P-Ch Fast Switching MOSFETs**

**P-Channel Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)**

| Symbol                   | Parameter                                      | Conditions                                                                                    | Min. | Typ. | Max.      | Unit             |
|--------------------------|------------------------------------------------|-----------------------------------------------------------------------------------------------|------|------|-----------|------------------|
| $\text{BV}_{\text{DSS}}$ | Drain-Source Breakdown Voltage                 | $V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$                                             | -20  | ---  | ---       | V                |
| $R_{\text{DS(ON)}}$      | Static Drain-Source On-Resistance <sup>2</sup> | $V_{\text{GS}}=-4.5\text{V}$ , $I_D=-4\text{A}$                                               | ---  | 35   | 45        | $\text{m}\Omega$ |
|                          |                                                | $V_{\text{GS}}=-2.5\text{V}$ , $I_D=-3\text{A}$                                               | ---  | 50   | 65        |                  |
| $V_{\text{GS(th)}}$      | Gate Threshold Voltage                         | $V_{\text{GS}}=V_{\text{DS}}$ , $I_D=-250\mu\text{A}$                                         | -0.4 | -0.7 | -1.0      | V                |
| $I_{\text{bss}}$         | Drain-Source Leakage Current                   | $V_{\text{DS}}=-16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^{\circ}\text{C}$            | ---  | ---  | -1        | $\text{uA}$      |
|                          |                                                | $V_{\text{DS}}=-16\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^{\circ}\text{C}$            | ---  | ---  | -5        |                  |
| $I_{\text{GSS}}$         | Gate-Source Leakage Current                    | $V_{\text{GS}}=\pm 12\text{V}$ , $V_{\text{DS}}=0\text{V}$                                    | ---  | ---  | $\pm 100$ | nA               |
| $g_{\text{fs}}$          | Forward Transconductance                       | $V_{\text{DS}}=-5\text{V}$ , $I_D=4\text{A}$                                                  | ---  | 5    | ---       | S                |
| $Q_g$                    | Total Gate Charge (-4.5V)                      | $V_{\text{DS}}=-10\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $I_D=-4\text{A}$                 | ---  | 6.9  | ---       | $\text{nC}$      |
| $Q_{\text{gs}}$          | Gate-Source Charge                             |                                                                                               | ---  | 1.2  | ---       |                  |
| $Q_{\text{gd}}$          | Gate-Drain Charge                              |                                                                                               | ---  | 1.5  | ---       |                  |
| $T_{\text{d(on)}}$       | Turn-On Delay Time                             | $V_{\text{DD}}=-10\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $R_G=6\Omega$<br>$I_D=4\text{A}$ | ---  | 12   | ---       | $\text{ns}$      |
| $T_r$                    | Rise Time                                      |                                                                                               | ---  | 34   | ---       |                  |
| $T_{\text{d(off)}}$      | Turn-Off Delay Time                            |                                                                                               | ---  | 33   | ---       |                  |
| $T_f$                    | Fall Time                                      |                                                                                               | ---  | 6    | ---       |                  |
| $C_{\text{iss}}$         | Input Capacitance                              | $V_{\text{DS}}=-10\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$                     | ---  | 661  | ---       | $\text{pF}$      |
| $C_{\text{oss}}$         | Output Capacitance                             |                                                                                               | ---  | 101  | ---       |                  |
| $C_{\text{rss}}$         | Reverse Transfer Capacitance                   |                                                                                               | ---  | 92   | ---       |                  |

**Diode Characteristics**

| Symbol          | Parameter                          | Conditions                                                              | Min. | Typ.  | Max. | Unit |
|-----------------|------------------------------------|-------------------------------------------------------------------------|------|-------|------|------|
| $V_{\text{SD}}$ | Diode Forward Voltage <sup>2</sup> | $V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25^{\circ}\text{C}$ | ---  | -0.81 | -1.2 | V    |

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.



### P-Channel Typical Characteristics

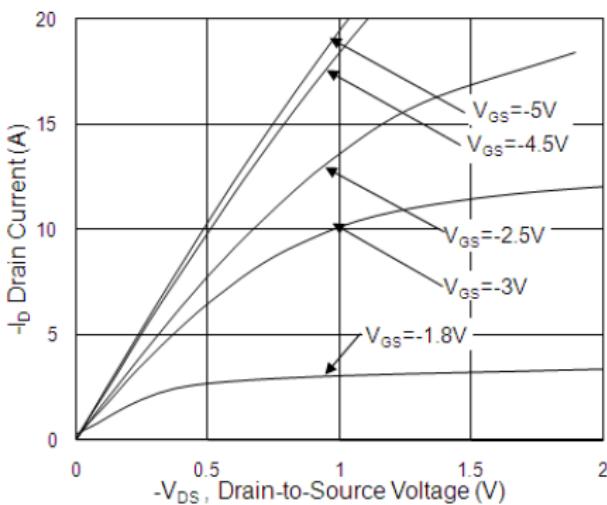


Fig.1 Typical Output Characteristics

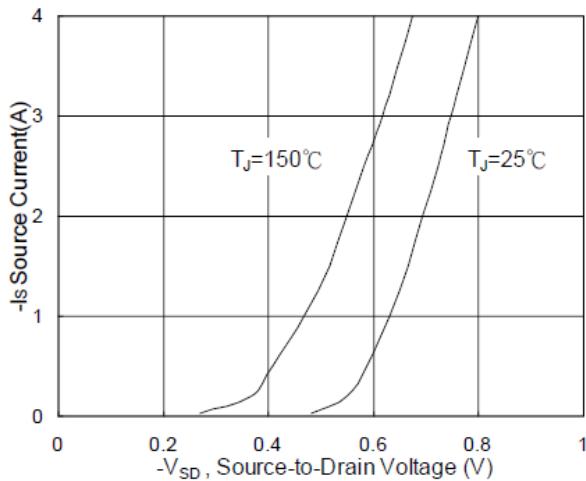


Fig.3 Source Drain Forward Characteristics

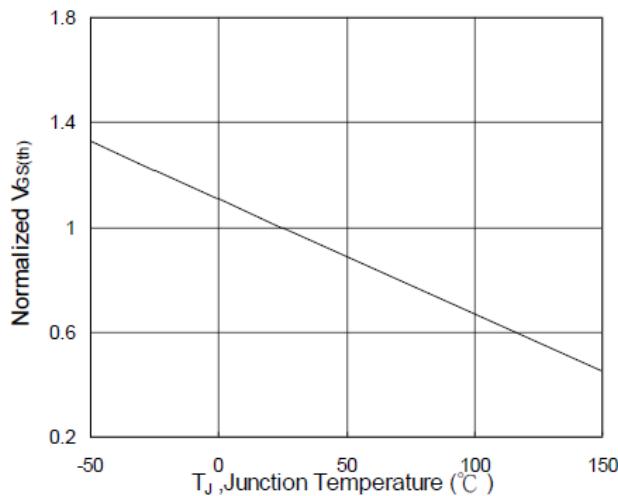


Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$

### N-Ch and P-Ch Fast Switching MOSFETs

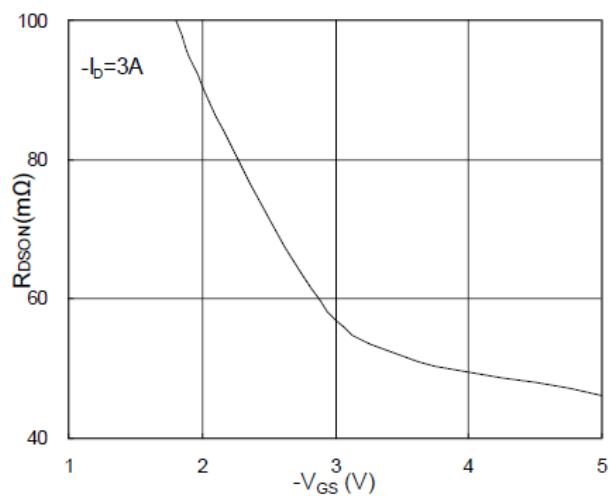


Fig.2 On-Resistance vs G-S Voltage

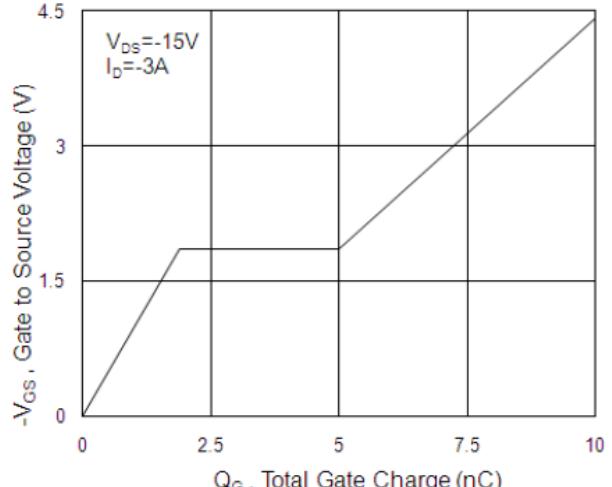


Fig.4 Gate-Charge Characteristics

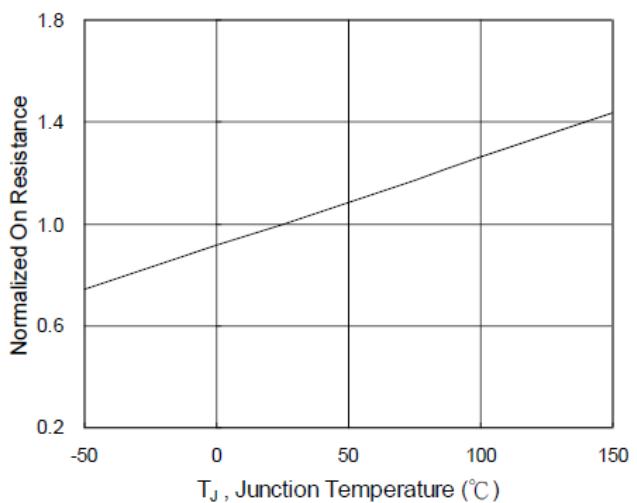


Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$



### N-Ch and P-Ch Fast Switching MOSFETs

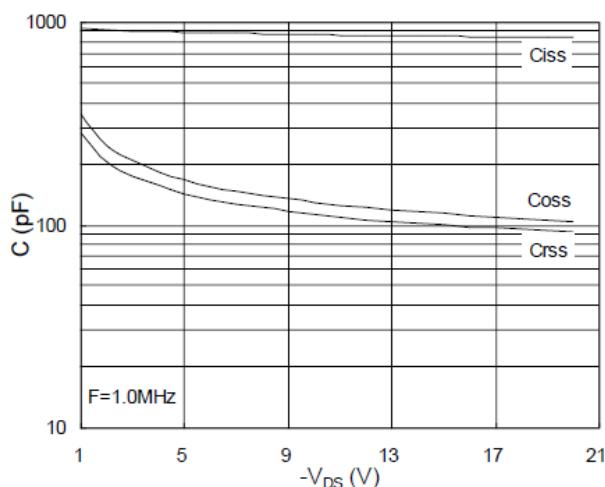


Fig.7 Capacitance

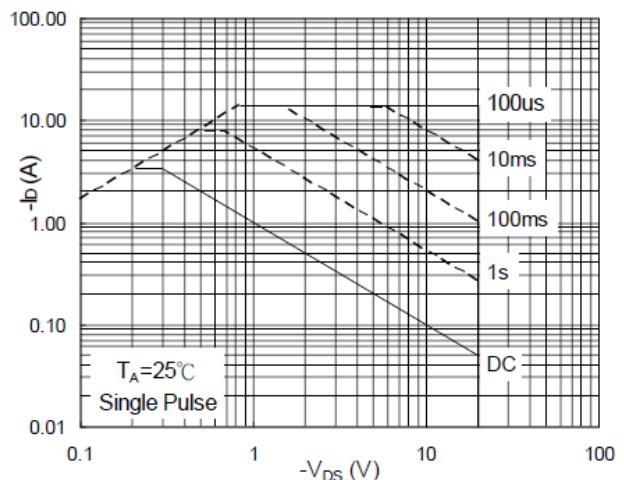


Fig.8 Safe Operating Area

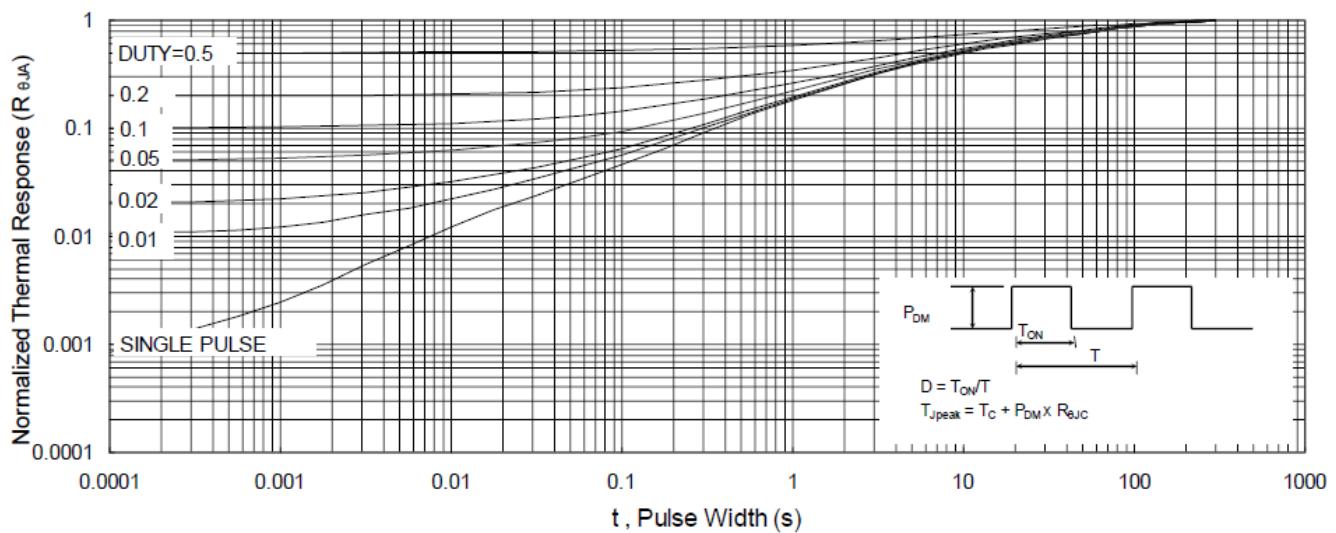


Fig.9 Normalized Maximum Transient Thermal Impedance

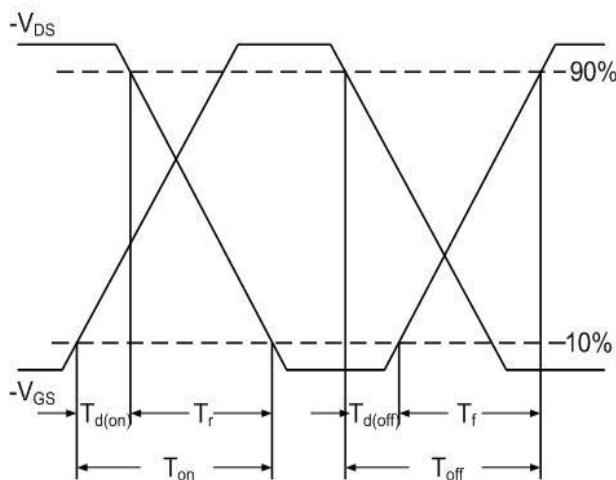


Fig.10 Switching Time Waveform

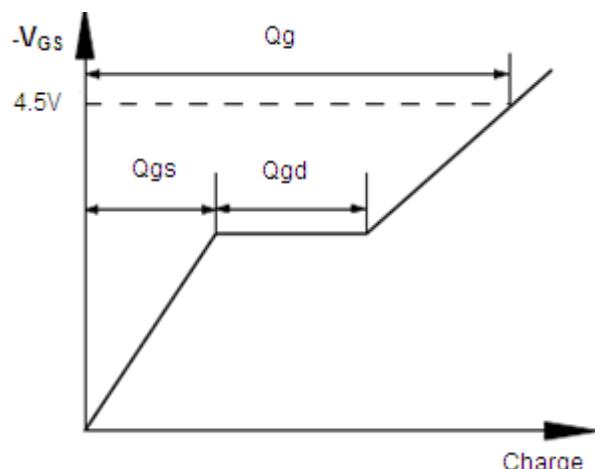
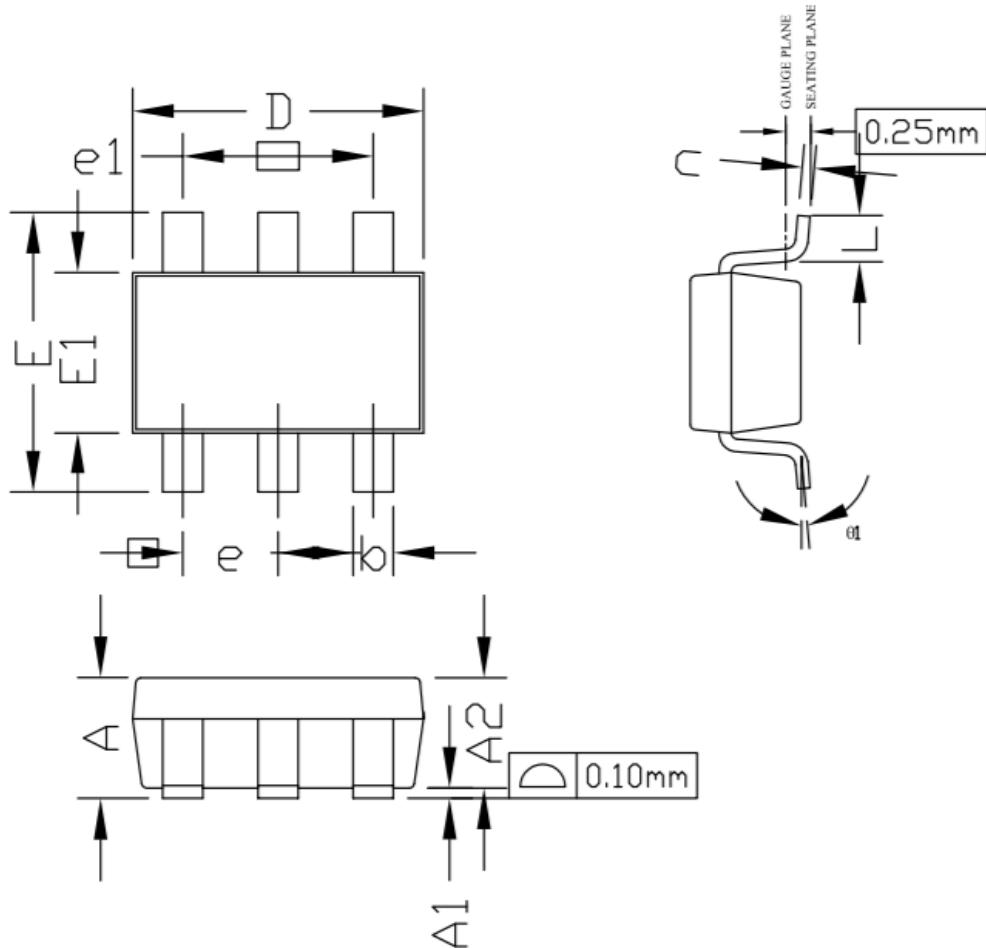


Fig.11 Gate Charge Waveform

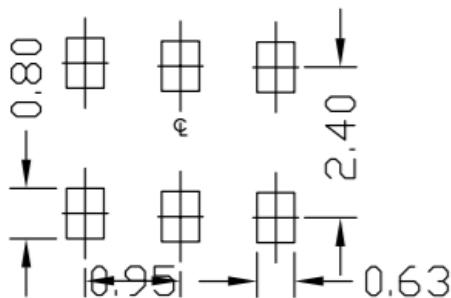


N-Ch and P-Ch Fast Switching MOSFETs

## SOT23-6 Package Outline Dimensions



RECOMMENDED LAND PATTERN



UNIT: mm

| SYMBOLS | DIMENSIONS IN MILLIMETERS |      |      | DIMENSIONS IN INCHES |       |       |
|---------|---------------------------|------|------|----------------------|-------|-------|
|         | MIN                       | NOM  | MAX  | MIN                  | NOM   | MAX   |
| A       | 0.80                      | —    | 1.25 | 0.031                | —     | 0.049 |
| A1      | 0.00                      | —    | 0.15 | 0.000                | —     | 0.006 |
| A2      | 0.70                      | 1.10 | 1.20 | 0.028                | 0.043 | 0.047 |
| b       | 0.30                      | 0.40 | 0.50 | 0.012                | 0.016 | 0.020 |
| c       | 0.08                      | 0.13 | 0.20 | 0.003                | 0.005 | 0.008 |
| D       | 2.70                      | 2.90 | 3.10 | 0.106                | 0.114 | 0.122 |
| E       | 2.50                      | 2.80 | 3.10 | 0.098                | 0.110 | 0.122 |
| E1      | 1.50                      | 1.60 | 1.70 | 0.059                | 0.063 | 0.067 |
| e       | 0.95 BSC.                 |      |      | 0.037BSC.            |       |       |
| el      | 1.90 BSC.                 |      |      | 0.075 BSC.           |       |       |
| L       | 0.30                      | —    | 0.60 | 0.012                | —     | 0.024 |
| theta1  | 0°                        | —    | 8°   | 0°                   | —     | 8°    |

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[TK16J60W,S1VQ\(O](#) [2SK2614\(TE16L1,Q\)](#) [DMN1017UCP3-7](#) [DMN1053UCP4-7](#) [SQJ469EP-T1-GE3](#) [NTE2384](#) [DMC2700UDMQ-7](#)  
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