

U18General-Purpose Hall Effect Latch Effect Latch

U18, Hall-Effect sensor, designed for electronic commutation of brush-less DC motor applications. The device includes an on-chip Hall voltage generator for magnetic sensing, a comparator that amplifies the Hall Voltage, and a Schmitt trigger to provide switching hysteresis for noise rejection, and open collector output. An internal band gap regulator is used to provide temperature compensated supply voltage for internal circuits and allows a wide operating supply range. The device is identical except for magnetic switch points.

The device includes on a single silicon chip a voltage regulator, Hall-voltage generator, small-signal amplifier, chopper stabilization, Schmitt trigger, open-collector output to sink up to 25mA. A south pole of sufficient strength will turn the output on. The North Pole is necessary to turn the output off. An on-board regulator permits operation with supply voltages of 3.5V to 20 V.

The package type is in a lead Halogen Free version was verified by third party organization.

Features and Benefits

- Temperature compensation.
- Wide operating voltage range.
- Open-Collector pre-driver.
- Reverse bias protection on power supply pin.

Applications

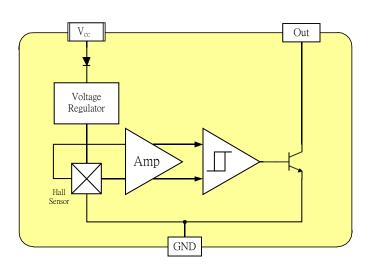
- High temperature Hall IC application
- Fan motor application
- BLDC motor application

Ordering number

Part No.	Temperature Suffix	Package Type	
U18KUA	$(-40^{\circ}\text{C to} + 125^{\circ}\text{C})$	(TO-92S)	

Functional Diagram

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Absolute Maximum Ratings At (Ta=25°C)

Character	,	Values	Unit	
Supply voltage, (Vcc)		20	V	
Output Voltage,(Vout)		30	V	
Reverse voltage, (Vcc) (V	ουτ)	-20	V	
Magnetic flux density		Unlimited	Gauss	
Output current, (Isink)		25	mA	
Operating Temperature	Range, "E" vers	ion -20 to +85	$^{\circ}\mathbb{C}$	
(Ta)				
Storage temperature rang	je, (<i>T</i> s)	-55 to +150	$^{\circ}$ C	
Maximum Junction Temp	, (<i>Tj</i>)	150	\mathbb{C}	
Thermal Resistance	$(heta_{\!\scriptscriptstyle ja})$ UA	206	°C/W	
memiai Nesistance	$(oldsymbol{ heta}_{jc})$ UA	148	°C/W	
Package Power Dissipation, (P₂) UA		606	mW	

Note: Do not apply reverse voltage to V_{CC} and V_{OUT} Pin, It may be caused for Miss function or damaged device.

Electrical Specifications

DC Operating Parameters: Ta=+25°C, Vcc=12V

Parameters	Test Conditions	Min	Тур	Max	Units
Supply Voltage,(Vcc)	Operating	3.5		20.0	V
Supply Current,(Icc)	B <b<sub>OP</b<sub>		4.0	8.0	mA
Output Saturation Voltage,(V _{Sat})	I _{OUT} = 10 mA, B>B _{OP}			700.0	mV
Output Leakage Current,(Ioff)	IOFF B <brp, vout="12V</td"><td></td><td></td><td>10.0</td><td>uA</td></brp,>			10.0	uA
Output Rise Time,(<i>T</i> _R)	RL=820 Ω , CL =20pF			1.5	uS
Output Fall Time, (T_F)	RL=820 Ω; CL =20pF			1.5	uS

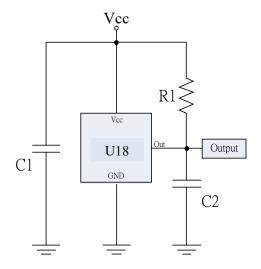
U18EUA Magnetic Specifications

DC Operating Parameters: T_A =+25 C, V_{CC} =12V

Parameter	Symbol	Test condition	Min	Тур	Max	Unit
Operate Point	Вор	UA	5(-60)		60(-5)	Gauss
Release Point	Brp	UA	-60 (5)		-5 (60)	Gauss
Hysteresis	B _{HYS}			60		Gauss



Typical application circuit



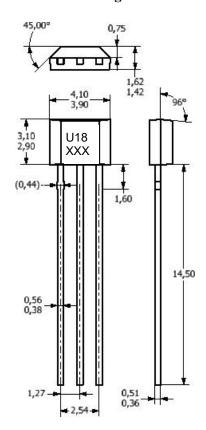
C1: 1000PF

C2: 15PF

R1: $10K\Omega$

Sensor Location, Package Dimension and Marking U18EUA Package

UA Package



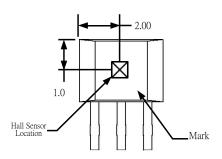
NOTES:

- 1).Controlling dimension: mm
- 2).Leads must be free of flash and plating voids
- Do not bend leads within 1 mm of lead to package interface.
- 4).PINOUT:

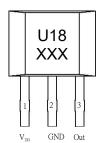
Pin 1 Vcc Pin 2 GND

Pin 3 Output

Hall Chip location



Output Pin Assignment (Top view)



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