

# Description

The 16N65F can be used in various power swithching circuit for system miniaturization and higher efficiency.The package form is TO-220/ TO-220F, which accords with the RoHS standard.

#### **General Features**

 $V_{DS} = 650V, I_D = 20A$  $R_{DS(ON)} < 0.47\Omega@V_{GS} = 10V$ 

## Application

• Power switch circuit of adaptor and charger.

### Package Marking and Ordering Information

Product ID	Pack	Marking	Units Tube
16N65F	TO-220F	16N65 XXX YYYY	50

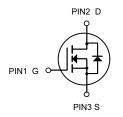
#### Absolute Maximum Ratings@T =25°C(unless otherwise specified)

Symbol	Parameter	Lmit	Unit	
V <sub>DSS</sub>	Drain-to-Source Voltage <sup>[1]</sup>	650	V	
V <sub>GSS</sub>	Gate-to-Source Voltage	±30		
ID	Continuous Drain Current	16	A	
I <sub>D@Tc=100</sub> ℃	Continuous Drain Current @ Tc=100℃	10		
I <sub>DM</sub>	Pulsed Drain Current at V <sub>GS</sub> =10V <sup>[2]</sup>	64		
E <sub>AS</sub>	Single Pulse Avalanche Energy	845	mJ	
P <sub>D</sub>	Power Dissipation	34	W	
T <sub>L</sub> T <sub>PAK</sub>	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10 seconds, Package Body for 10 seconds	300 260	°C	
T <sub>J</sub> & T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to 150		
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case 3.7		°C AA4	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient	52	°C∕₩	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.



TO-220F



N-Channel MOSFET



Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	650	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 650 V, V_{GS} = 0 V$	-	-	1.0	μΑ
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 30V$	-	-	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2	3	4	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(4)</sup>	$V_{GS} = 10V, I_D = 8A$	-	0.48	0.62	Ω
$C_{iss}$	Input Capacitance		-	2747	-	pF
C <sub>oss</sub>	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-	224	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	27	-	pF
$Q_g$	Total Gate Charge		-	62	-	nC
$Q_{gs}$	Gate Source Charge	$V_{GS} = 0$ to 10V $V_{DS} = 520V, I_{D} = 16A$	-	14	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge		-	24	-	nC
t <sub>d(on)</sub>	Turn-On DelayTime		-	38	-	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 310V$	-	52	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime	$I_D$ = 16A, $R_{GEN}$ = 24 $\Omega$	-	176	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	68	-	ns
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	16	А
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	64	А
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 16A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	476	-	ns
Qrr	Body Diode Reverse Recovery Charge	I <sub>F</sub> = 16A, di/dt = 100A/us	-	6.9	-	μC

# **Electrical Characteristics** $T_J = 25^{\circ}C$ unless otherwise specified

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

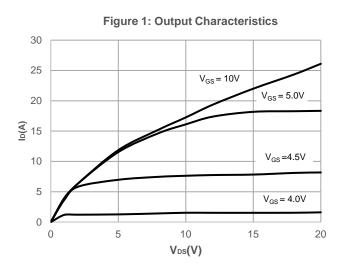
2. E\_{AS} condition: Starting T\_J=25C, V\_{DD}=50V, V\_G=10V, R\_G=25ohm, L=10mH, I\_{AS}=13A

3.  $R_{\theta JA}$  is measured with the device mounted on a minimum recommended pad of 2oz copper FR4 PCB

4. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.



# **Typical Characteristics**



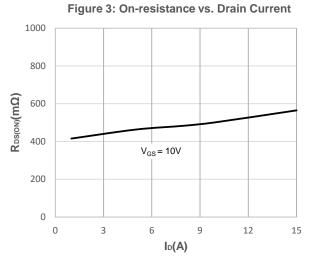
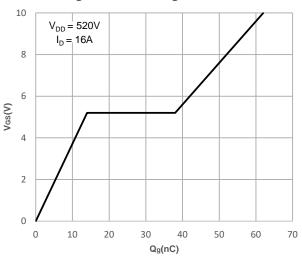
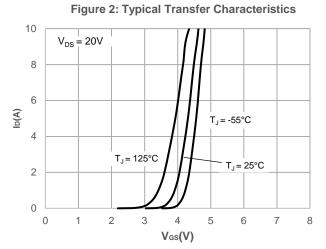
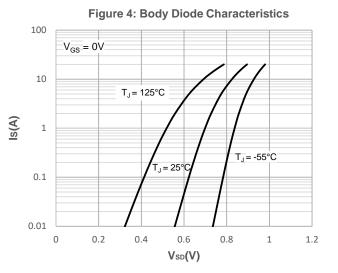


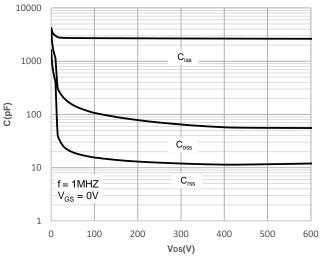
Figure 5: Gate Charge Characteristics



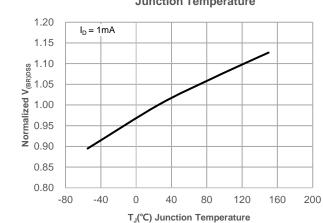






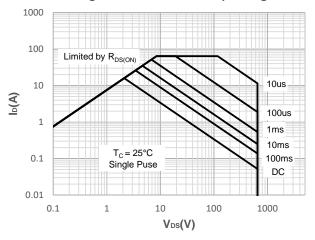


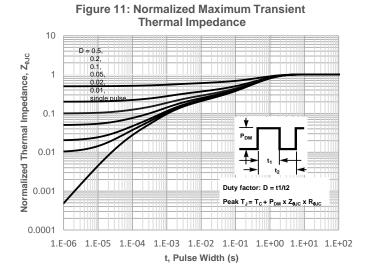


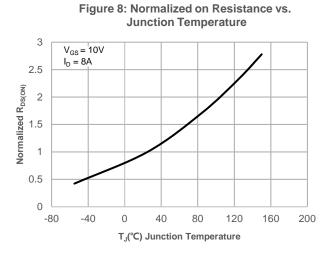


# Figure 7: Normalized Breakdown voltage vs.

Figure 9: Maximum Safe Operating Area







#### Figure 10: Maximum Continuous Drian Current vs. Case Temperature

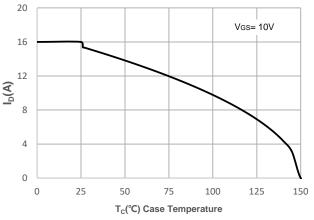
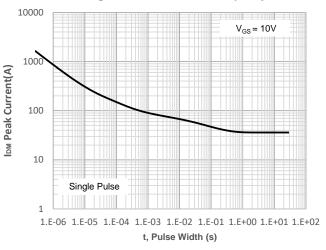


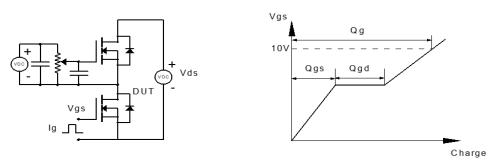
Figure 12: Peak Current Capacity



# **Junction Temperature**



# **Test Circuit**





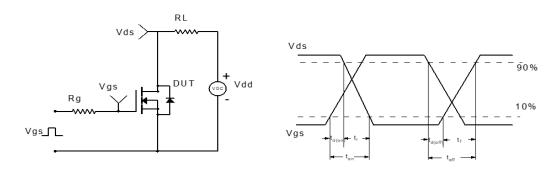


Figure 2: Resistive Switching Test Circuit & Waveform

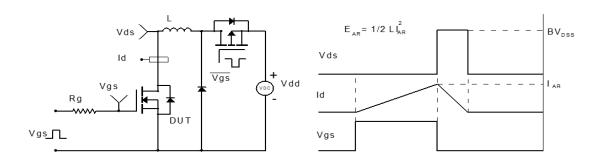
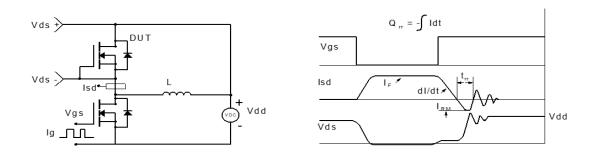


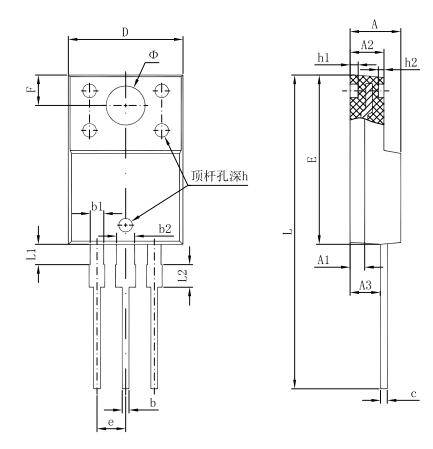
Figure 3: Unclamped Inductive Switching Test Circuit& Waveform



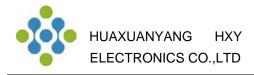




# Package Dimension TO-220F



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A	4.300	4.700	0.169	0.185	
A1	1.300	REF.	0.051 REF.		
A2	2.800	3.200	0.110	0.126	
A3	2.500	2.900	0.098	0.114	
b	0.500	0.750	0.020	0.030	
b1	1.100	1.350	0.043	0.053	
b2	1.500	1.750	0.059	0.069	
С	0.500	0.750	0.020	0.030	
D	9.960	10.360	0.392	0.408	
E	14.800	15.200	0.583	0.598	
е	2.540 TYP.		0.100 TYP.		
F	2.700 REF.		0.106 REF.		
Φ	3.500 REF.		0.138 REF.		
h	0.000	0.300	0.000	0.012	
h1	0.800 REF.		0.031 REF.		
h2	0.500 REF.		0.020 REF.		
L	28.000	28.400	1.102	1.118	
L1	1.700	1.900	0.067	0.075	
L2	1.900	2.100	0.075	0.083	



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