



### Description

The 1N65G uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### General Features

$V_{DS} = 650V$   $I_D = 1A$

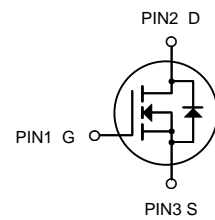
$R_{DS(ON)} < 12\Omega$  @  $V_{GS} = 10V$

### Application

- Battery protection
- Load switch
- Uninterruptible power supply



SOT-223



N-Channel MOSFET

### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
1N65G	SOT-223	1N65 XXXX	4000

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	1	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	4.8	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation <sup>4</sup>	1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	62.5	$^\circ C/W$



### Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	650	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA	---	0.057	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =0.6A	---	9.5	12	Ω
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2	---	4	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-5.68	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =650V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =650V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C	---	---	5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =± 30V , V <sub>DS</sub> =0V	---	---	± 100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =0.5A	---	35	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz	---	1.7	---	Ω
Q <sub>g</sub>	Total Gate Charge (4.5V)	V <sub>DS</sub> =520V , V <sub>GS</sub> =10V , I <sub>b</sub> =1A	---	4	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	0.9	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	2.5	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =325V , V <sub>GS</sub> =10V , R <sub>G</sub> =50 Ω , I <sub>D</sub> =1A	---	4	---	ns
T <sub>r</sub>	Rise Time		---	24	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	6	---	
T <sub>f</sub>	Fall Time		---	24	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz	---	119	---	pF
C <sub>oss</sub>	Output Capacitance		---	19	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	2	---	
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	1	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,5</sup>		---	---	4.8	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C	---	---	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =15A , dI/dt=100A/μs , T <sub>J</sub> =25°C	---	160	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	0.3	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=28A
- 4.The power dissipation is limited by 150°C junction temperature 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation



### Typical Characteristics

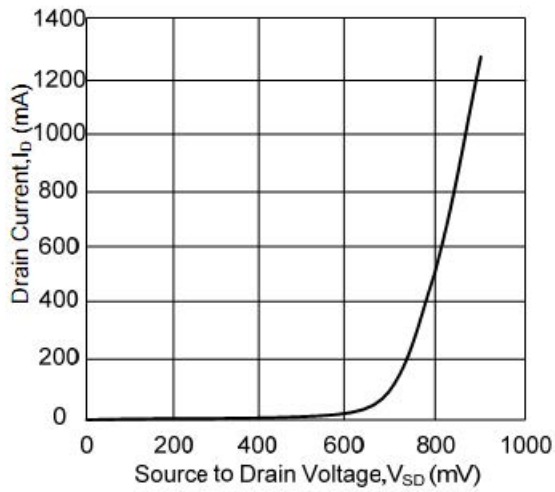


Fig1. Drain Current vs. Source to Drain Voltage

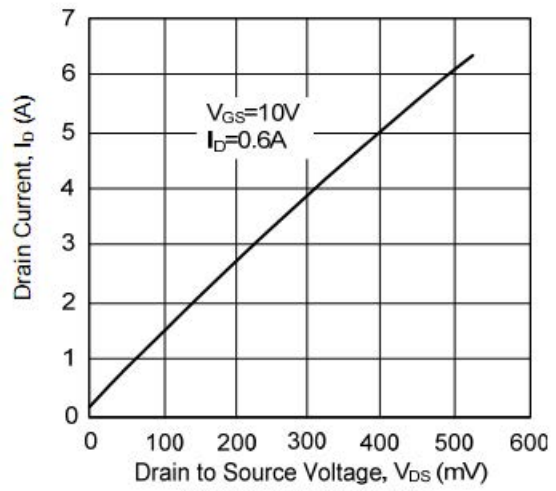


Fig2. Resistance Characteristics

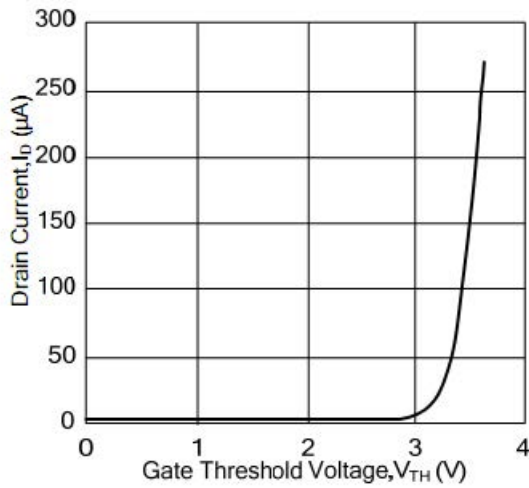


Fig3. Drain Current vs. Gate Threshold Voltage

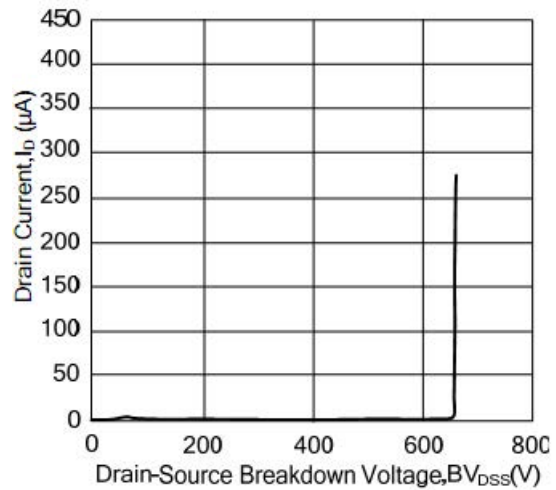
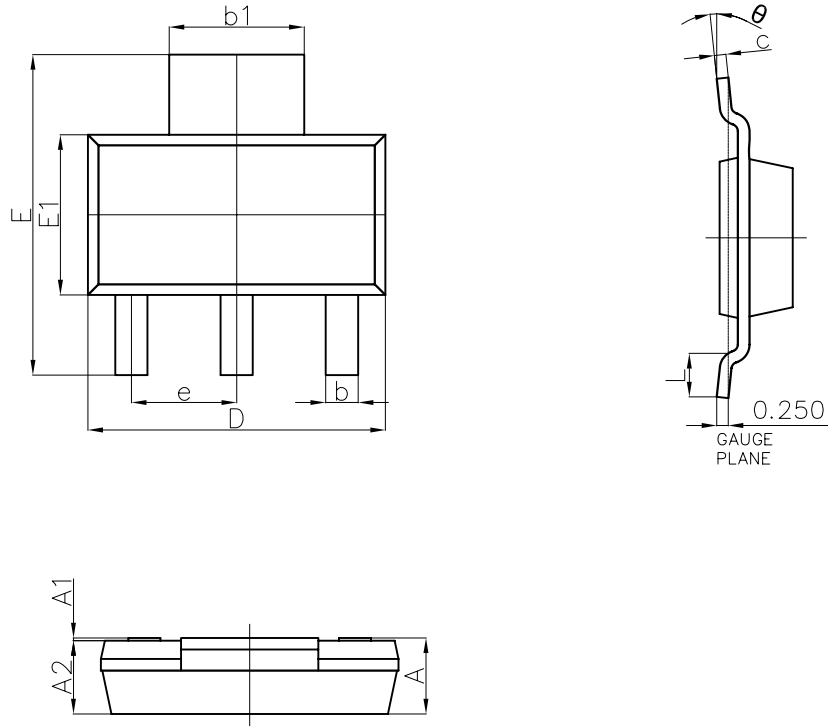


Fig4. Drain-Source Breakdown Voltage



**SOT-223 Package Outline Dimensions**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	—	1.800	—	0.071
A1	0.020	0.100	0.001	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.840	0.026	0.033
b1	2.900	3.100	0.114	0.122
c	0.230	0.350	0.009	0.014
D	6.300	6.700	0.248	0.264
E	6.700	7.300	0.264	0.287
E1	3.300	3.700	0.130	0.146
e	2.300(BSC)		0.091(BSC)	
L	0.750	—	0.030	—
theta	0°	10°	0°	10°



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