

## **Description**

The CSD17578Q3A uses advanced trench technology

to provide excellent  $R_{DS(ON)}$ , low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

### **General Features**

 $V_{DS} = 30V I_{D} = 60 A$ 

 $R_{DS(ON)}$  < 8 m $\Omega$  @  $V_{GS}$ =10V

## **Application**

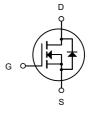
**Battery protection** 

Load switch

Uninterruptible power supply



DFN3X3-8L



N-Channel MOSFET

## **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
CSD17578Q3A	DFN3X3-8L	HXY MOSFET	5000

## Absolute Maximum Ratings (TC=25°C unless otherwise specified)

Symbol	Parameter	Rating	Units
V <sub>D</sub> s	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	60	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	20	Α
Іом	Pulsed Drain Current <sup>2</sup>	140	Α
EAS	Single Pulse Avalanche Energy <sup>3</sup>	115.2	mJ
las	Avalanche Current	48	Α
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	59	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R <sub>θ</sub> JA	Thermal Resistance Junction-ambient <sup>1</sup> 62		°C/W
R <sub>θ</sub> Jc	Thermal Resistance Junction-Case <sup>1</sup>	2.1	°C/W



Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
∆BVbss/∆Tj	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.027		V/°C
		V <sub>GS</sub> =10V , I <sub>D</sub> =20A		6	8	
RDS(ON)	Static Drain-Source On- Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		7.5	10	mΩ
V <sub>G</sub> S(th)	Gate Threshold Voltage		1.2		2.5	V
$\triangle V_{\text{GS(th)}}$	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-5.8		mV/°(
		V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	uA
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =30A		43		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.7		Ω
$Q_g$	Total Gate Charge (4.5V)			20		nC
$Q_{gs}$	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , J <sub>D</sub> =15A		7.6		
$Q_{gd}$	Gate-Drain Charge	10-13A		7.2		
T <sub>d(on)</sub>	Turn-On Delay Time			7.8		
Tr	Rise Time	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V ,		15		
T <sub>d(off)</sub>	Turn-Off Delay Time	R <sub>G</sub> =3.3Ω		37.3		ns
T <sub>f</sub>	Fall Time	_I <sub>D</sub> =15A		10.6		İ
Ciss	Input Capacitance			2295		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V ,		267		pF
Crss	Reverse Transfer Capacitance	_f=1MHz		210		
Is	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force			40	Α
Ism	Pulsed Source Current <sup>2,6</sup>	Current			140	Α
Vsp	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	V

## **Diode Characteristics**

#### Note

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leqq 300 us$  , duty cycle  $\leqq 2\%$
- 3 .The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}\text{=}25\text{V}, V_{\text{GS}}\text{=}10\text{V}, L\text{=}0.1\text{mH}, I_{\text{AS}}\text{=}34\text{A}$
- 4. The power dissipation is limited by 150 °C junction temperature
- 5 .The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



## **Typical Characteristics**

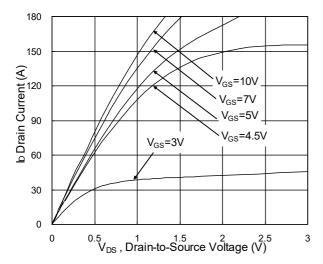


Fig.1 Typical Output Characteristics

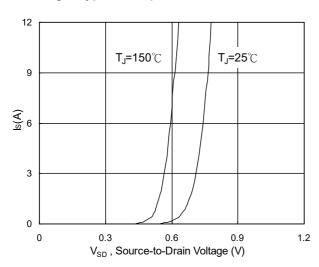


Fig.3 Forward Characteristics of Reverse

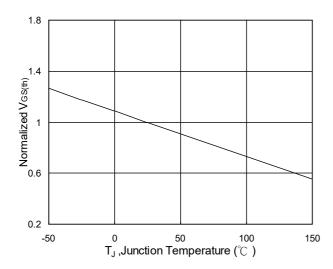


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

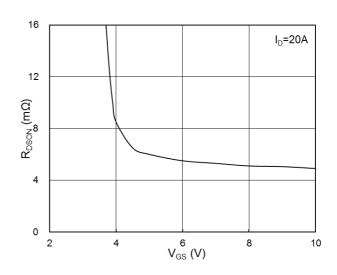


Fig.2 On-Resistance vs. G-S Voltage

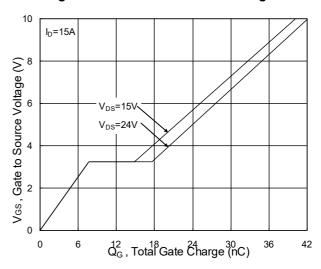


Fig.4 Gate-Charge Characteristics

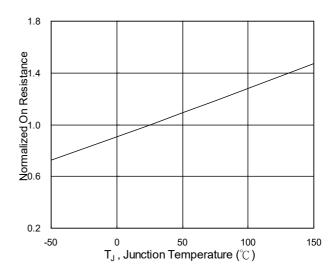
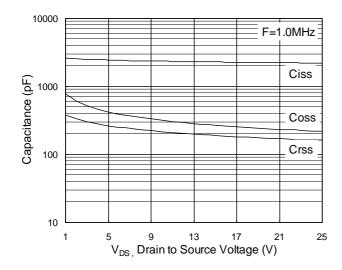


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>





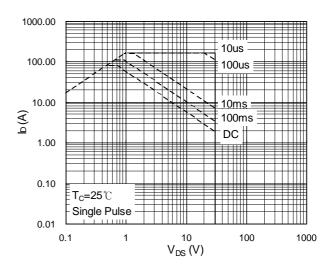


Fig.7 Capacitance

Fig.8 Safe Operating Area

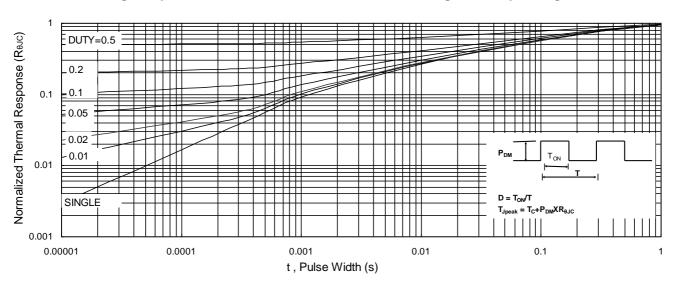


Fig.9 Normalized Maximum Transient Thermal Impedance

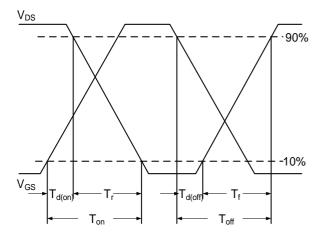


Fig.10 Switching Time Waveform

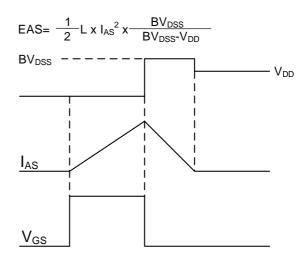
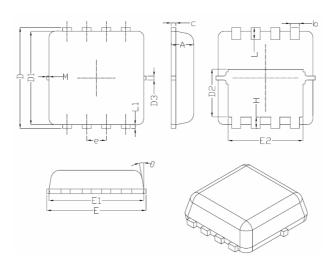


Fig.11 Unclamped Inductive Switching Waveform

# **DFN3X3-8L Package Information**



Sumb al	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
e	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
M	*	*	0.15	
θ		10°	12 <sup>°</sup>	



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BXP4N65F AOL1454G WMJ80N60C4 BXP2N20L BXP2N65D BXT1150N10J BXT1700P06M TSM60NB380CP ROG RQ7L055BGTCR
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