

#### **Features**

- 650-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V<sub>F</sub>

#### **Benefits**

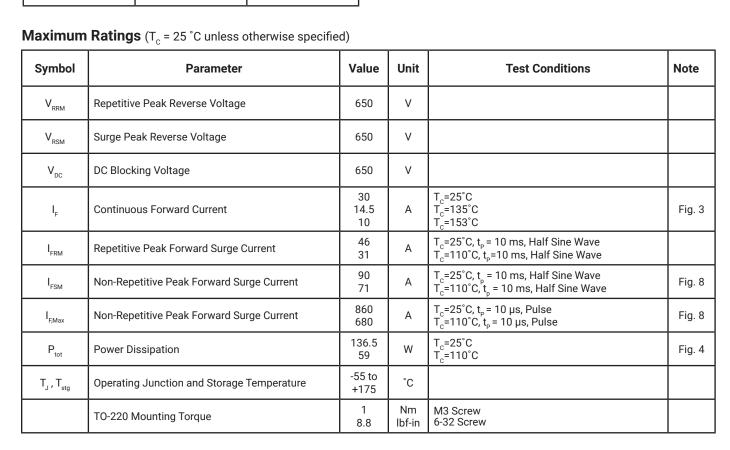
- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

### Applications

- Switch Mode Power Supplies
- Power Factor Correction
- Motor Drives



compliant lead-free	start lead-free		
Part Number	Package	Marking	
HC3D10065A	TO220-2L	HC3D10065A	





TO220-2L Package







#### **Electrical Characteristics**

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V <sub>F</sub>	Forward Voltage	1.5 2.0	1.8 2.4	V	I <sub>F</sub> = 10 A T_=25°C I <sub>F</sub> = 10 A T_=175°C	Fig. 1
I <sub>R</sub>	Reverse Current	12 24	60 220	μA	V <sub>R</sub> = 650 V T <sub>J</sub> =25°C V <sub>R</sub> = 650 V T <sub>J</sub> =175°C	Fig. 2
Q <sub>c</sub>	Total Capacitive Charge	24		nC	V <sub>R</sub> = 400 V, I <sub>F</sub> = 10 A di/dt = 500 A/µs T <sub>J</sub> = 25°C	Fig. 5
С	Total Capacitance	460.5 44 40		pF	V <sub>R</sub> = 0 V, T <sub>J</sub> = 25°C, f = 1 MHz V <sub>R</sub> = 200 V, T <sub>J</sub> = 25°C, f = 1 MHz V <sub>R</sub> = 400 V, T <sub>J</sub> = 25°C, f = 1 MHz	Fig. 6
Ε <sub>c</sub>	Capacitance Stored Energy	3.6		μJ	V <sub>R</sub> = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

#### **Thermal Characteristics**

	Symbol	Parameter	Тур.	Unit	Note
ſ	$R_{_{\!$	Thermal Resistance from Junction to Case	1.1	°C/W	Fig. 9

# **Typical Performance**

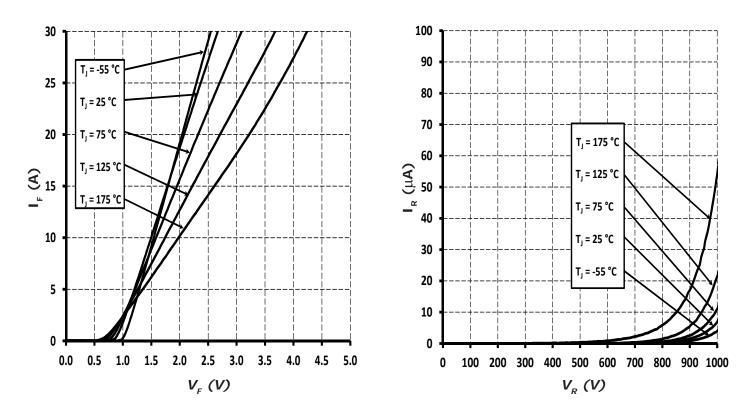
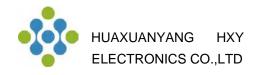


Figure 1. Forward Characteristics

Figure 2. Reverse Characteristics



# **Typical Performance**

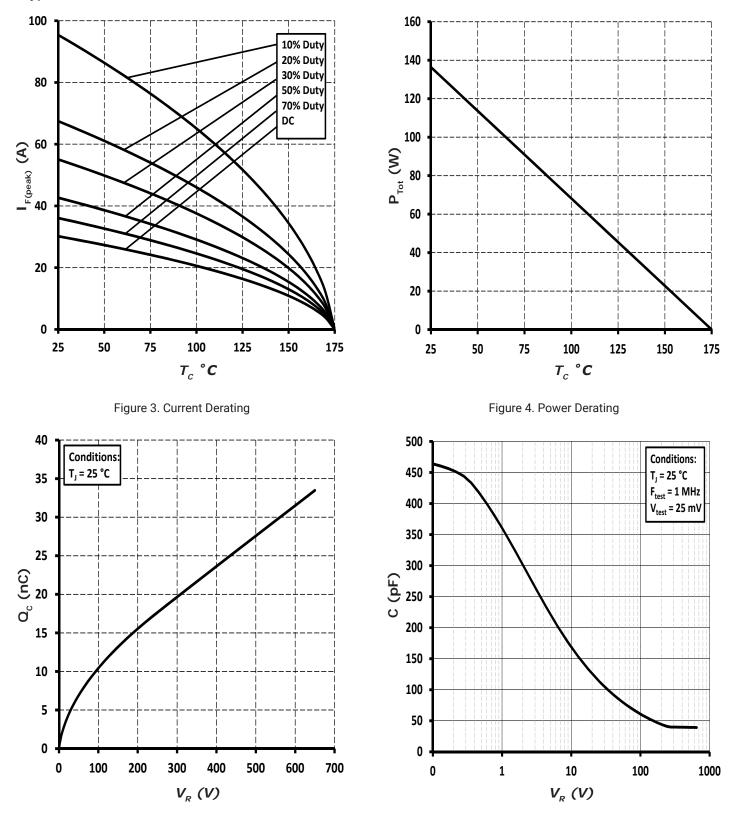
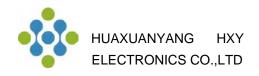


Figure 5. Total Capacitance Charge vs. Reverse Voltage

Figure 6. Capacitance vs. Reverse Voltage



# **Typical Performance**

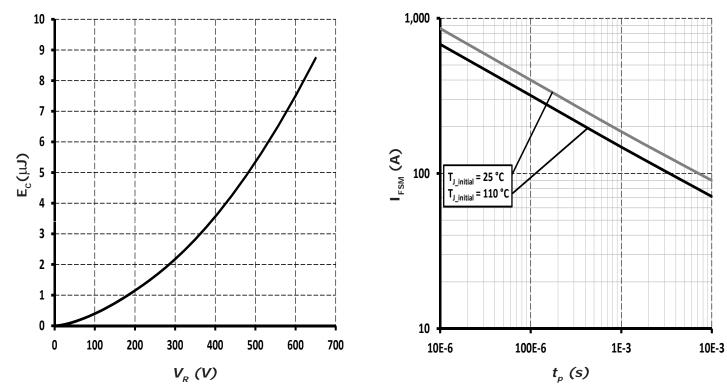


Figure 7. Capacitance Stored Energy

Figure 8. Non-repetitive peak forward surge current versus pulse duration (sinusoidal waveform)

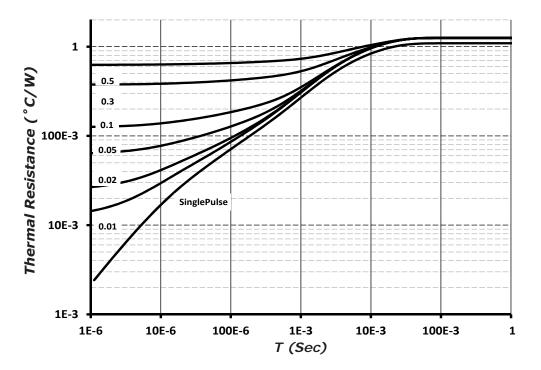
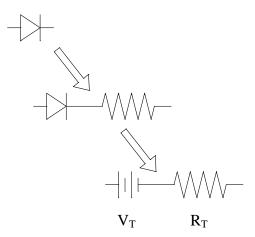


Figure 9. Transient Thermal Impedance





$$Vf_T = V_T + If * R_T$$

$$V_T = 0.94 + (T_J * -1.3 * 10^{-3})$$
  
 $R_T = 0.044 + (T_J * 4.4 * 10^{-4})$ 

Note: T<sub>j</sub> = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

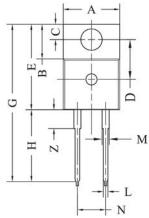


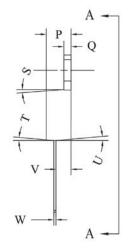
# **Package Dimensions**

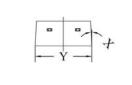


PIN1 O

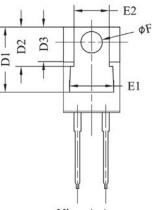
PIN 2 O







O CASE

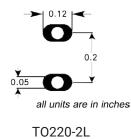


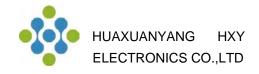
POS	Inc	hes	Millin	neters	
POS	Min Max		Min	Max	
А	.381	.410	9.677	10.414	
В	.235	.255	5.969	6.477	
С	.100	.120	2.540	3.048	
D	.223	.337	5.664	8.560	
D1	.457	·490	11.60-1	11.60-12.45 typ	
D2	.277:	303 typ	7.04-7	7.04-7.70 typ	
D3	.244:	252 typ	6.22-	6.22-6.4 typ	
E	.590	.615	14.986	15.621	
E1	.302	.326	7.68	8.28	
E2	.227	251	5.77	6.37	
F	.143	.153	3.632	3.886	
G	1.105	1.147	28.067	29.134	
Н	.500	.550	12.700	13.970	
L	.025	.036	.635	.914	
М	.045	.055	1.143	1.550	
Ν	.195	.205	4.953	5.207	
Р	.165	.185	4.191	4.699	
Q	.048	.054	1.219	1.372	
S	3°	6°	3°	6°	
Т	3°	6°	3°	6°	
U	3°	6°	3°	6°	
V	.094	.110	2.388	2.794	
W	.014	.025	.356	.635	
Х	3°	5.5°	3°	5.5°	
Y	.385	.410	9.779	10.414	
z	.130	.150	3.302	3.810	

View A-A

1. Dimension L, M, W apply for Solder Dip Finish

**Recommended Solder Pad Layout** 





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