

Discription

The HCESD0603LC5VB-M protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

Features

- ★ Small Body Outline Dimensions: 0.61 mm x 0.31 mm
- ★ Low Body Height: 0.28 mm
- ★ Low Leakage
- ★ Response Time is Typically < 1 ns
- ★ ESD Rating of Class 3 per Human Body Model
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ These are Pb-Free Devices
- ★ We declare that the material of product compliance with RoHS requirements and Halogen Free.

Orderingin formation

Product ID	Pack	Qty(PCS)
HCESD0603LC5VB-M	DFN0603-2L	10000

Absolute Ratings(Tamb = 25°C)

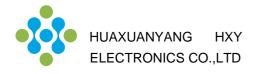
Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (t _P = 8/20µs)	30	W
TL	Maximum lead temperature for soldering during 10s	260	°C
T _{stg}	Storage Temperature Range	-55 to +150	°C
T _{op}	Operating Temperature Range	-40 to +125	°C
Tj	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge	±20	ΚV
	contact discharge	±20	I X V



DFN0603-2L



Circuit Diagram



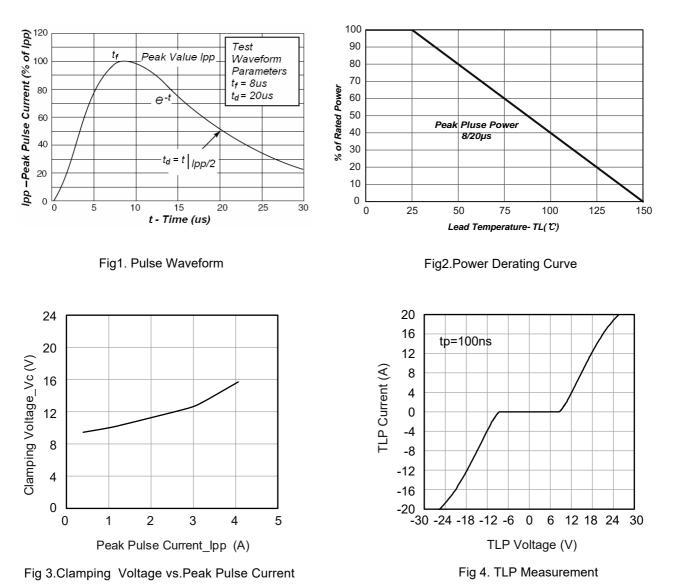
Electrical Characteristics

V _{RWM} (V)	I _R (uA) @ V _{RWM}	V _{BR} (V)@ I _T (Note 1)	Ι _τ	V _c (V) @ I _{PP} =1 A*	V _c (V) @ Max I _{PP} *	I _{РР} (А)*	Р _{РК} (W)*	C (pF)
Max	Max	Min	mA	Тур	Max	Мах	Мах	Мах
5.0	1.0	6.0	1	8	12	2.5	30	3.0

*Surge current waveform per Figure 1.

1. V_{BR} is measured with a pluse test current I_T at an ambient temperature of 25° C.

Typical Characteristics





Max

0.64

0.34

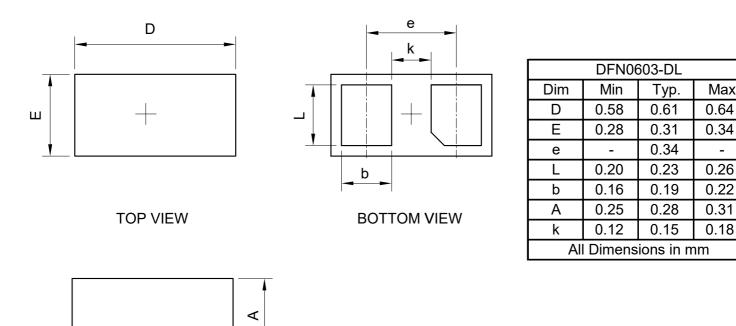
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0.26

0.22

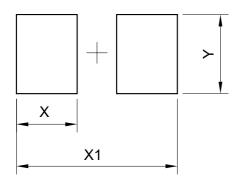
0.31

Package Outline Dimension



SIDE VIEW

Suggested Pad layout



DFN0603-DL		
DIM (mm)		
Х	0.23	
X1	0.61	
Y	0.30	



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