

Discription

The HESDSBLC3V3LB protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN1006-2L

Features

★ Transient protection for high-speed data lines IEC 61000-4-2(ESD) ±8kV (Contact) ±15kV (Air)

IEC 61000-4-4(EFT) 40A (5/50 ns)

★ Peak power dissipation: 84W (8/20us)

★ Working voltages: 3.3V

★ Ultra-small package (1.0mmx0.6mmx0.5mm)

★ Protects one I/0 line

★ Low clamping voltage

★ Low leakage current



Circuit Diagram

Orderingin formation

| Product ID | Product ID Pack | |
|---------------|-----------------|-------|
| HESDSBLC3V3LB | DFN1006-2L | 10000 |

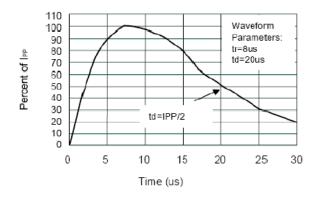
Absolute Ratings(Tamb = 25°C)

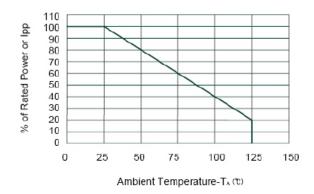
| Symbol | Parameter | | Value | Units |
|------------------|---|---|-------------|-------|
| P _{PP} | Peak Pulse Power ($t_p = 8/20 \mu s$) | | 84 | W |
| TL | Maximum lead temperature for soldering during 10s | | 260 | °C |
| T _{stg} | Storage Temperature Range | | -55 to +150 | °C |
| T _{op} | Operating Temperature Range | | -55 to +150 | °C |
| Tj | Maximum junction temperature | | 150 | °C |
| | IEC61000-4-2 (ESD) air discha contact discha | _ | ±15 ±8 | KV |
| | IEC61000-4-4 (EFT) | | 40 | Α |

Electrical Characteristics Ratings at 25°C

| Symbol | Parameter | Test Condition | Min | Тур | Max | Units |
|---------------------|----------------------------------|----------------------------------|-----|-----|-----|-------|
| V_{RWM} | Reverse Working Voltage | | | | 3.3 | V |
| V _{BR} | Reverse Breakdown Voltage | Iτ = 1mA | 3.6 | | | V |
| I R | Reverse Leakage Current | V _{RWM} = 3.3V | | | 1.0 | μA |
| Vc | Clamping Valtage | $I_{RWM} = 1A, t_p = 8/20 \mu s$ | | | 8 | V |
| Vc Clamping Voltage | $I_{RWM} = 7A, t_p = 8/20 \mu s$ | | | 12 | V | |
| C | Junction Capacitance | V _R = 0V, f = 1MHz | | 10 | 15 | pF |

Typical Characteristics

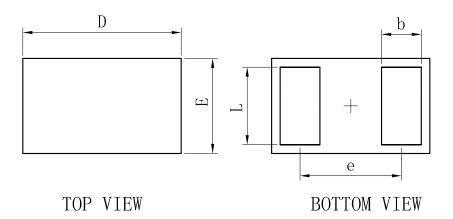




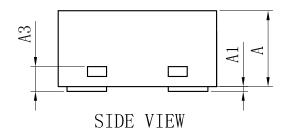
Pulse Waveform

Power Derating Curve

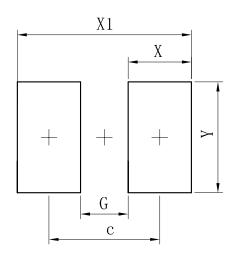
Outline And Dimensions



| DFN1006-2L | | | |
|----------------------|------------|-------|-------|
| Dim | Min | Тур | Max |
| D | 0. 95 | 1.00 | 1.05 |
| Е | 0. 55 | 0.60 | 0.65 |
| е | _ | 0.64 | - |
| L | 0.44 | 0.49 | 0.54 |
| b | 0. 20 | 0. 25 | 0.30 |
| A | 0.43 | 0.48 | 0. 53 |
| A1 | 0 | - | 0.05 |
| A3 | 0. 127REF. | | |
| All Dimensions in mm | | | |



Soledering Footprint



| Dimensions | (mm) |
|------------|------|
| С | 0.70 |
| G | 0.30 |
| X | 0.40 |
| X1 | 1.10 |
| Y | 0.70 |



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