## **Discription**

The HPESD5V0U1UT protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect 2 unidirectional

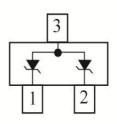
It gives designer the flexibility to protect 2 unidirectional line in applications where arrays are not practical.



SOT-23

#### **Features**

- ★ We declare that the material of product compliance with RoHS requirements and Halogen Free.
- ★ 2 unidirectionaltransilfunctions
- ★ Low leakage current:IR max< 20 µA at VRM
- ★ 300W peak pulse power(8/20µs)
- ★ Transient protection for data lines as per
- ★ IEC61000-4-2(ESD) 15KV(air) 8KV(contact)
- ★ IEC61000-4-5(Lightning) see IPPM below



Circuit Diagram

## **Orderingin formation**

Product ID	Pack	Qty(PCS)
HPESD5V0U1UT	SOT-23	3000

## Absolute Ratings(Tamb = 25°C)

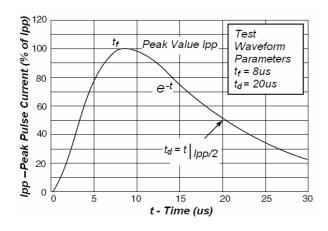
Symbol	Parameter	Value	Units
$P_{PP}$	Peak Pulse Power (t <sub>p</sub> = 8/20μs)	100	W
TL	Maximum lead temperature for soldering during 10s	260	°C
T <sub>stg</sub>	Storage Temperature Range	-55 to +150	°C
T <sub>op</sub>	Operating Temperature Range	-40 to +125	°C
Tj	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air dischar contact dischar		KV

## **Electrical Characteristics** Ratings at 25°C ambient temperature unless otherwise specified.VF = 0.9V at IF = 10mA

Device	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA) @ V <sub>RWM</sub>	V <sub>BR</sub> (V)@ I <sub>T</sub> (Note 1)	I <sub>T</sub>	V <sub>C</sub> (V) @ Max I <sub>PP</sub> *	I <sub>PP</sub> (A)*	C (pF)
	Max	Max	Min	mA	Max	Max	Тур
HPESD5V0U1UT	5	0.5	6	1	25	4	0.5

<sup>1.</sup>  $V_{BR}$  is measured with a pluse test current  $I_T$  at an ambient temperature of 25°C.

## **Typical Characteristics**

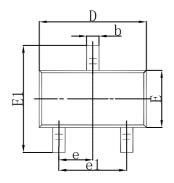


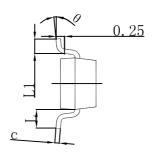
110 100 90 Peak Pluse Power % of Rated Power 80 8/20us 70 60 50 40 30 Average Power 20 10 0 25 75 125 150 Lead Temperature - T<sub>L</sub> (°C)

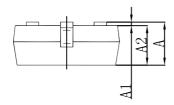
Fig 1. Pulse Waveform

Fig 2.Power Derating

# **SOT-23 Package Outline Dimensions**

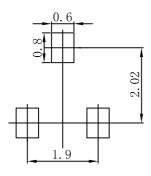






Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
Α	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
С	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
е	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

# **SOT-23 Suggested Pad Layout**



### Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
  3.The pad layout is for reference purposes only.



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