

Discription

The HRCLAMP0521Z protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical. **Features**

- ★ Transient protection for high-speed data lines IEC 61000-4-2(ESD) ±8kV (Contact) ±15kV (Air) IEC 61000-4-4(EFT) 40A (5/50 ns)
- ★ Peak power dissipation: 100W (8/20us)
- ★ Working voltages : 5V
- ★ Ultra-small package (0.6mmx0.3mmx0.3mm)
- ★ Low capacitance: 0.55pF (Typical)
- ★ Low clamping voltage
- ★ Low leakage current

Orderingin formation

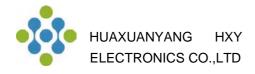
Product ID	Pack	Qty(PCS)
HRCLAMP0521Z	DFN0603-2L	15000

Absolute Ratings(Tamb = 25°C)

Symbol	Parameter		Value	Units
P _{PP}	Peak Pulse Power (t _P = 8/20µs)		100	W
TL	Maximum lead temperature for soldering during 10s		260	°C
T _{stg}	Storage Temperature Range		-55 to +150	°C
T _{op}	Operating Temperature Range		-40 to +125	°C
Tj	Maximum junction temperature		150	°C
		ir discharge t discharge	土30 土30	ΚV

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Circuit Diagram

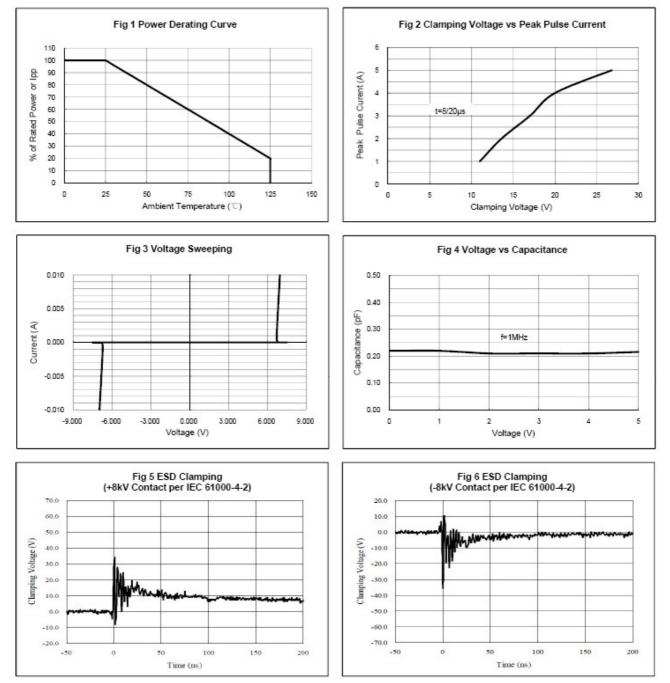


Electrical Characteristics

Symbol	Parameter	Test Condition	Min	Тур	Max	Units
Vrwm	Reverse Working Voltage				5.0	V
Vbr	Reverse Breakdown Voltage	l⊤ = 1mA	6.0			V
IR	Reverse Leakage Current	$V_{RWM} = 5.0V$			100	nA
Vc		I _{RWM} = 1A, t _P = 8/20µs			13	V
VC	Clamping Voltage	$I_{RWM} = 4A, t_p = 8/20 \mu s$			25	V
CJ	Junction Capacitance	$V_R = 0V, f = 1MHz$		0.55	0.7	pF

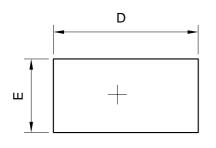


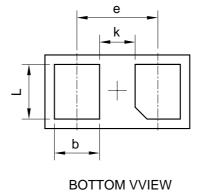
Typical Characteristics





Outline And Dimensions





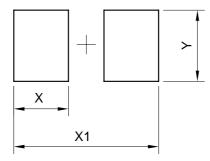
DFN0603-2L			
Dim	Min	Тур.	Max
D	0.58	0.61	0.64
E	0.28	0.31	0.34
е	-	0.34	-
L	0.20	0.23	0.26
b	0.16	0.19	0.22
А	0.25	0.28	0.31
k	0.12	0.15	0.18
All Dimensions in mm			

TOP VIEW



SSIDE VIEW

Soledering Footprint



DFN0603-2L		
DIM	(mm)	
Х	0.23	
X1	0.61	
Y	0.30	



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