

#### Description

The HXY30N03DF uses advanced trench technology

to provide excellent  $R_{DS(ON)}$ , low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 30V I<sub>D</sub> = 30 A

 $R_{DS(ON)} < 13m\Omega @ V_{GS}=10V$ 

## Application

Battery protection

Load switch

Uninterruptible power supply

#### Package Marking and Ordering Information

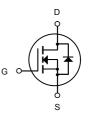
Product ID	Pack	Marking	Qty(PCS)		
HXY30N03DF	DFN3X3-8L	30N03DF XXX YYYY	5000		
Absolute Maximum Ratings (Tc=25° unless otherwise noted)					

#### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	30	V
VGS	Gate-Source Voltage	±20	V
I₀@Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	30	А
I₀@Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	18	А
IDM	Pulsed Drain Current <sup>2</sup>	55	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	22.1	mJ
IAS	Avalanche Current	21	А
P₀@Tc=25°C	Total Power Dissipation <sup>4</sup>	20	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-ambient <sup>1</sup>	75	°C/W
R₀JC	Thermal Resistance Junction-Case <sup>1</sup>	6	°C/W







N-Channel MOSFET



Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
$\triangle BV$ DSS/ $\triangle T_J$	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.022		V/°C
Rds(on)	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =10A		8	13	
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		12	20	mΩ
VGS(th)	Gate Threshold Voltage		1.0		2.5	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	─V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-5.1		mV/°C
ldss	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	
		V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	uA
lgss	Gate-Source Leakage Current	$V_{GS}=\pm20V$ , $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =1A		4.5		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		2.5		Ω
Qg	Total Gate Charge (4.5V)			7.2		nC
Qgs	Gate-Source Charge	V <sub>DS</sub> =20V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		1.4		
Qgd	Gate-Drain Charge			2.2		
Td(on)	Turn-On Delay Time	–V <sub>DD</sub> =12V , V <sub>GS</sub> =10V , –R <sub>G</sub> =3.3 −I <sub>D</sub> =5A		4.1		
Tr	Rise Time			9.8		
Td(off)	Turn-Off Delay Time			15.5		ns
T <sub>f</sub>	Fall Time			6.0		
Ciss	Input Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		572		
Coss	Output Capacitance			81		pF
Crss	Reverse Transfer Capacitance			65		
ls	Continuous Source Current <sup>1,5</sup>				28	А
lsм	Pulsed Source Current <sup>2,5</sup>	−V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			55	А
Vsd	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1.2	V

#### Electrical Characteristics (TJ=25 °C, unless otherwise noted)

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width  $\leq 300 us$  , duty cycle  $\leq 2\%$ 

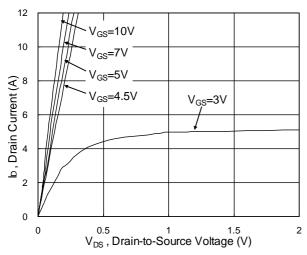
3. The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}\text{=}25\text{V}, V_{\text{GS}}\text{=}10\text{V}, \text{L=}0.1\text{mH}, \text{I}_{\text{AS}}\text{=}21\text{A}$ 

4 .The power dissipation is limited by 150  $^\circ\text{C}$  junction temperature 5.The data is theoretically the same as  $I_D$  and

 $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.



# **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

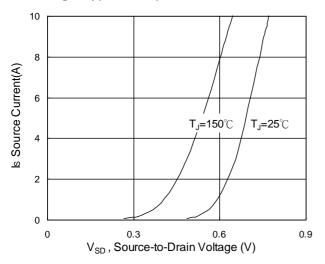


Fig.3 Forward Characteristics Of Reverse

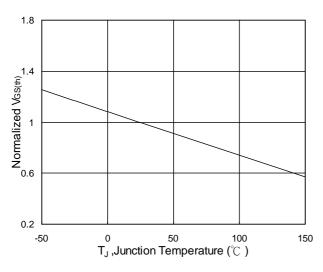


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$ 

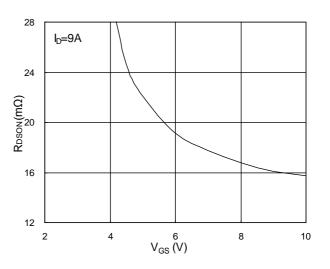


Fig.2 On-Resistance vs. Gate-Source

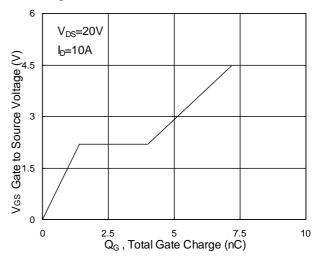


Fig.4 Gate-Charge Characteristics

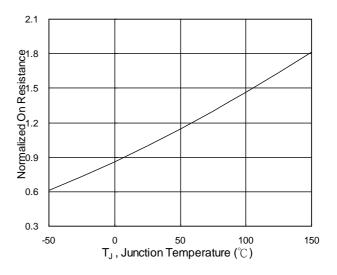
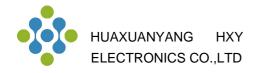


Fig.6 Normalized R<sub>DSON</sub> vs T<sub>J</sub>



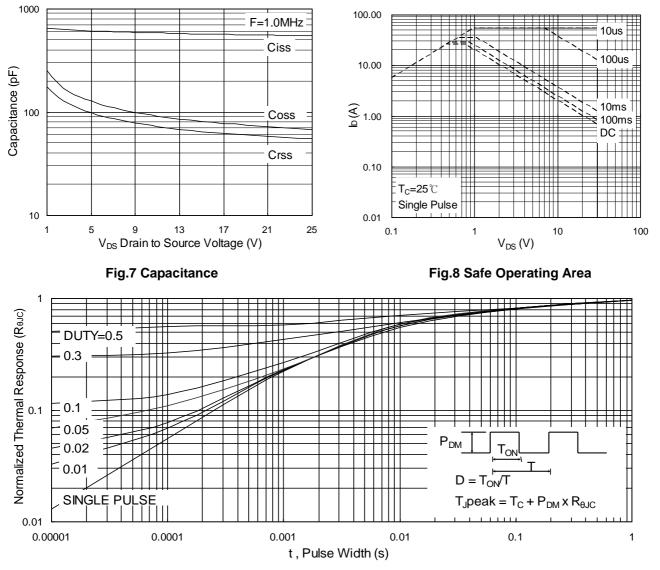


Fig.9 Normalized Maximum Transient Thermal Impedance

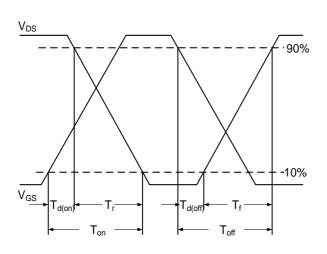
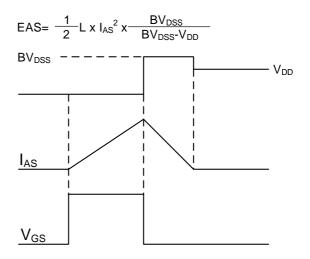


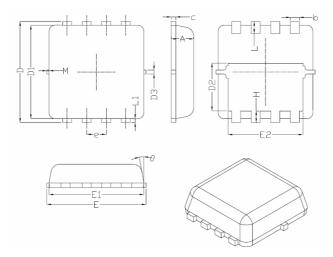
Fig.10 Switching Time Waveform







# DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters			
	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
e	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
Μ	*	*	0.15	
θ		10 <sup>°</sup>	12 <sup>°</sup>	



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