



## Description

The HXY4402S uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

## General Features

$V_{DS} = 20V$   $I_D = 20A$

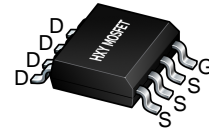
$R_{DS(ON)} < 5.5m\Omega$  @  $V_{GS}=4.5V$

## Application

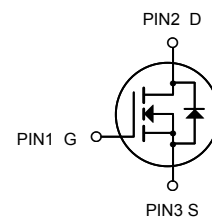
Battery protection

Load switch

Uninterruptible power supply



SOP-8



N-Channel MOSFET

## Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY4402S	SOP-8	4402 XXX YYYY	3000

## Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D$	Drain Current – Continuous ( $T_C=25^\circ C$ )	20	A
	Drain Current – Continuous ( $T_C=70^\circ C$ )	16	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	140	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	162	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	57	A
$P_D$	Power Dissipation ( $T_C=25^\circ C$ )	3.1	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction to ambient	40	$^\circ C/W$



Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 uA, V <sub>GS</sub> =0V	20			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.5		1.6	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A			5.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A T <sub>J</sub> =125°C			7	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =18A			7	
On State Drain Current	I <sub>D(ON)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =5V	140			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =20A		105		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =10V, f=1MHz	3080		4630	pF
Output Capacitance	C <sub>oss</sub>		520		960	
Reverse Transfer Capacitance	C <sub>rss</sub>		350		810	
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	0.6		2.1	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =10V, I <sub>D</sub> =20A	28		43	nC
Gate Source Charge	Q <sub>gs</sub>		7		11	
Gate Drain Charge	Q <sub>gd</sub>		7		17	
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =10V, R <sub>L</sub> =0.5Ω, R <sub>GEN</sub> =3Ω		7		ns
Turn-On Rise Time	t <sub>r</sub>			8		
Turn-Off DelayTime	t <sub>d(off)</sub>			70		
Turn-Off Fall Time	t <sub>f</sub>			18		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A, di/dt= 500A/us	13		20	nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		29		43	
Maximum Body-Diode Continuous Current	I <sub>S</sub>				4	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V			1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.



Typical Characteristics

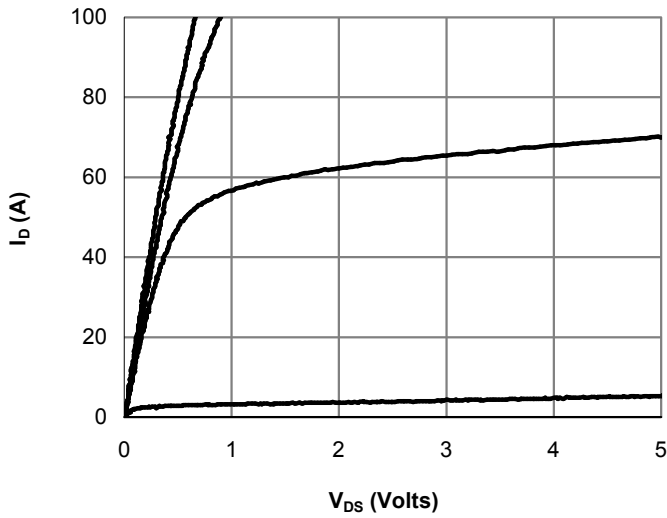


Fig 1: On-Region Characteristics (Note E)

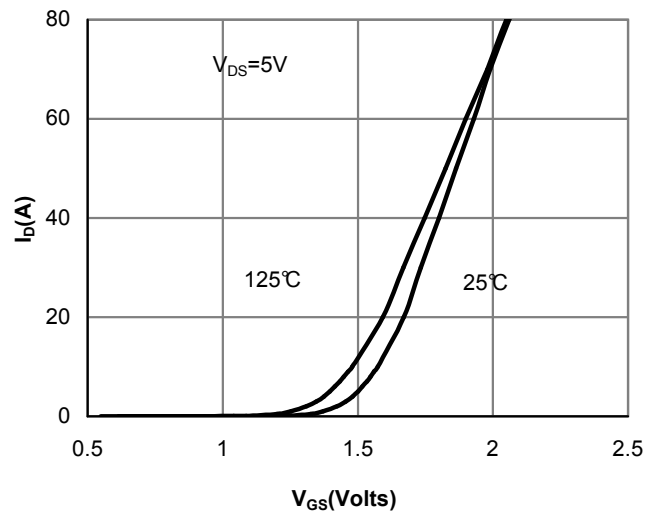


Figure 2: Transfer Characteristics (Note E)

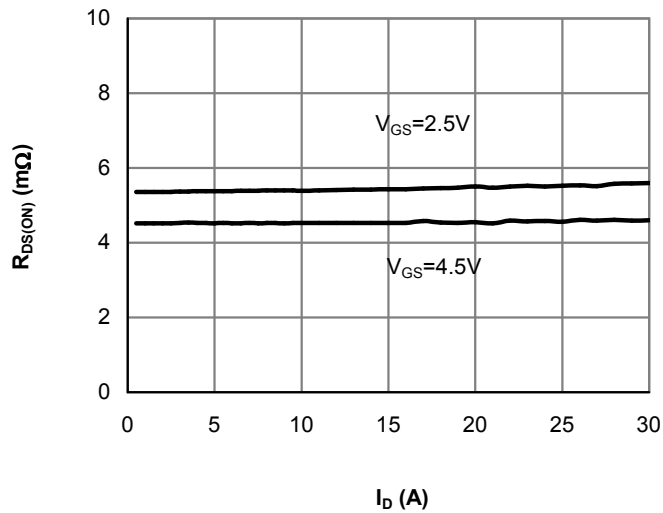


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

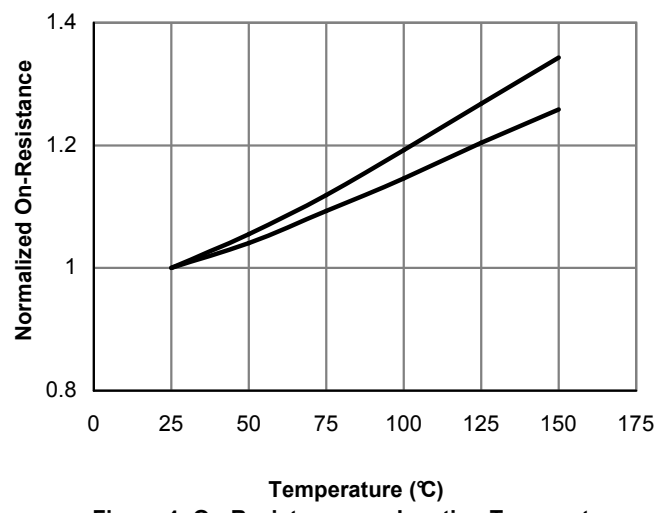


Figure 4: On-Resistance vs. Junction Temperature (Note E)

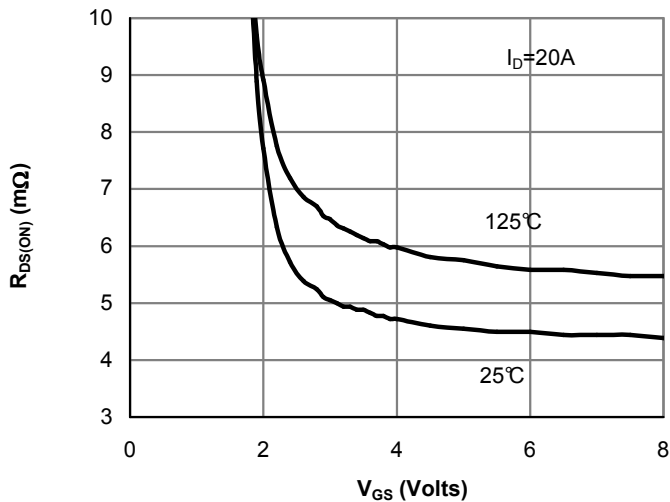


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

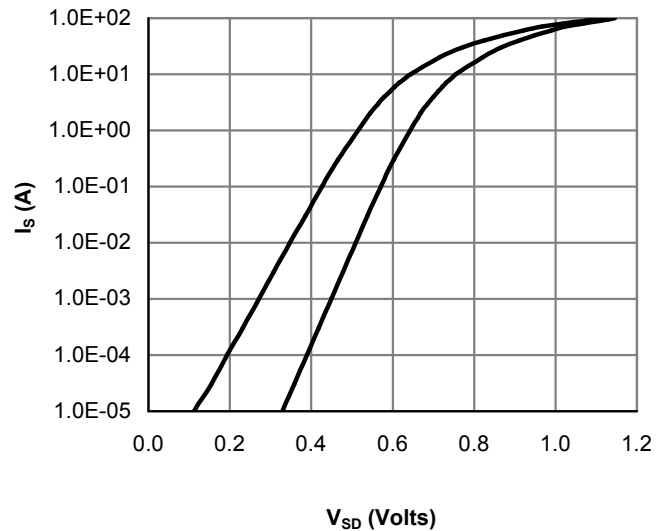


Figure 6: Body-Diode Characteristics (Note E)



### Typical Characteristics

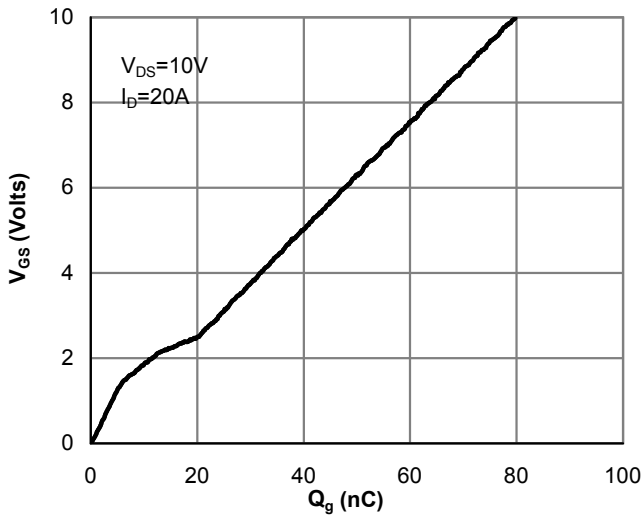


Figure 7: Gate-Charge Characteristics

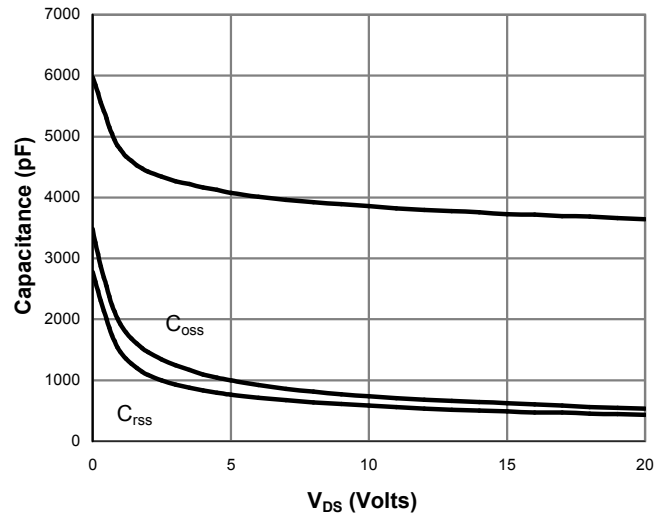


Figure 8: Capacitance Characteristics

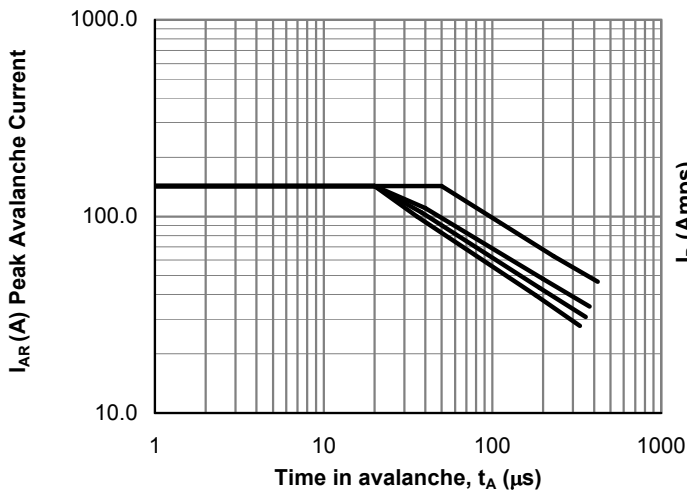


Figure 9: Single Pulse Avalanche capability (Note C)

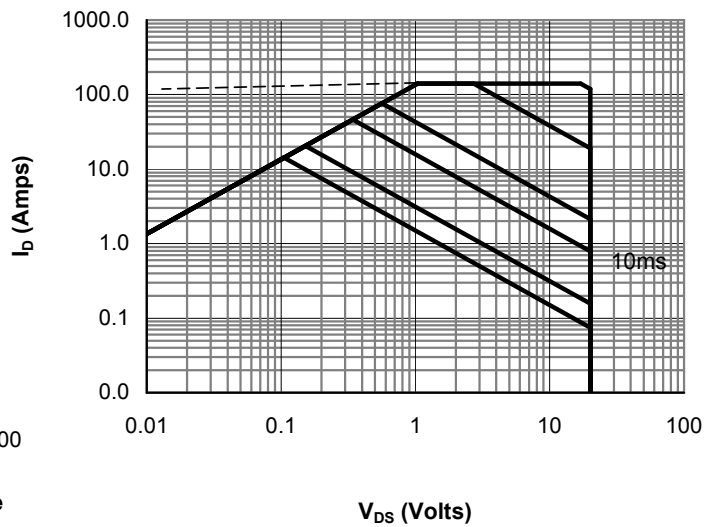


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)



### Typical Characteristics

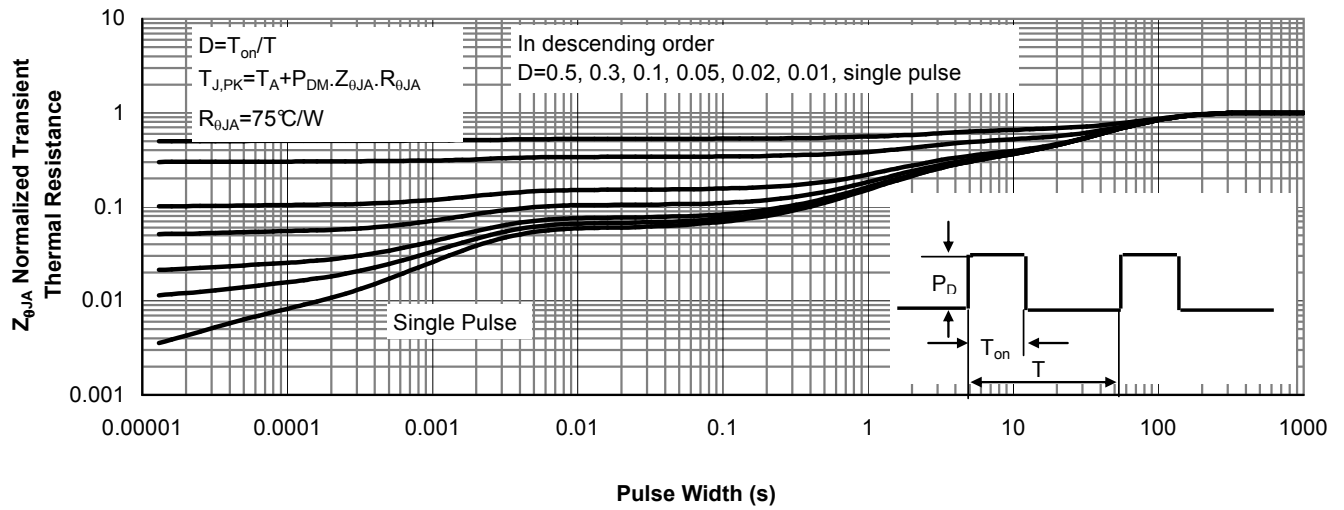
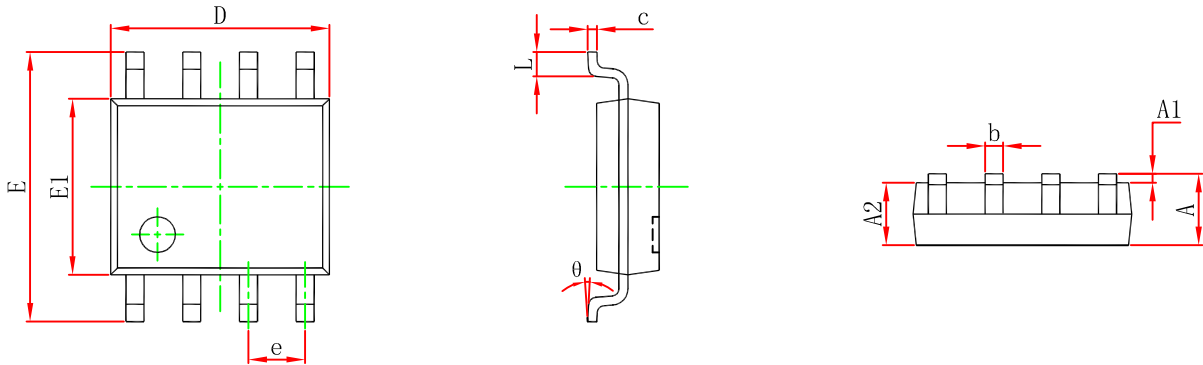


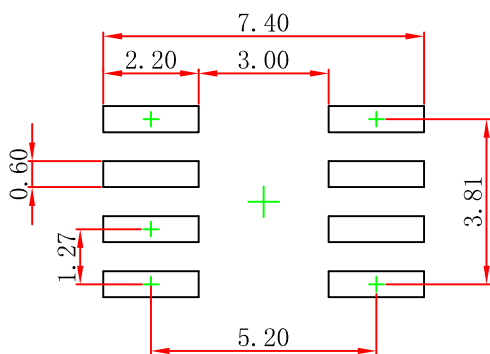
Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)



### SOP-8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



- Note:
1. Controlling dimension: in millimeters.
  2. General tolerance:  $\pm 0.05\text{mm}$ .
  3. The pad layout is for reference purposes only.



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