

Description

The HXY4805S uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gat e charge. It can be used in a wide variety of applications.

General Features

 $V_{DS} = -30V, I_{D} = -8.5A$

 $R_{DS(ON)}$ < 25m @ V $_{GS}$ =-10V

 $R_{DS(ON)}$ < 42m @ V GS=-4.5V

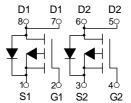
Application

PWM application

Load switch



SOP-8



Dual P-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY4805S	SOP-8	4805 XXXX	3000

Absolute Maximum Ratings (T_A=25 ^oC unless otherwise noted)

Symbol	Parameter	Limit	Unit
V _D s	Drain-Source Voltage	-30	V
Vgs	Gate-Source Voltage	±20	V
l _D	Drain Current-Continuous	-8.5	Α
Ірм	Drain Current-Pulsed (Note 1)	-26	Α
PD	Maximum Power Dissipation	1.5	W
T _J ,T _{STG}	T _J ,T _{STG} Operating Junction and Storage Temperature Range		$^{\circ}$
Reja	Thermal Resistance,Junction-to-Ambient (Note 2)	85	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA		-0.022		V/°C	
D-avan	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-6A	20		25		
R _{DS(ON)}	Static Dialii-Source On-Resistance	V _{GS} =-4.5V , I _D =-4A		25	42	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} . In =-250uA	-1.0		-2.5	٧	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	VGS=VDS , ID =-250UA		4.6		mV/°C	
	Dunin Course Looke as Course	V _{DS} =-24V , V _{GS} =0V , T _J =25°C			-1		
IDSS	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =55°C			-5	uA	
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA	
gfs	Forward Transconductance	V _{DS} =-5V , I _D =-6A		17		S	
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		13		Ω	
Q_g	Total Gate Charge (-4.5V)			12.6			
Q_{gs}	Gate-Source Charge	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-6A		4.8		nC	
Q_{gd}	Gate-Drain Charge			4.8			
T _{d(on)}	Turn-On Delay Time			4.6			
Tr	Rise Time	V_{DD} =-15 V , V_{GS} =-10 V , R_{G} =3.3 Ω ,		14.8		20	
T _{d(off)}	Turn-Off Delay Time	I _D =-6A		41		ns	
Tf	Fall Time			19.6			
Ciss	Input Capacitance			1345			
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz		194		pF	
Crss	Reverse Transfer Capacitance			158			

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,5}	V- V- OV Force Current			-6.5	Α
I _{SM}	Pulsed Source Current ^{2,5}	V _G =V _D =0V , Force Current			-26	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25°C			-1.2	V
t _{rr}	Reverse Recovery Time			16.3		nS
Q _{rr}	Reverse Recovery Charge	IF=-6A , dI/dt=100A/µs , T _J =25°C		5.9		nC

Note

- 1. The data tested by surface mounted on a 1 inch $^2\,\text{FR-4}$ board with 2OZ copper.
- 2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =-25V, V_{GS} =-10V, L=0.1mH, I_{AS} =-38A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



Typical Characteristics

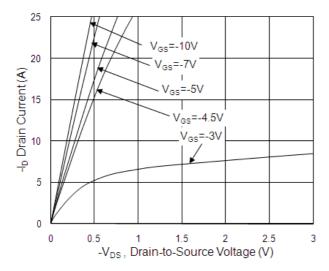


Fig.1 Typical Output Characteristics

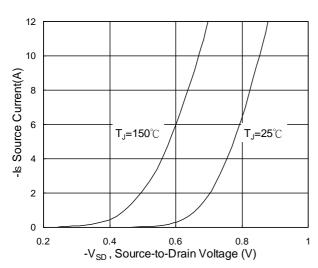


Fig.3 Forward Characteristics of Reverse

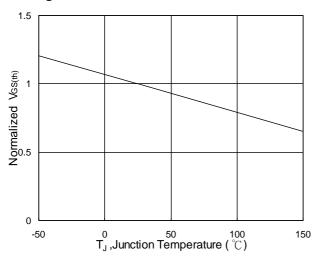


Fig.5 Normalized V_{GS(th)} vs. T_J

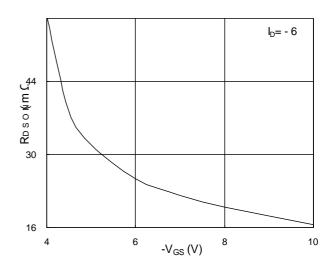


Fig.2 On-Resistance v.s Gate-Source

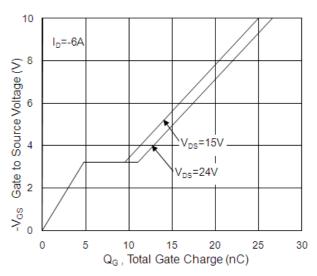


Fig.4 Gate-Charge Characteristics

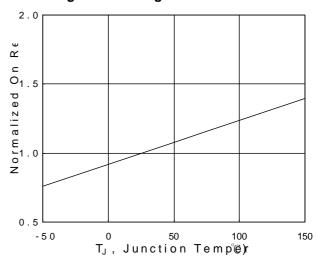
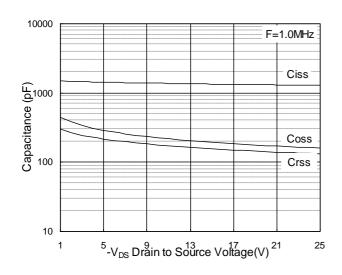


Fig.6 Normalized R_{DSON} vs. T_J



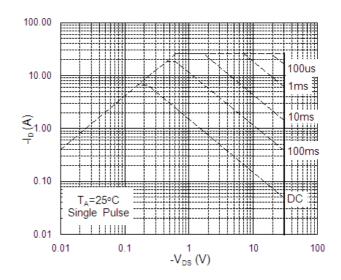


Fig.7 Capacitance

Fig.8 Safe Operating Area

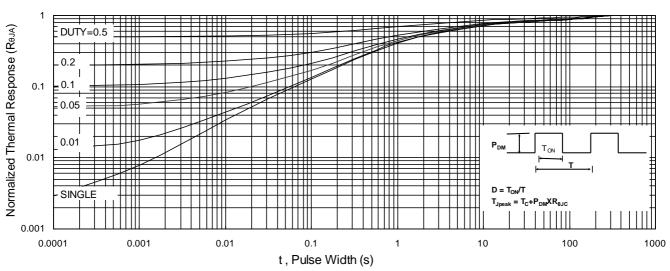


Fig.9 Normalized Maximum Transient Thermal Impedance

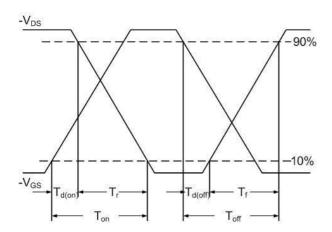


Fig.10 Switching Time Waveform

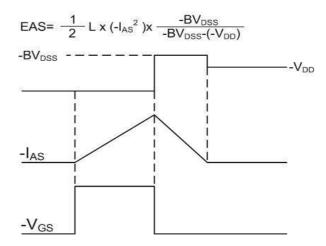
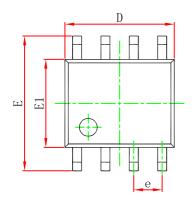
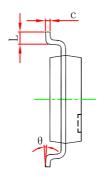


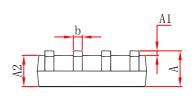
Fig.11 Unclamped Inductive Switching Waveform



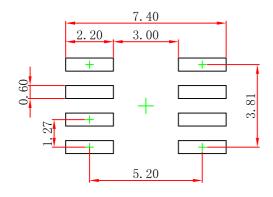
SOP-8 Package Outline Dimensions







Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
c	0.170	0.250	0.007	0.010	
D	4.800	5.000	0.189	0.197	
e	1. 270 (BSC)		0.050 (BSC)		
E	5.800	6. 200	0. 228	0.244	
E1	3.800	4.000	0.150	0.157	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



- Note: 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.

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