

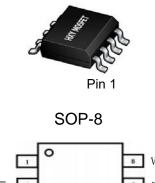
DESCRIPTION

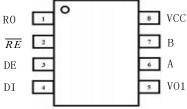
The MAX3485EESA is a low power half duplex transceiver that meets the specifications of RS-485 and RS-422. IC contains one driver and one receiver. The driver slew rates is not limited, allowing them to transmit up to 10Mbps. These transceivers draw between 120μ A and 500μ A of supply current when unloaded or fully loaded with disabled drivers. All parts operate from a single 3.3V supply. Drivers are short-circuit current limited and are protected against excessive power dissipation by thermal shutdown circuitry that places the driver outputs into a high-impedance state. The receiver input has a fail-safe feature that guarantees a logic-high output if the input is open circuit. The ESD tolerance is more than ±8kV for both Human Body Model and ±15kV for IEC61000-4-2 Air Discharge Method on this device.

FEATURES

- · Operates from a Single 3.3V Supply
- Interoperable with +5.0V Logic
- –7V to 12V Common-Mode Input Voltage Range
- Three-State Outputs
- · Driver/ Receiver Enable
- Half-Duplex Communication
- 1/8 Unit Load Allows Up To 256 Devices On The Bus
- · Data Rate up to 10 Mbps
- Current-Limiting and Thermal Shutdown for Driver Overload Protection
- ESD Specifications ±15kV IEC61000-4-2 Air Discharge ±8kV Human Body Model

PIN CONFIGURATION





APPLICATIONS

- · Low Power RS-485 Systems
- Telecommunications
- · Packet Switching
- Industrial-Control Local Area Networks
- Data Concentration
- Data Multiplexers
- Integrated Services Digital Network (ISDN)



PIN DESCRIPTION

Pin No.	Pin Name	Pin Function	
1	RO	Receiver Output	
2	RE	Receiver Output Enable. Active Low	
3	DE	Driver Output Enable. Active High	
4	DI	Driver Input	
5	GND	Ground	
6	A	Non-Inverting Driver Output/ Non-Inverting Receiver Input	
7	В	Inverting Driver Output/ Inverting Receiver Input	
8	VCC	Power Supply	

ABSOLUTE MAXIMUM RATINGS (Note 1)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	Vcc	-0.3	6.0	V
Control Input Voltage	V_{RE}, V_{DE}	-0.3	6.0	V
Driver Input Voltage	V _{DI}	-0.3	6.0	V
Driver Output Voltage	V _A , V _B	-15	15	V
Receiver Input Voltage	V _A , V _B	-15	15	V
Receiver Output Voltage	V _{RO}	-0.3	V _{CC} + 0.3	V
Junction Temperature Range	TJ	-40	125	°C
Storage Temperature Range	T _{STG}	-65	150	°C

Note 1. Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

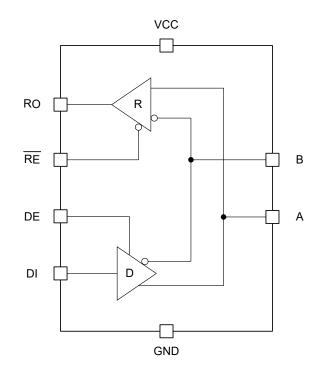
RECOMMENDED OPERATING CONDITIONS (Note 2)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	Vcc	0	3.3 ± 5%	V
Ambient Temperature Range	T _A	-40	85	°C

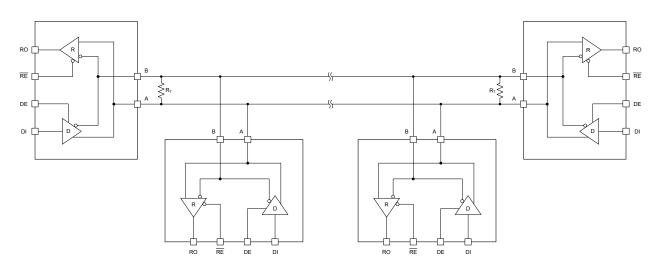
Note 2. The device is not guaranteed to function outside its operating ratings.



BLOCK DIAGRAM



TYPICAL APPLICATION



< Typical RS-485 Network with Half-Duplex Transceivers >



DC ELECTRICAL CHARACTERISTICS

 V_{CC} = 3.3V, T_A = 25°C, unless otherwise noted.

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
		Unloaded, R _L = ∞, Figure 1		GND	-	Vcc	
Differential Output Voltage	V _{OD}	With Load, R_L = 50 Ω (RS-422), Figure 1		1	-	V _{cc}	V
		With Load, R_L = 27 Ω (RS-485), Figure 1		0.6	-	V _{cc}	
Change in Magnitude of Driver Differential Output Voltage for Complimentary States ^(NOTE 3)	ΔV_{OD}	R_L = 27 Ω or R_L = 50 Ω , Figure 1		-	-	0.2	v
Driver Common- Mode Output Voltage	Voc	$R_L = 27\Omega \text{ or } R_L = 50\Omega$), Figure 1	-	-	3.0	v
Input High Voltage	VIH	DE, DI, RE		2.0	-	-	V
Input Low Voltage	V _{IL}	DE, DI, RE		-	-	0.6	V
Logic Input Current	I _{IN1}	DE, DI, RE		-	-	±10	μA
Driver Short-Circuit Output Current	I _{OSD}	V_{OUT} = High or Low, -7V $\leq V_{OUT} \leq 12V$		-	-	±250	mA
Receiver Differential Input Threshold	V _{TH}	$-7V \le V_{CM} \le 12V$		-0.2	-	0.2	v
Receiver Input Hysteresis	ΔV _{TH}	V _{CM} = 0V		-	20	-	mV
Receiver Output High Voltage	V _{OH}	I _{OUT} = −1.5mA, V _{ID} = 200mV, Figure 2		V _{CC} -0.4	-	-	V
Receiver Output Low Voltage	V _{OL}	I _{OUT} = 2.5mA, V _{ID} = 2	00mV, Figure 2	-	-	0.4	V
Three-State (High Impedance) Output Current at Receiver	I _{OZR}	$0 \le V_{OUT} \le V_{CC}, \overline{RE} = V_{CC}$		-	-	±1.0	μA
Receiver Input Resistance	R _{IN}	$-7V \le V_{CM} \le 12V$		12	15	-	kΩ
	I _{IN2}	DE = 0V, V _{CC} = 0V or 3.5V	V _{IN} = 12V	-	-	1.0	mA
Receiver Input Current (A, B)			$V_{IN} = -7V$	-	-	-0.8	mA
Receiver Short Circuit Output Current	I _{OSR}	$0V \le V_{OUTR} \le V_{CC}$		7	-	60	mA
		No Load,	$\frac{DE}{RE} = V_{CC},$ RE = 0V or V _{CC}	-	425	2000	
Supply Current	Icc	$DI = 0V \text{ or } V_{CC}$	$\frac{DE}{RE} = 0V,$	-	300	1500	μA

Note 3. ΔV_{OD} and ΔV_{OC} are the changes in V_{OD} and V_{OC} , respectively, when the DI input changes state.



MAX3485EESA RS-485/RS-422 Transceivers

AC ELECTRICAL CHARACTERISTICS

 V_{CC} = 3.3V, T_A = 25°C, unless otherwise noted.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Maximum Data Rate		DE = \overline{RE} = V _{CC} or 0V	10	-	-	Mbps
Driver Differential Output Transition Time	t _{TD}	Figure 3	-	5	20	ns
Driver Input to Output Propagation Delay, Low-to-High Level	t _{PLH}	Figure 4	-	17	60	ns
Driver Input to Output Propagation Delay, High-to-Low Level	t _{PHL}	Figure 4	-	17	60	ns
Driver Propagation Delay Skew	t _{skew}	Figure 4, t _{PLH} – t _{PHL}	-	2	10	ns
Driver Output Enable Time to High	t _{PZH}	Figure 5	-	35	120	ns
Driver Output Enable Time to Low	t _{PZL}	Figure 6	-	30	120	ns
Driver Output Disable Time from High	t _{PHZ}	Figure 5	-	20	120	ns
Driver Output Disable Time from Low	t _{PLZ}	Figure 6	-	20	120	ns
Receiver Input to Output Propagation Delay, Low-to-High Level	t _{RPLH}	Figure 7	-	40	100	ns
Receiver Input to Output Propagation Delay, High-to-Low Level	t _{RPHL}	Figure 7	-	35	100	ns
Differential Receiver Propagation Delay Skew	t _{RSKEW}	Figure 7, t _{RPLH} – t _{RPHL}	-	4	-	ns
Receiver Output Enable Time to Low	t _{PRZL}	Figure 8	-	10	60	ns
Receiver Output Enable Time to High	t _{PRZH}	Figure 8	-	10	60	ns
Receiver Output Disable Time from Low	t _{PRLZ}	Figure 8	-	10	60	ns
Receiver Output Disable Time from High	t _{PRHZ}	Figure 8	-	10	60	ns

FUNCTION TABLE

Transmitting						
	Inputs	Outputs				
RE	DE	DI	В	А		
Х	1	1	0	1		
Х	1	0	1	0		
0	0	Х	Z	Z		
1	0	Х	Z	Z		

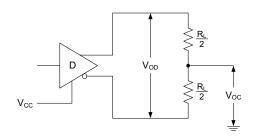
Receiving					
	Output				
RE	RE DE A-B				
0	0 0 ≥ +0.2V				
0	0 0 ≤ −0.2V				
0	0 0 Inputs Open				
1	1 0 X				

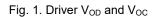
X: Don't Care

Z: High Impedance



PARAMETER MEASUREMENT INFORMATION





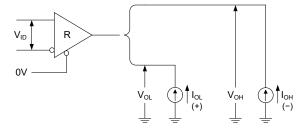


Fig. 2. Receiver V_{OH} and V_{OL}

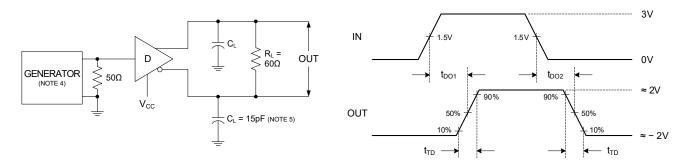


Fig. 3. Driver Differential Output Delay and Transition Times

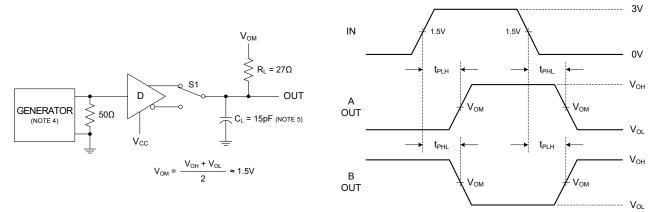


Fig. 4. Driver Propagation Delay



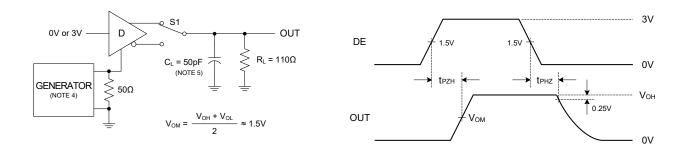


Fig. 5. Driver Enable and Disable Times, Output High

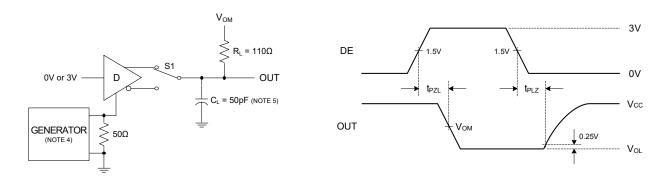


Fig. 6. Driver Enable and Disable Times, Output Low

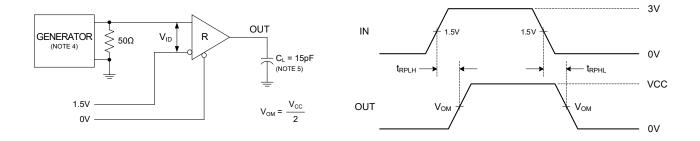


Fig. 7. Receiver Propagation Delay



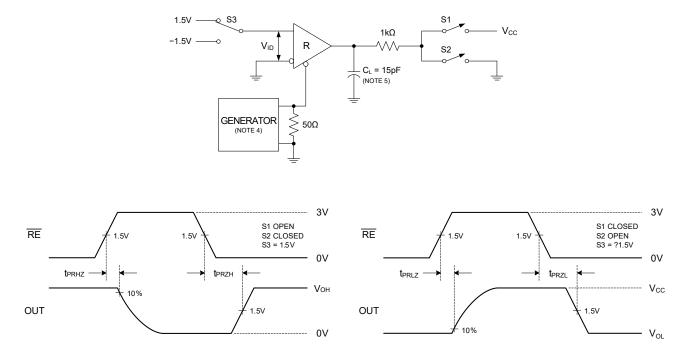


Fig. 8. Receiver Enable and Disable Times

- Note 4. The input pulse is supplied by a generator with the following characteristics: PRR = 250kHz, 50% duty cycle, $t_r \le 6.0$ ns, $Z_O = 50\Omega$.
- Note 5. C_L includes probe and stray capacitance.



APPLICATION INFORMATION

Functional Description

The MAX3485EESA is 3.3V low power half-duplex differential transceiver that meets the requirements of RS-485 and RS-422. The RS-485 Standard is ideal for multi-drop applications and for long-distance interfaces. RS-485 allows up to 32 drivers and 32 receivers to be connected to a data bus, making it an ideal choice for multi-drop applications. Since the cabling can be as long as 4,000 feet, RS-485 transceivers are equipped with a wide common mode range of -7V to 12V to accommodate ground potential differences. Because RS-485 is a differential interface, data is virtually immune to noise in the transmission line.

Output Enable

The receiver output RO is enabled when \overline{RE} is low. RO is high impedance when \overline{RE} is high. The driver outputs are enabled by bringing DE high. They are high impedance when DE is low. If the driver outputs are enabled, the parts function as line drivers. While they are high impedance, they function as line receivers if \overline{RE} is low.

Driver

The driver outputs of the MAX3485EESA are differential outputs meeting the RS-485 and RS-422 standards. The typical voltage output swing with no load will be 0 Volts to 3.3 Volts. With worst case loading of 54Ω across the differential outputs, the driver can maintain greater than 1.5V levels. The driver of the MAX3485EESA has an enable control line which is active high. A logic high on DE (pin 3) will enable the differential driver outputs. A logic low on DE (pin 3) will tri-state the driver outputs. The transmitter of the MAX3485EESA will operate up to 10Mbps.

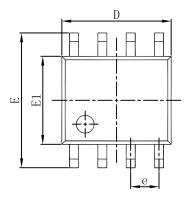
Receiver

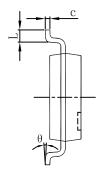
The MAX3485EESA receiver has differential inputs with an input sensitivity as low as ± 200 mV. Input impedance of the receiver is typically $15k\Omega$ ($12k\Omega$ minimum). A wide common mode range of -7V to + 12V allows for large ground potential differences between systems. The receiver of the MAX3485EESA has a tri-state enable control pin. A logic lowon \overline{RE} (pin 2) will enable the receiver, a logic high on \overline{RE} (pin 2) will disable the receiver. The receiver for the MAX3485EESA will operate up to 10Mbps. The

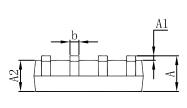
receiver is equipped with the fail-safe feature. Fail-safe feature guarantees that the receiver output will be in a high state when the input is left unconnected.



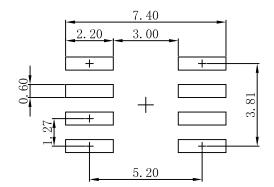
SOP-8 Package Outline Dimensions







Symbol	Dimensions In	Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
с	0.170	0.250	0.007	0.010	
D	4.800	5.000	0.189	0.197	
e	1.270 (BSC)	0.050		
Е	5.800	6.200	0.228	0.244	
E1	3.800	4.000	0.150	0.157	
L	0.400	1.270	0.016	0.050	
θ	0 °	8°	0 °	8°	



Note: 1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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