

Description

The SI2300 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

 $V_{DS} = 20V I_D = 6.0A$ $R_{DS(ON)} < 27m\Omega W_{GS} = 4.5V$

Application

Battery protection Load switch Uninterruptible power supply





PIN2 D PIN1 G PIN3 S

N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
SI2300	SOT-23	2300	3000

Absolute Maximum Ratings (T_A=25[°]C unless otherwise noted)

Parameter	Limit	Unit
Drain-Source Voltage	20	V
Gate-Source Voltage	±12	V
Drain Current-Continuous	6	A
Drain Current-Pulsed (Note 1)	25	A
Maximum Power Dissipation	0.35	W
Operating Junction and Storage Temperature Range -55 To 150		°C
Thermal Resistance, Junction-to-Ambient (Note 2)	100	°C/W
-	Drain-Source Voltage Gate-Source Voltage Drain Current-Continuous Drain Current-Pulsed (Note 1) Maximum Power Dissipation Operating Junction and Storage Temperature Range	Drain-Source Voltage20Gate-Source Voltage±12Drain Current-Continuous6Drain Current-Pulsed (Note 1)25Maximum Power Dissipation0.35Operating Junction and Storage Temperature Range-55 To 150



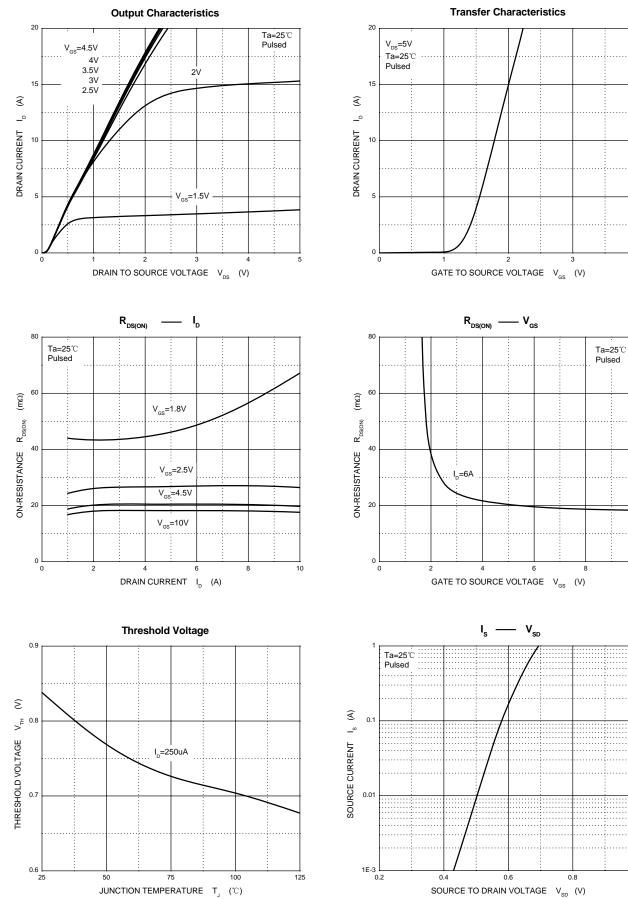
$T_a {=} 25\,^\circ\! {C}\, unless$ otherwise specified

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
STATIC PARAMETERS							
Drain-source breakdown voltage	V (BR) DSS	Vgs = 0V, Id =250µA	20			V	
Gate-source leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA	
Zero gate voltage drain current	I _{DSS}	VDS =16V, VGS =0V			1.0	μA	
Gate threshold voltage	VGS(th)	V _{DS} =V _{GS} , I _D =250µA	0.5	0.7	1.0	V	
Drain-source on-state resistance	RDS(on)	Vgs =4.5V, Id =5.0A		22	27		
		Vgs =2.5V, Id =4.0A		35	42	mΩ	
		Vgs =1.8V, Id =2.0A			73		
Diode forward voltage	V _{SD}	V _{GS} =0V,I _S =1A		0.75	1	V	
Forward transconductance	g _{fS}	V _{DS} =5V, I _D =3.8A	4			S	
DYNAMIC PARAMETERS*							
Input capacitance	Ciss			630		pF	
Output capacitance	C _{oss}	VDS =10V,VGS =0V,f =1MHz		164			
Reverse transfer capacitance	C _{rss}			137			
Gate resistance	Rg	VDS =0V,VGS =0V,f =1MHz		1.5		Ω	
SWITCHING PARAMETERS*							
Turn-on delay time	td(on)			5.5			
Rise time	tr	V _{GS} =5V,V _{DS} =10V,		14			
Turn-off delay time	td(off)	$R_L=1.7\Omega, R_{GEN}=6\Omega$		29		ns	
Fall time	tr]		10.2			

*These parameters have no way to verify.



Typical Characteristics

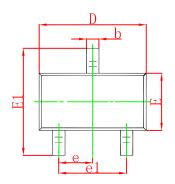


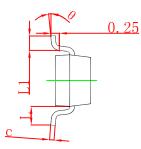
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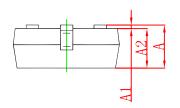
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SOT-23 Package Outline Dimensions

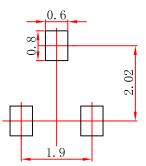






Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

SOT-23 Suggested Pad Layout



Note: 1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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