

Description

The SI7848BDP-T1-E3 uses advanced trench technology

to provide excellent $R_{DS(ON)}$, low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

General Features

V_{DS} = 40V I_D =60 A

 $R_{DS(ON)}$ < 8.5m Ω @ V_{GS}=10V

Application

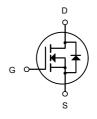
Battery protection

Load switch

Uninterruptible power supply



DFN5X6-8L



N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
SI7848BDP-T1-E3	DFN5X6-8L	HXY MOSFET	5000

Absolute Maximum Ratings (T_C=25[°]Cunless otherwise noted)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	40	V	
Vgs	Gate-Source Voltage	±20	V	
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	А		
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	35	А	
Ідм	Pulsed Drain Current ²	105	А	
EAS	Single Pulse Avalanche Energy ³	48	mJ	
las	Avalanche Current	35	А	
P _D @T _C =25°C	Total Power Dissipation ⁴	ipation ⁴ 39		
Тѕтс	Tstg Storage Temperature Range -55 to 150		°C	
T _J Operating Junction Temperature Range		-55 to 150	°C	
R _{BJA} Thermal Resistance Junction-ambient (Steady State) ¹		62	°C/W	
Rejc	Thermal Resistance Junction-Case ¹	3.2	°C/W	



Electrical Characteristics (T = 25 , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	40			V	
Rds(on)		V _{GS} =10V , I _D =10A		7	8.5	mΩ	
	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =5A		10	15		
VGS(th)	Gate Threshold Voltage	Gate Threshold Voltage V _{GS} =V _{DS} , I _D =250uA		1.7	3	V	
	Drain-Source Leakage Current	V _{DS} =32V , V _{GS} =0V , T _J =25°C			1	uA	
Ipss		V _{DS} =32V , V _{GS} =0V , T _J =55°C			5		
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA	
gfs	Forward Transconductance	V _{DS} =10V , I _D =5A		27		S	
Qg	Total Gate Charge (4.5V)			20			
Qgs	Gate-Source Charge	V _{DS} =20V , V _{GS} =4.5V , I _D =10A		5.8		nC	
Qgd	Gate-Drain Charge			9.5			
Td(on)	Turn-On Delay Time			15.2			
Tr	Rise Time	V _{DD} =15V , V _{GS} =10V		8.8			
Td(off)	Turn-Off Delay Time			74		ns	
Tf	Fall Time	ID- IA		7			
Ciss	Input Capacitance			690			
Coss	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		193		pF	
Crss	Reverse Transfer Capacitance			38			
ls	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current			70	Α	
VsD	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C			1	V	

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width \leqq 300us , duty cycle \leqq 2%
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V,L=0.1mH,I_{AS}=47A
- 4.The power dissipation is limited by 150 $^{\circ}\mathrm{C}$ junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

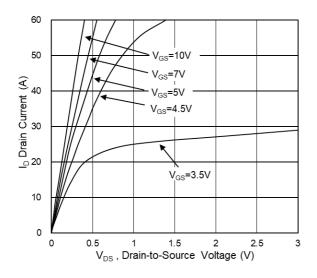


Fig.1 Typical Output Characteristics

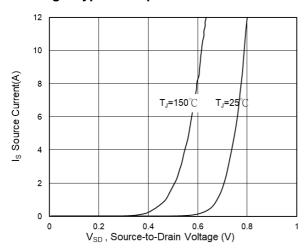


Fig.3 Source Drain Forward Characteristics

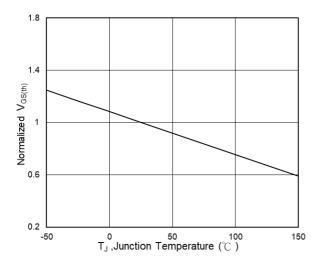


Fig.5 Normalized $V_{\text{GS(th)}}$ vs T_{J}

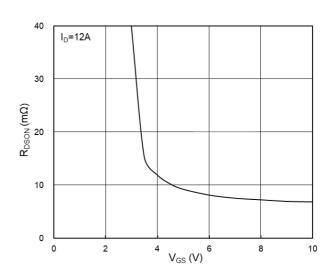


Fig.2 On-Resistance vs G-S Voltage

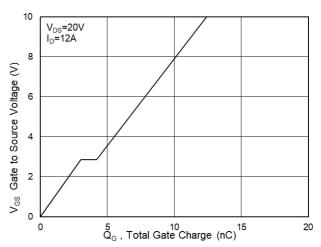


Fig.4 Gate-Charge Characteristics

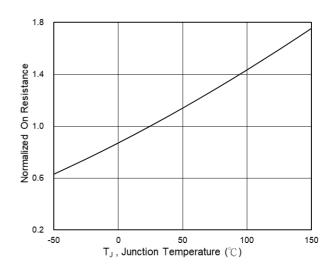
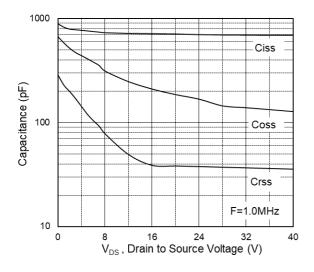


Fig.6 Normalized R_{DSON} vs T_J



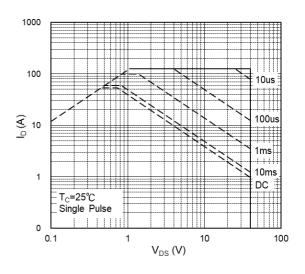
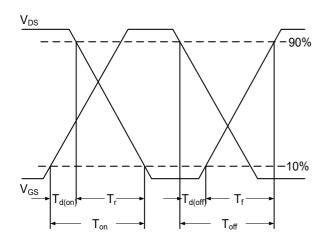


Fig.7 Capacitance Fig.8 Safe Operating Area Normalized Thermal Response (Reuc) DUTY=0.5 0.3 0.1 0.05 0.02 0.01 $D = T_{ON}/T$ SINGLE PUL T_J peak = $T_C + P_{DM} x R_{\theta JC}$ 0.01 0.00001 0.0001 0.001 0.01 0.1 t, Pulse Width (s)

Fig.9 Normalized Maximum Transient Thermal Impedance





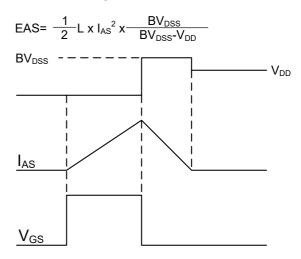
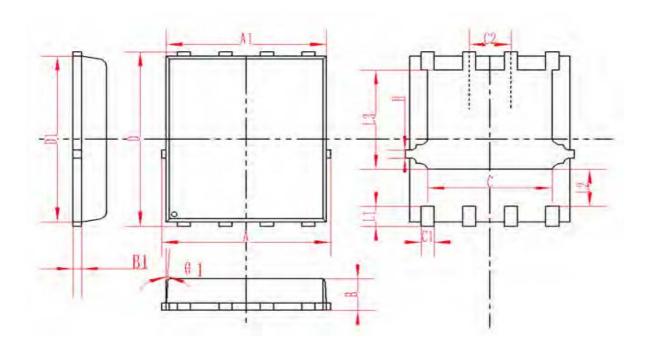


Fig.11 Unclamped Inductive Waveform



DFN5X6-8L Package Information



SYMBOL	MM		INCH			
	MIN	NOM	MAX	MIN	NOM	MAX
Α	5.3	5.5	5.7	0.208	0.216	0.224
A1	5.1	5.2	5.3	0.2	0.204	0.209
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.85	6.05	6.25	0.23	0.238	0.246
В	0.85	0.95	1.05	0.033	0.037	0.041
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP		0.5TYP			
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010

N-Channel Enhancement Mode MOSFET

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