

#### Description

The SI7848BDP-T1-GE3 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a

Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 40V I<sub>D</sub> =60 A

 $R_{DS(ON)} < 8.5 m\Omega @ V_{GS} = 10V$ 

#### Application

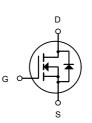
Battery protection

Load switch

Uninterruptible power supply



DFN5X6-8L



N-Channel MOSFET

#### **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
SI7848BDP-T1-GE3	DFN5X6-8L	HXY MOSFET	5000

#### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
Vds	Drain-Source Voltage	40	V
Vgs	Gate-Source Voltage	±20	V
I₀@Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	А	
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	35	А
Ідм	Pulsed Drain Current <sup>2</sup>	105	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	48	mJ
las	Avalanche Current	35	А
P₀@Tc=25°C	Total Power Dissipation <sup>4</sup>	39	W
Тѕтс	Storage Temperature Range	Storage Temperature Range -55 to 150	
TJ	Operating Junction Temperature Range -55 to 15		°C
R <sub>0JA</sub>	R <sub>0JA</sub> Thermal Resistance Junction-ambient (Steady 62 State) <sup>1</sup>		°C/W
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	3.2	°C/W



# SI7848BDP-T1-GE3

N-Channel Enhancement Mode MOSFET

Symbol	Parameter         Conditions           Drain-Source Breakdown Voltage         VGS=0V , ID=250uA		Min.	Тур.	Max.	Unit	
BVDSS			40			V	
Rds(on)		V <sub>GS</sub> =10V , I <sub>D</sub> =10A		7	8.5	mΩ	
	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		10	15		
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		1.0	1.7	3	V	
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =32V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	– uA	
		V <sub>DS</sub> =32V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5		
lgss	Gate-Source Leakage Current	V <sub>GS=</sub> ±20V , V <sub>DS</sub> =0V			±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =5A		27		S	
Qg	Total Gate Charge (4.5V)			20			
Qgs	Gate-Source Charge	V <sub>DS</sub> =20V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		5.8		nC	
Qgd	Gate-Drain Charge			9.5			
Td(on)	Turn-On Delay Time			15.2			
Tr	Rise Time	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V		8.8		ns	
Td(off)	Turn-Off Delay Time	- R <sub>G</sub> =3.3 Ω I <sub>D</sub> =1A		74			
T <sub>f</sub>	Fall Time			7			
Ciss	Input Capacitance			690			
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		193		pF	
Crss	Reverse Transfer Capacitance	-		38			
ls	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			70	A	
Vsd	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	V	

### Electrical Characteristics (T = 25 , unless otherwise noted)

Note :

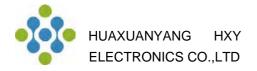
1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width  $\leqq$  300us , duty cycle  $\leqq$  2%

3. The EAS data shows Max. rating . The test condition is  $V_{DD}=25V$ ,  $V_{GS}=10V$ , L=0.1 mH,  $I_{AS}=47$  A

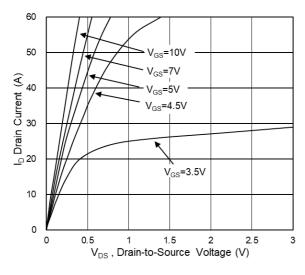
4.The power dissipation is limited by 150  $^\circ\!\mathrm{C}$  junction temperature

5.The data is theoretically the same as  $I_{\text{D}}$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.



# SI7848BDP-T1-GE3 N-Channel Enhancement Mode MOSFET

## **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

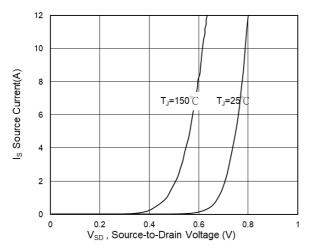


Fig.3 Source Drain Forward Characteristics

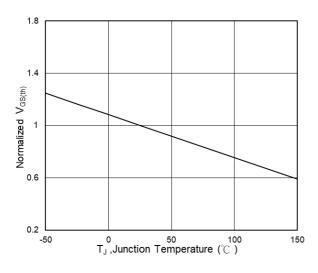


Fig.5 Normalized  $V_{GS(th)}\,vs\,T_J$ 

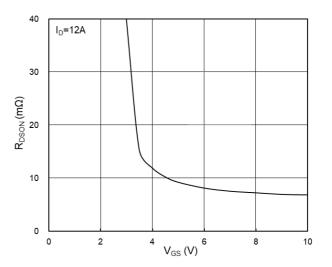


Fig.2 On-Resistance vs G-S Voltage

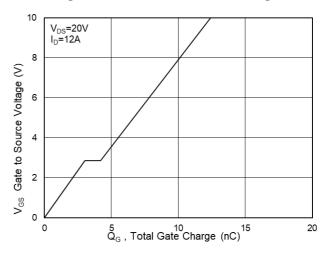


Fig.4 Gate-Charge Characteristics

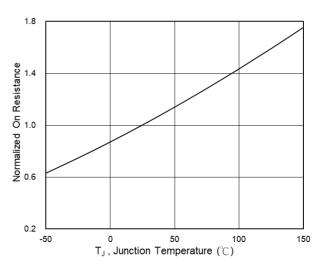
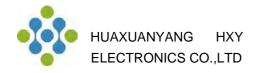


Fig.6 Normalized R<sub>DSON</sub> vs T<sub>J</sub>



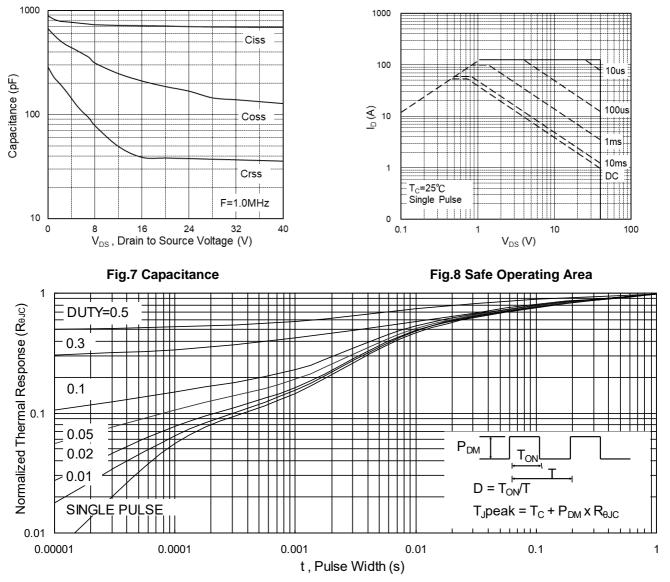


Fig.9 Normalized Maximum Transient Thermal Impedance

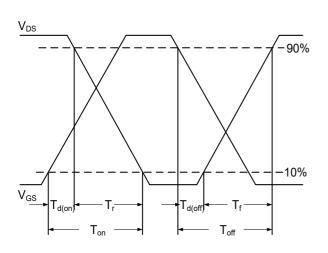


Fig.10 Switching Time Waveform

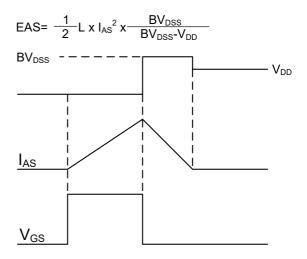
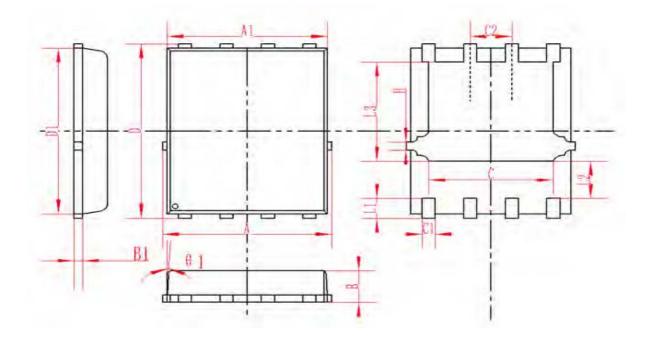


Fig.11 Unclamped Inductive Waveform



# DFN5X6-8L Package Information



SYMBOL	MM		INCH			
	MIN	NOM	MAX	MIN	NOM	MAX
А	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP		0.5TYP			
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010



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