

General Description

The SISB46DN-T1-GE3 use advanced SGT MOSFET technology to

provide low RDS(ON), low gate charge, fast switching

and excellent avalanche characteristics.

This device is specially designed to get better ruggedness

and suitable.

General Features

V_{DS} =40V I_D =40A

 $R_{DS(ON)} < 9.5m\Omega@V_{GS}=10V$

Applications

Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications

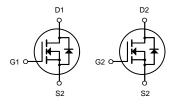
Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
SISB46DN-T1-GE3	DFN3X3-8L	HXY MOSFET	5000

Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	VDS	40	V
Gate source voltage	VGS	±20	V
Continuous drain current ¹⁾	ID	40	А
Pulsed drain current ²⁾	ID, pulse	180	А
Power dissipation ³⁾	PD	43.6	W
Single pulsed avalanche energy ⁵⁾	EAS	26.1	mJ
Operation and storage temperature	Tstg, Tj	-55 to 150	°C
Thermal resistance, junction-case	RθJC	2.8	°C/W
Thermal resistance, junction-ambient ⁴⁾	RθJA	62	°C/W





Dual N-Channel MOSFET

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I _D =250uA	40			V
D	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =12A		7.2	9.5	
Rds(on)	Static Drain-Source On-Resistance-	V _{GS} =4.5V , I _D =10A		10.0	15	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.35		3	V
laco	Drain Source Leakage Current	V_{DS} =32V , V_{GS} =0V , T_{J} =25°C			1	
IDSS	Drain-Source Leakage Current	V _{DS} =32V , V _{GS} =0V , T _J =55℃			5	uA
lgss	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$			±100	nA
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.7		Ω
Qg	Total Gate Charge (4.5V)			5.8		
Qgs	Gate-Source Charge	V_{DS} =20V , V_{GS} =4.5V , I_{D} =12A		3		nC
Q_{gd}	Gate-Drain Charge			1.2		
T _{d(on)}	Turn-On Delay Time			14.3		
Tr	Rise Time	V_{DD} =15V , V_{GS} =10V , R_{G} =3.3 Ω		5.6		
T _{d(off)}	Turn-Off Delay Time	I _D =1A		20		ns
T _f	Fall Time			11		
Ciss	Input Capacitance			690		
Coss	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		193		pF
Crss	Reverse Transfer Capacitance			38		
ls	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current			40	А
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃			1	V

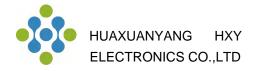
Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%

3. The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS}=31A

4. The power dissipation is limited by 150°C junction temperature 5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



SISB46DN-T1-GE3 Dual N-SGT Enhancement Mode MOSFET

Typical Characteristics

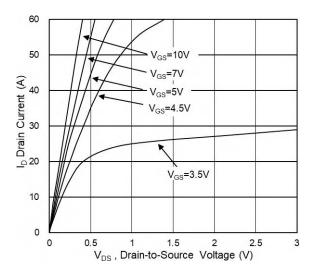


Fig.1 Typical Output Characteristics

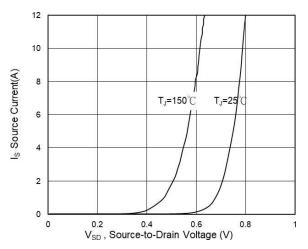


Fig.3 Source Drain Forward Characteristics

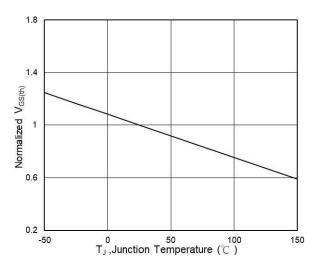


Fig.5 Normalized $V_{\text{GS}(\text{th})}\, vs\; T_J$

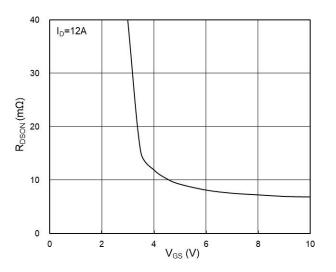


Fig.2 On-Resistance vs G-S Voltage

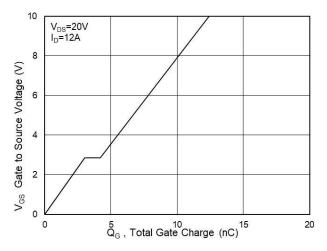


Fig.4 Gate-Charge Characteristics

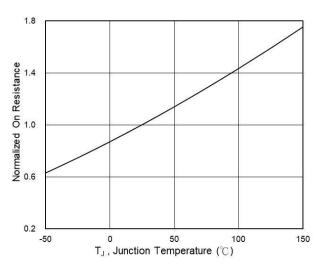
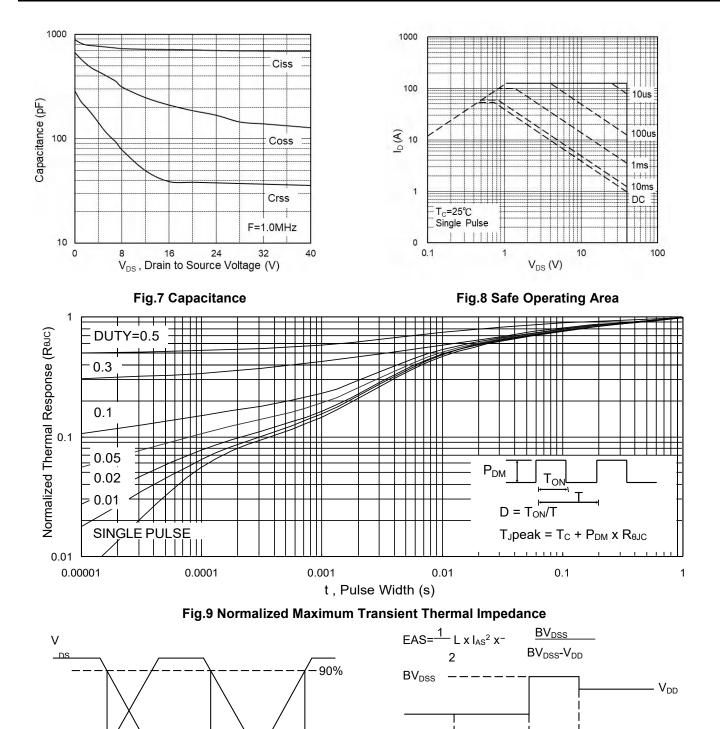


Fig.6 Normalized RDSON vs TJ



SISB46DN-T1-GE3 Dual N-SGT Enhancement Mode MOSFET



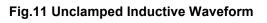


 V_{GS}

Т

 $\mathsf{T}_{\mathsf{off}}$

T_{d(off}



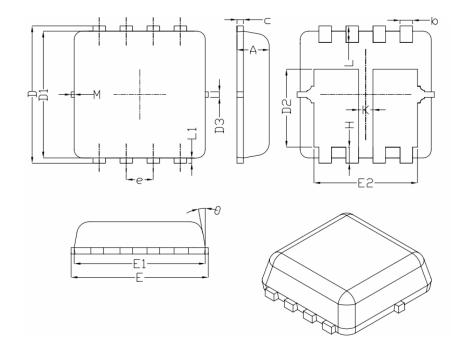
 T_{on}

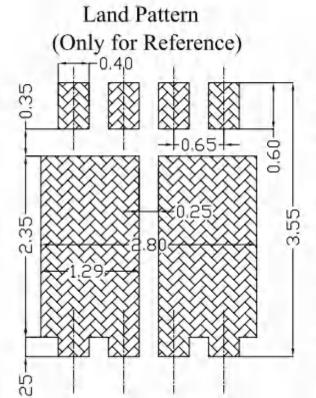
 \bar{V}_{GS}

T_{d(or}



DFN3X3-8L Package Information





	DIMENSIONAL REOMTS		
SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
b	0.25	0.30	0.35
Ċ	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3		0.13	++-
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
е	A	0.65BSC	
H	0.30	0.39	0.50
L	0.30	0.40	0.50
LI		0.13	
K	0.30		
θ		10ª	120
M	-ske	*	0.15



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