

DESCRIPTION

The ST3232BDR is a 3 V powered EIA/TIA-232 and V.28/V.24 communication interfaces with low power requirements, high data-rate capabilities and enhanced electrostatic discharge (ESD) protection to \pm 8 kV using IEC1000-4-2 contact discharge and \pm 15 kV using the human body model. The ST3232E has a proprietary lowdropout transmitter output stage providing true RS-232 performance from 3 to 5 V supplies with a dual charge pump.

The device is guaranteed to run at data rates of 250 kbps while maintaining RS-232 output levels.

FEATURES

- ESD protection for RS-232 I/O pins
- ±15 kV human body model
- ±8 kV IEC 1000-4-2 contact discharge
- 300 µA supply current
- 250 kbps minimum guaranteed data rate
- 6 V/µs minimum guaranteed slew rate
- Meet EIA/TIA-232 specifications down to 3 V
- Available in SOP-16

ORDERING INFORMATION



APPLICATIONS

- Notebook, subnotebook and palmtop computers
- Battery-powered equipment
- Hand-held equipment
- Peripherals and printers

PackageOder No.ComplianceSupplied AsSOP-16ST3232BDRRoHS, GreenTube



PIN CONFIGURATION



PIN DESCRIPTION

Pin No.	Pin Name	Pin Description	
1	C1+	Positive terminal for the first charge pump capacitor	
2	V+	Doubled voltage terminal	
3	C1-	Negative Terminal for the first charge pump capacitor	
4	C2+	Positive terminal for the second charge pump capacitor	
5	C2-	Negative terminal for the second charge pump capacitor	
6	V-	Inverted voltage terminal	
7	T2OUT	Second transmitter output voltage	
8	R2IN	Second receiver input voltage	
9	R2OUT	Second receiver output voltage	
10	T2IN	Second transmitter input voltage	
11	T1IN	First transmitter input voltage	
12	R10UT	First receiver output voltage	
13	R1IN	First receiver input voltage	
14	T1OUT	First transmitter output voltage	
15	GND	Ground	
16	VCC	Supply voltage	



SPECIFICATIONS

Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	
Supply Voltage	V _{CC}	-0.3	6.0	V	
Transmitter High Output Voltage	V ₊	V _{CC} -0.3	7.0	V	
Transmitter Low Output Voltage	V.	-0.3	-14	V	
Transmitter Input Voltage	V _{TIN}	-0.3	V++6	V	
Receiver Input Voltage	V _{RIN}	-25	25	V	
Voltage Applied to Transmitter Output	V _{TOUT}	V ₋ -13.2	V ₊ +12.2	V	
Voltage Applied to Receiver Output	V _{ROUT}	-0.3	V _{CC} +0.3	V	
Storage Temperature Range	T _{STG}	-65	150	°C	
Thermal resistance junction-to-case ^{(1) (2)}	R _{th-jc}	30		*CAN	
Thermal resistance junction-to-ambient (1) (2)	R _{th-ja}	9	5	C/W	

1. Short-circuits can cause excessive heating and destructive dissipation.

2. R_{th} are typical values.

ESD protection

PIN	TEST CONDITIONS	TYP	UNIT
DOUT, RIN	Human-Body Model	±15	kV
D _{OUT} , R _{IN}	IEC-1000-4-2	±8	kV

Electrical Characteristics

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	ТҮР(1)	MAX	UNIT
ISUPPLY	VCC Power supply current	No load, $V_{CC} = 3V \text{ or } 5V$		0.3	1	mA
1 All two and the set $T_{\rm r} = 25^{\circ}$						

(1) All typical values are at $T_A = 25^{\circ}C$.

Logic input

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{TIL}	Input logic threshold low	T-IN			0.8	V
V _{HYS}	Transmitter input hysteresis			0.25		V
IIL	Input leakage currentT-IN			±0.01	±1	V
N.	Input logic threshold high	V _{CC} = 3.3 V	2			V
VTIH		V _{CC} = 5 V	2.4			v



Transmitter

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{TOUT}	Output voltage swing	All transmitter outputs are loaded with 3 k $\!\Omega$ to GND	±5	±5.4		V
R _{TOUT}	Transmitter output resistance	Driver high-level input voltage (D _{IN})	300	10M		W
I _{SC}	Output short-circuit current			±60		mA
I _{TOL}	Output leakage current	V _{CC} = 0 V or 3.3 V or 5.5 V, V _{CC} =±12 V Transmitters disable			±25	uA

Timing characteristics

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	ТҮР	MAX	UNIT
D _R	Data transfer rate	$R_L = 3 k\Omega$, $C_{L2}= 1000 pF$ one transmitter switching	250			kbps
t _{PHLR} t _{PLHR}	Propagation delay input to output	$R_{XIN} = R_{XOUT}, C_L = 150 \text{ pF}$		0.15		μs
t _{OER}	Receiver output enable time	Normal operation		50		ns
t _{ODR}	Receiver output disable time	Normal operation		50		ns
t _{PHLT} - t _{THL}	Transmitter propagation delay difference	(1)		200		ns
t _{PHLR} - t _{THR}	Receiver propagation delay difference			50		ns
S _{RT}	Transition slew rate	$\begin{array}{l} {\sf T}_{\sf A} = 25 \ ^\circ {\sf C} {\sf R}_{\sf L} = 3 \ {\sf k}\Omega \ {\sf to} \ 7 \ {\sf k}\Omega \ {\sf V}_{\sf CC} = \\ {\sf 3.3 \ {\sf V}} \ {\sf measured from + 3 \ {\sf V}} \ {\sf to} \ -3 \ {\sf V} \ {\sf or} \ -3 \ {\sf V} \\ {\sf to} \ +3 \ {\sf V} \\ {\sf C}_{\sf L} = 150 \ {\sf pF} \ {\sf to} \ 1000 \ {\sf pF} \\ {\sf C}_{\sf L} = 150 \ {\sf pF} \ {\sf to} \ 2500 \ {\sf pF} \end{array}$	6 4		30 30	V/µs V/µs

(1) Transmitter skew is measured at the transmitter zero-cross points.

Receiver

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	ТҮР	МАХ	UNIT
V _{RIN}	Receiver input voltage operating range		-25		25	V
V	RS-232 input threshold low	$T_A = 25 \text{ °C}, V_{CC} = 3.3 \text{ V}$	0.6	1.1		V
^V RIL		$T_A = 25 \text{ °C}, V_{CC} = 5 \text{ V}$	0.8	1.5		
V _{RIH}	RS-232 input threshold high	$T_A = 25 \text{ °C}, V_{CC} = 3.3 \text{ V}$		1.4	2.4	V
		$T_A = 25 \text{ °C}, V_{CC} = 5 \text{ V}$		1.8	2.4	
V _{RIHYS}	Input hysteresis			0.5		V
R _{RIN}	Input resistance	T _A = 25 °C	3	5	7	kΩ
V _{ROL}	TTL/CMOS Output voltage low	I _{OUT} = 1.6 mA			0.4	V
V _{ROH}	TTL/CMOS Output voltage high	I _{OUT} = -1 mA	V _{CC} -0.6	V _{CC} -0.1		V



APPLICATION Application circuits





TYPICAL PERFORMANCE CHARACTERISTICS





Output current vs. output low voltage



Receiver input resistance



Output current vs. output high voltage



PACKAGE OUTLINE DIMENSIONS

SOP-16





SYMPOL		MILLIMETER					
STWBOL	MIN	NOM	МАХ				
A	1.50	1.60	1.70				
A1	0.10	0.15	0.25				
A2	1.40	1.45	1.50				
A3	0.60	0.65	0.70				
b	0.30	0.40	0.50				
с	0.15	0.20	0.25				
D	9.80	9.90	10.00				
E	5.80	6.00	6.20				
E1	3.85	3.90	3.95				
e	1.27BSC						
L	0.50	0.60	0.70				
L1		1.05BSC					
θ	0°	4°	8°				



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