

## **General Description**

The TPN6R303NC use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness and suitable

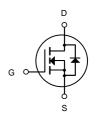


DFN3X3-8L

#### **General Features**

 $V_{DS} = 30V$   $I_{D} = 62A$ 

 $R_{DS(ON)}$  < 6.3m $\Omega$ @  $V_{GS}$ =10V



#### N-Channel MOSFET

### **Applications**

Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

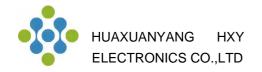
Synchronous-rectification applications

# Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
TPN6R303NC	DFN3X3-8L	HXY MOSFET	5000

## Absolute Maximum Ratings (TC=25°C unless otherwise specified)

Symbol	Parameter	Rating	Units
V <sub>D</sub> s	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	62	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	38	Α
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	27	Α
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	24	Α
Ірм	Pulsed Drain Current <sup>2</sup>	135	Α
EAS	Single Pulse Avalanche Energy <sup>3</sup>	29.8	mJ
las	Avalanche Current	27	Α
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	30	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
Reja	Thermal Resistance Junction-ambient <sup>1</sup>	50	°C/W
Rejc	Thermal Resistance Junction-Case <sup>1</sup> 4.6		°C/W



## ElectricalCharacteristics(TJ=25°Cunlessotherwisenoted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
D	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		5	6.3	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		6.9	9	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	1.2		2.5	V
1	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
IDSS		V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			5	
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A		67		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.7		Ω
Qg	Total Gate Charge (4.5V)			8		
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		2.4		nC
Q <sub>gd</sub>	Gate-Drain Charge			3.2		
$T_{d(on)}$	Turn-On Delay Time			7.1		
Tr	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =3.3 $\Omega$		40		ns
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =15A		15		
T <sub>f</sub>	Fall Time			6		
C <sub>iss</sub>	Input Capacitance			814		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		498		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			41		
Is	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			62	Α
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	V
t <sub>rr</sub>	Reverse Recovery Time	IF=20A , di/dt=100A/μs ,		15		nS
$Q_{rr}$	Reverse Recovery Charge	TJ=25℃		25		nC

#### Note

<sup>1.</sup> The data tested by surface mounted on a 1 inch $^2\,\text{FR-4}$  board with 2OZ copper.

<sup>2.</sup>The data tested by pulsed , pulse width  $\leqq$  300us , duty cycle  $\leqq$  2%

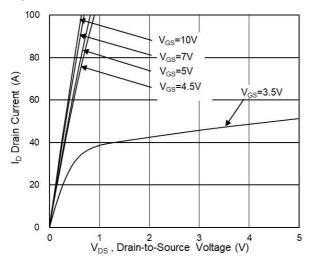
<sup>3.</sup> The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH, $I_{AS}$ =24A

<sup>4.</sup> The power dissipation is limited by 150℃ junction temperature

<sup>5.</sup> The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



### **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

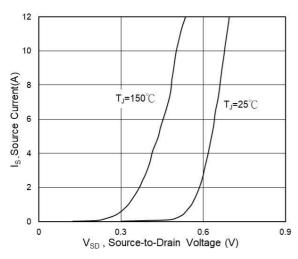


Fig.3 Source Drain Forward Characteristics

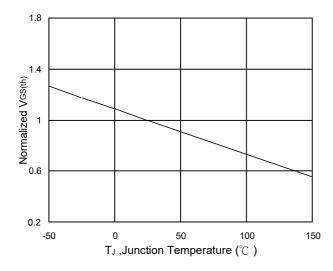


Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$ 

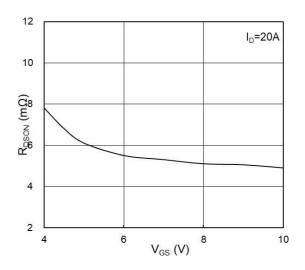


Fig.2 On-Resistance vs G-S Voltage

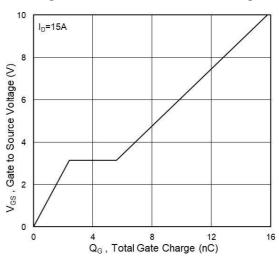


Fig.4 Gate-Charge Characteristics

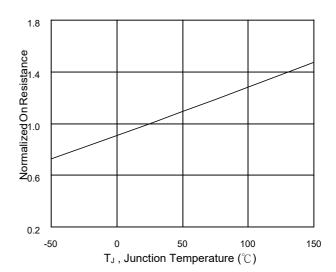
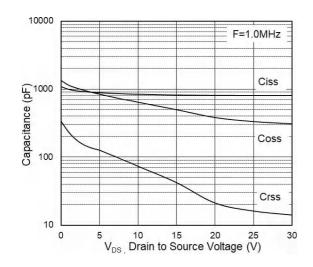


Fig.6 Normalized RDSON vs TJ



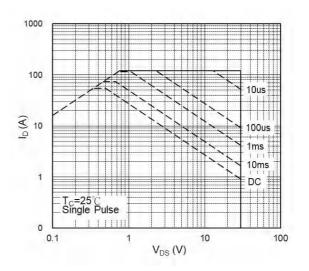


Fig.7 Capacitance

Fig.8 Safe Operating Area

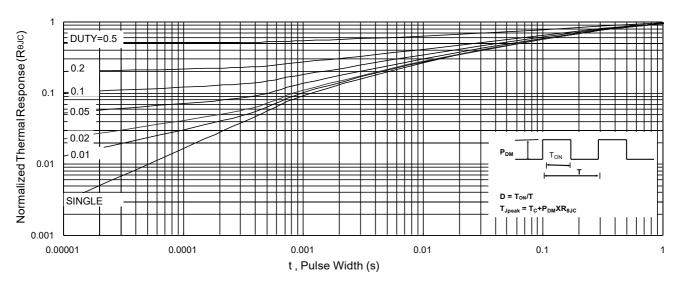


Fig.9 Normalized Maximum Transient Thermal Impedance

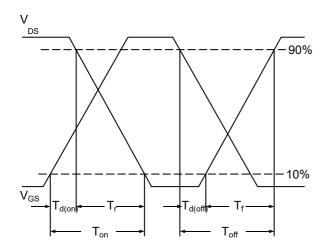


Fig.10 Switching Time Waveform

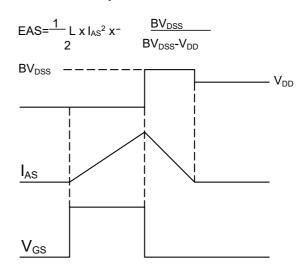
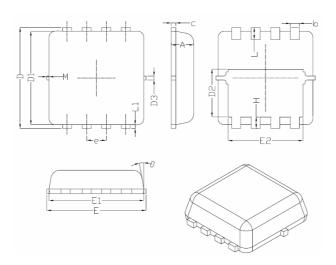


Fig.11 Unclamped Inductive Switching Waveform

## **DFN3X3-8L Package Information**



Complete I	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65	BSC		
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
M	*	*	0.15	
θ		10 <sup>°</sup>	12 <sup>°</sup>	



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