

#### Description

The VN2110 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , This device is suitable for use as a load switch or in PWM applications.

#### **General Features**

 $V_{DS} = 100V, I_D = 0.17A$  $R_{DS(ON)} < 6 \ \Omega @ V_{GS} = 10V$ ESD Rating: 1500V HBM

### Application

Battery protection

Load switch

Uninterruptible power supply

#### Package Marking and Ordering Information

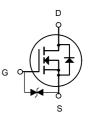
Product ID	Pack	Brand	Qty(PCS)
VN2110	SOT-23	HXY MOSFET	3000

#### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Limit	Unit
VDS	Drain-Source Voltage	100	V
Vgs	Gate-Source Voltage	±20	V
ID	Drain Current-Continuous	0.17	A
Ы	Drain Current-Pulsed (Note 1)	0.68	Α
PD	Maximum Power Dissipation	0.35	W
Tj,Tstg	Operating Junction and Storage Temperature Range	-55 To 150	°C
Reja	Thermal Resistance, Junction-to-Ambient (Note 2)	350	°C/W







N-Channel MOSFET



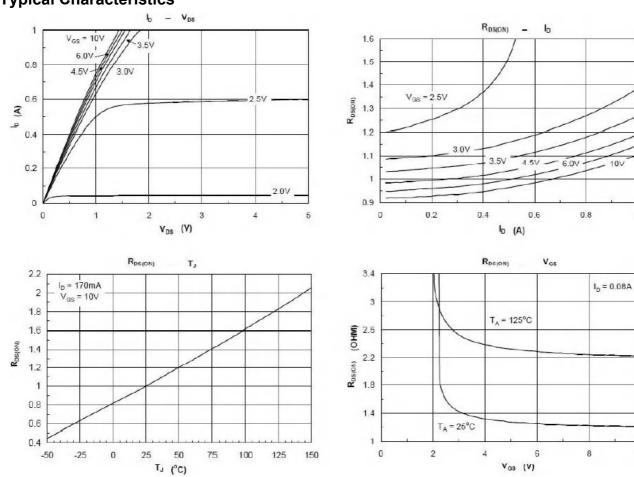
## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Test conditions	Мn	Тур	Max	Unit
Static						
V <sub>(BR)DSS</sub>	Drain-source breakdown voltage	V <sub>GS</sub> =0, I <sub>D</sub> =250µA 100			V	
V <sub>GS(th)</sub>	Gate threshold voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA 1.5		2.5	V	
I <sub>GSS</sub>	Gate-body leakage current	V <sub>DS</sub> =0, V <sub>GS</sub> =±20V		±10	μA	
DSS	Zero gate voltage drain current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	μA
R <sub>DS(on)</sub>	Drain-source on-resistance <sup>a</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =0.17A			6.0	Ω
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.17A			9.0	Ω
Vsd	Diode forward voltage	I <sub>S</sub> =0.2A,V <sub>GS</sub> =0V			1.0	V
Dynamic						
Ciss	Input capacitance			30		
Coss	Output capacitance	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz		10		pF
Crss	Reverse transfer capacitance <sup>b</sup>	_		7		
Switching	b					
t <sub>d(on)</sub>	Turn-on delay time			1.7		
tr	Rise time	V <sub>GS</sub> =10V,V <sub>DS</sub> =50V		9		nS
t <sub>d(off)</sub>	Turn-off delay time	I <sub>D</sub> =200mA, R <sub>GEN</sub> =6Ω		17		
t <sub>f</sub>	Fall time			7		1

Notes :

a. Pulse Test : Pulse width≤300µs, duty cycle ≤2%.

b. Guaranteed by design, not subject to producting.



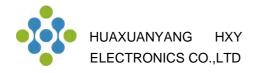
## **Typical Characteristics**

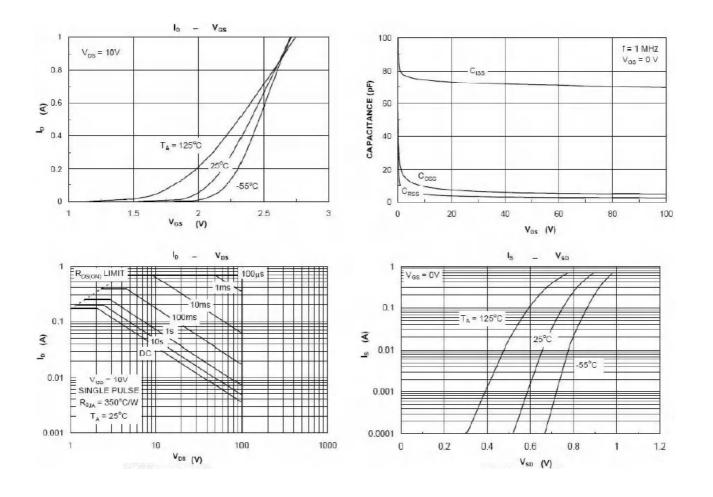
Shenzhen HuaXuanYang Electronics CO.,LTD

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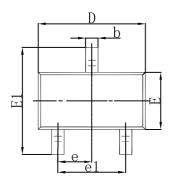
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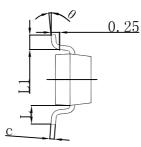


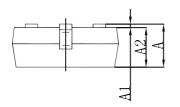




## **SOT-23 Package Outline Dimensions**

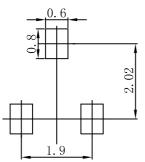






Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
e	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

# SOT-23 Suggested Pad Layout



Note: 1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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