

版次:第3版

N A	MULTILAYER CHIP INDUCTORS	СОМРО	SITE SPECIFICATION	1/
M E	EBLS-201212	SPEC#	EBLS2012-101K	8

1. SCOPE

This specification applies to the EBLS-2012 series Multilayer Chip Inductors

2. STANDARD ATMOSPHERIC CONDITIONS

Unless otherwise specified the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature : $20\pm15^{\circ}$ C Relative humidity : $30\sim70\%$

If there may be any doubt on the results, measurements shall be made within

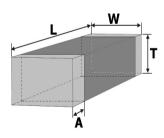
the following limits:

Ambient temperature : 25±5°C Relative humidity : 30~70%

3. RATINGS

PART NO	INDUCTANCE	Q	SELF-RESONANT FREQUENCY(MHz)		RATED CURRENT
	AT 1 MHz 60mV	Min	Min	(Ω) Max	(mA)Max
EBLS2012-101K	100 μ H±10%	25	7	3.1	2

4. DIMENSION



OPERATING TEMP. RANGE : -40° C ~ $+85^{\circ}$ C STORAGE TEMP. RANGE : -10° C ~ $+40^{\circ}$ C

TYPE I		14/	т	A (100 /100)
ITPE	L	VV	I	A(m/m)
EBLS-2012	2.0±0.2	1.25±0.2	1.25±0.2	0.2~0.8

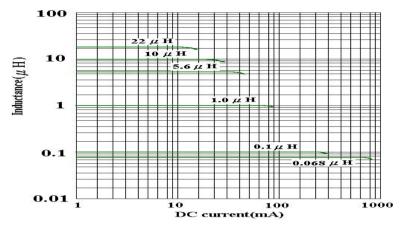
HISTORY	DATE	REVISION		SIGN.	SIGN.
PLANNED BY	CHECKED BY	APPROVED BY			
JSHING	HSU	Chi Chi Huang			



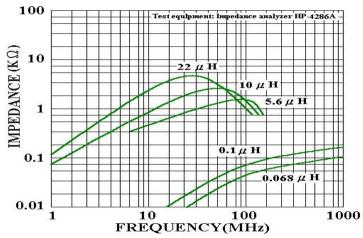
版次:第3版

N A	MULTILAYER CHIP INDUCTORS	COMPO	SITE SPECIFICATION	2/
M E	EBLS-201212	SPEC#	EBLS2012-101K	8

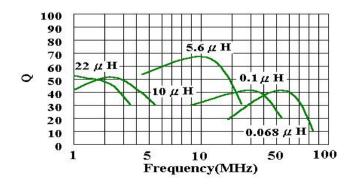
INDUCTANCE VS DC SUPERPOSITION CHARACTERISTICS



IMPEDANCE VS FREQUENCY CHARACTERISTICS



Q VS FREQUENCY CHARACTERISTICS





版次:第3版

N	1	
Α	١	
Ν	1	
Е		

MULTILAYER CHIP INDUCTORS

COMPOSITE SPECIFICATION

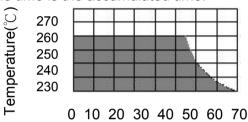
3/8

EBLS-201212

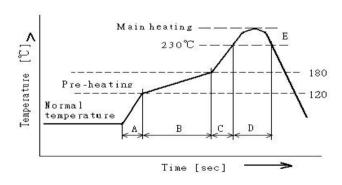
SPEC# EBLS2012-101K

- 6) Reflow soldering conditions
- Pre—heating should be in such a way that the temperature difference between solder and ferrite surface is limited to 150°C max. Also cooling into solvent after soldering should be in such a way that the temperature difference is limited to 100°C max.

 Unenough pre—heating may cause cracks on the ferrite, resulting in the deterioration of product
 - Unenough pre—heating may cause cracks on the ferrite, resulting in the deterioration of product quality.
- Products should be soldered within the following allowable range indicated by the slanted line. The excessive soldering conditions may cause the corrosion of the electrode, When soldering is repeated, allowable time is the accumulated time.



Temperature Profile



Α	Slope of temp. rise	※ 1 to 5	※ ℃/sec
В	Heat time	50 to 150	፠ sec
Ь	Heat temperature	120 to 180	% °C
С	Slope of temp. rise	1 to 5	※ ℃/sec
D	Time over 230°ℂ	90~120	፠ sec
Е	Peak temperature	255~260	% °℃
E	Peak hळdd time	10 max.	፠ sec
	[™] No. of mounting	3	※ times

(Melting area of solder)

6-1 Reworking with soldering iron

Preheating	150℃, Iminute
Tip temperature	280°C max
Soldering time	3seconds max.
Soldering iron output	30w max.
End of soldering iron	§ 3mm max.

Reworking should be limited to only one time.

Note: Do not directly touch the products with the tip of the soldering iron in order to prevent the crack on the ferrite material due to the thermal shock.

6-2 Solder Volume

Solder shall be used not to be exceed the upper limits as shown below.

Upper Limit
Recommendable



Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance.



版次:第3版

N A	MULTILAYER CHIP INDUCTORS	COMPO	4/	
M E	EBLS-201212	SPEC#	EBLS2012-101K	/8

7 EQUIPMENT

7-1 IMPEDANCE

Impedance shall be measured with HP-4286A impedance analyzer or equivalent system

7-2 DC RESISTANCE

DC resistance shall be measured using HP 4338 digital mili—ohm meter with 4 terminal method.

8.MECHANICAL CHARACTERISTICS

ITEM	Specification	TEST CONDITIONS
TERMINAL	Without deformation cases	Solder chip on PCB and applied 10N
STRENGTH	impedance shall be satisfied ±20%	(1.02Kgf) for 10 sec
	DC resistance shall be satisfied.	CHIP BEAD
		Close spore indi
Substrate	Without deformation cases,	After soldering a chip to a test substrate,
bending test	inductance shall be satisfied ± 20%	bend the substrate by 3mm hold for 10s
	DC resistance shall be satisfied.	and then return.
		Soldering shall be done in accordance
		with the recommended PC board pattern
		and reflow soldering.
		unit: mm 45 45 45 100
RESISTANCE	No visible damage	Solder Temp. : 265±3℃
TO SOLDER	Electrical characteristics and mechanic	Immersion time : 6±1 sec
HEAT	characteristics shall be satisfied.	Preheating : 100° C to 150° C, 1 minute.
		Measurement to be made after keeping at room
	Consult standard MIL-STD-202	temp for 24±2 hrs.
	METHOD 210	Solder: Sn-3Ag-0.5Cu
SOLDER-	95% min. coverage of all	Solder temp. : 240±5°C
ABILITY	metabolised area	Immersion time : 3±1 sec
		Solder : Sn-3Ag-0.5Cu
	Consult standard J-STD-002	



版次:第3版

MULTILAYER CHIP INDUCTORS COMPOSITE SPECIFICATION 5 Α M EBLS-201212 SPEC# EBLS2012-101K 9. RELIABILITY AND TEST CONDITIONS 9-1 HIGH TEMPERATURE RESISTANCE

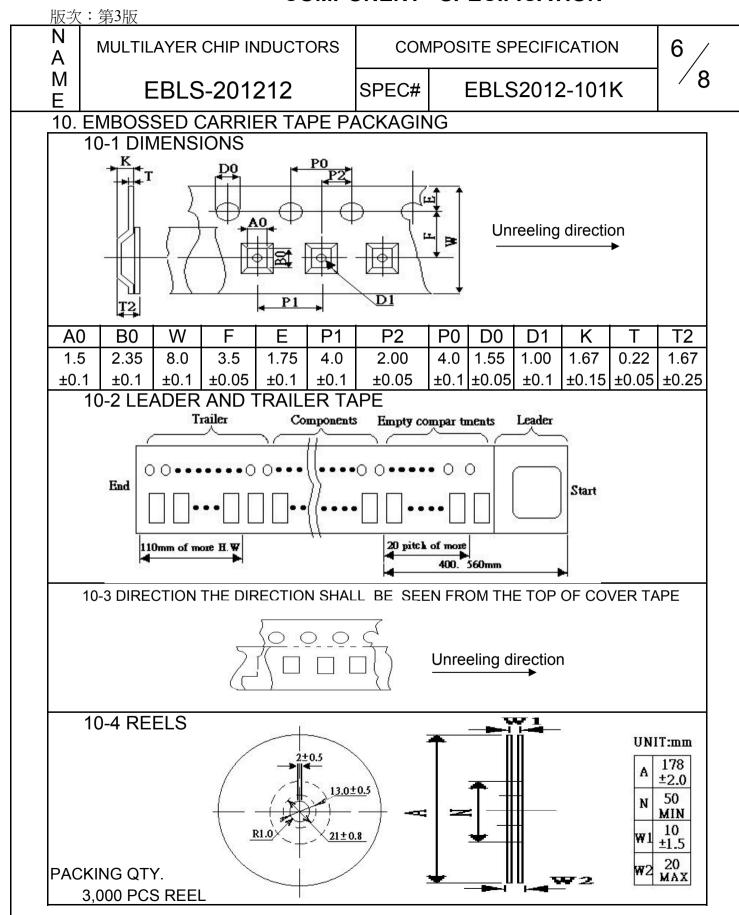
- a. Performance specification
- 1. Appearance: no mechanical damage
- 2.Inductance shall be with ±20% of the initial value
- b.Test condition
- 1.Temperature +85°C±2°C
- 2.Applied current: Rated current 3.Testing time: 1008±12hrs (maximum value)
- 4.Measurement : After placing at room ambient temperature for 24 hours minimum 9-2 HUMIDITY RESISTANCE
- - a.Performance specification
 - 1. Appearance: no mechanical damage
 - 2.Inductance shall be with ±20% of the initial value
 - b.Test condition
 - 1.Humidity: 90 to 95% RH

 - 2.Temperature : 60±2°C 3.Applied current : Rated current (maximum value)
 - 4. Testing tine: 1008±12hours
 - 5.Measurement: After placing at room ambient temperature for 24 hours minimum
- 9-3 TEMPERATURE CYCLE a.Performance specification
 - 1. Appearance: no mechanical damage
 - 2.Inductance shall be with ±20% of the initial value
 - b.Test condition
 - 1.Temperature -40°C,+85°C kept stabilized for 30 minutes each
 - 2.Cycle: 100 cycles
 - 3. Measurement: After placing for 24hours minimum at room ambient temperature
 - 4. step1. -40°C temp±3°C 30±3 minutes step2. Room temperature 2to5 minutes

 - step3. +85°C temp \pm 2°C 30 \pm 3 minutes step4. room temperature 2to5 minutes
- 9-4 LOW TEMPERATURE STORAGE LIFE TEST
 - a.Performance specification
 - 1. Appearance: no mechanical damage
 - 2.Inductance shall be with ±20% of the initial value
 - b.Test condition
 - 1.Temperature -40°C ±2°C
 - 2.Testing time: 1008±12hours
- 3.Measurement: After placing for 24 hours minimum at room ambient temperature 9-5 THERMAL SHOCK
 - a.Performance specification
 - 1. Appearance: no mechanical damage
 - 2.Inductance shall be with ±20% of the initial value
 - b.Test condition
 - 1.Temperature -40°C,+85°C kept stabilized for 30 minutes each
 - 2.Cvcle: 100 cvcles
- 3. Measurement: After placing for 24 hours minimum at room ambient temperature 9-6 VIBRATION TEST
 - a.Performance specification
 - 1. Appearance: no mechanical damage
 - 2.Inductance shall be with ±20% of the initial value
 - b.Test condition
 - 1.Waveform:Sine wave
 - 2.Frequency:10~55~10 Hz
 - 3. Sweep time: 1 min
 - 4.Amplitude:1.5mm(peak-peak) 5.Direction:X,Y,Z(3 axes)

 - 6. Duration: 2 hrs./axis, total 6 hrs.



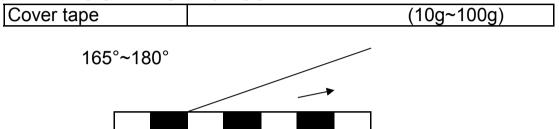




版次:第3版

N MULTILAYER CHIP INDUCTORS COMPOSITE SPECIFICATION 7/
BE EBLS-201212 SPEC# EBLS2012-101K

10-5 PEELING STRENGTH OF COVER TAPE



Test condition

1) peel angle: 165°~180° vs carrier tape

2) peel speed: 300mm/min

11.PACKAGING

1) Tape & Reel packaging in composite specification 6/8

- 2) Reel and a bag of desiccant shall be packed in Nylon or plastic bag
- 3) Maximum of 5 bags shall be packaged in a inner box
- 4) Maximum of 6 inner box shall be packaged in a outer box

12.Reel Label

Producing the goods label needs to indicate (1) Pb Free (2) RoHS Compliant



版次:第3版

1	δZ	MULTILAYER CHIP INDUCTORS	COMPO	SITE SPECIFICATION	8/
	M E	EBLS-201212	SPEC#	EBLS2012-101K	8

13. STORAGE

- 13-1The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Packages must be stored at 40°C or less and 70% RH or less.
- 13-2 The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust or harmful gas (hydrogen chloride, sulfurous acid gas or hydrogen sulfide).
- 13-3 Packaging material may be deformed if packages are stored where they are exposed to heat or direct sun—light.
- 13-4 Minimum packages, such as polyvinyl heat—seal packages shall not be opened until just before they are used. If opened, use the reels as soon as possible.
- 13-5 Solderability specified in composite specification 4/8 shall be for 6 months from the date of delivery on condition that they are stored at the environment specified clause 13-1 & 13-2.

For those parts which passed more than 6 months shall be checked solderability before it is used.

- 14. Quality System
- ISO/TS16949
- IECQ QC 080000

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Fixed Inductors category:

Click to view products by HYHONGYEX manufacturer:

Other Similar products are found below:

CR32NP-151KC CR32NP-180KC CR32NP-181KC CR32NP-1R5MC CR32NP-390KC CR32NP-3R9MC CR32NP-680KC CR32NP820KC CR32NP-8R2MC CR43NP-390KC CR43NP-560KC CR43NP-680KC CR54NP-181KC CR54NP-470LC CR54NP-820KC
CR54NP-8R5MC 70F224AI MGDQ4-00004-P MHL1ECTTP18NJ MHQ1005P10NJ MHQ1005P1N0S MHQ1005P2N4S MHQ1005P3N6S
MHQ1005P5N1S MHQ1005P8N2J PE-51506NL PE-53601NL PE-53602NL PE-53630NL PE-53824SNLT PE-92100NL PG0434.801NLT
PG0936.113NLT 9220-20 9310-16 PM06-2N7 PM06-39NJ A01TK 1206CS-471XJ HC2LP-R47-R HC2-R47-R HC3-2R2-R HCF13053R3-R 1206CS-151XG RCH664NP-140L RCH664NP-4R7M RCH8011NP-221L RCP1317NP-332L RCP1317NP-391L RCR1010NP-470M