## PLC High Density Analog I/O Module

Features -8-pt ( $0-10 \mathrm{~V}$ DC / 4-20mA) input module
-2-pt (-10 to +10VDC / 4-20mA) output module

- 16-bit Resolution
- Fast Conversion Times
- Configure up to 56 analog I/Os



## Specifications

| General Specifications |  |  |
| :--- | :--- | :--- |
| Part Number | FC4A-J8C1 | FC4A-K2C1 |
| Rated Power Voltage | 24 V DC |  |


| Analog Input Specifications |  |  |  |
| :---: | :---: | :---: | :---: |
| Part Number |  | FC4A-J8C1 |  |
| Analog Input Signal Type |  | Voltage Input | Current Input |
| Input Range |  | 0 to 10V DC | 4 to 20 mA |
| Input Impedance |  | $1 \mathrm{M} \Omega$ | $100 \Omega$ |
|  | Sample Duration Time | 2 ms maximum |  |
|  | Sample Repetition Time | 2 ms maximum |  |
|  | Total Input System Transfer Time ${ }^{\text {Note } 1}$ | 8 ms x channels +1 scan time |  |
|  | Type of Input | Single-ended input |  |
|  | Operating Mode | Self-scan |  |
|  | Conversion Method | Successive approximation register method |  |
| $\begin{aligned} & \text { 흘 } \\ & \text { w } \\ & \text { I } \\ & \text { ㅡㅡㄹ } \end{aligned}$ | Maximum Error at $25^{\circ} \mathrm{C}$ | $\pm 0.2 \%$ of full scale |  |
|  | Temperature Coefficient | $\pm 0.005 \%$ of full scale $/{ }^{\circ} \mathrm{C}$ |  |
|  | Repeatability after Stabilization Time | $\pm 0.5 \%$ of full scale |  |
|  | Non-lineality | $\pm 0.04 \%$ of full scale |  |
|  | Maximum Error | $\pm 1 \%$ of full scale |  |


| Analog Input Specifications |  |  |  |
| :---: | :---: | :---: | :---: |
| $\stackrel{\text { Iた }}{0}$ | Digital Resolution | 50000 increments (16 bits) |  |
|  | Input Value of LSB | 0.2 mV | $0.32 \mu \mathrm{~A}$ |
|  | Data Type in Application Program | Default: 0 to 50000 |  |
|  | Monotonicity | Optional: -32768 to 32767 (selectable for each channel) ${ }^{\text {Note } 2}$ |  |
|  | Input Data Out of Range | Detectable ${ }^{\text {Note } 3}$ |  |
|  | Maximum Temporary Deviation during Electrical Noise Tests ${ }^{\text {Note } 4}$ | $\pm 3 \%$ maximum |  |
|  | Input Filter | Software |  |
|  | Recommended Cable for Noise Immunity | Twisted pair cable |  |
|  | Crosstalk | 2 LSB maximum |  |
| Isolation |  | Isolated between input and power circuit |  |
|  |  | Photocoupler-isolated between input and internal circuit |  |
| Effect of Improper Input Connection |  | No damage |  |
| Maximum Permanent Allowed Overload (No Damage) |  | 11 V DC | 22 mA DC |
| Sele | tion of Analog Input Signal Type | Using software programming |  |

Notes:

1. Total input system transfer time $=$ Sample repetition time + Internal processing time. The total input system transfer time increases in proportion to the number of of channels used.
2. The data processed in the analog $\mathrm{I} / 0$ module can be linear-converted to a a value between -32768 and 32767 . The the optional range designation, and analog $\mathrm{I} / 0$ data minimum and maximum values can be selected using data registers allocated to analog I/O modules.
3. When an error is detected, a corresponding error code is stored to a data register allocated to analog I/O operating status.
4. The value is measured when a 500 V clamp voltage is applied to the power supply and I/O lines.

## Dimensions (mm)

## FC4A-K2C1



Analog Output Specifications

| Current Loop Open |  | Not detectable |
| :---: | :---: | :---: |
| Noise Resistance | Maximum Temporary <br> Deviation during <br> Electrical Noise Tests ${ }^{\text {Note } 4}$ | $\pm 3 \%$ maximum |
|  | Recommended Cable for Noise Immunity | Twisted pair cable |
|  | Crosstalk | 2 LSB maximum |
| Isolation |  | Isolated between output and power circuit |
|  |  | Photocoupler-isolated between output and internal circuit |
| Effect of Improper Output Connection |  | No damage |
| Selection of Analog Output Signal Type |  | Using software programming |

Mounting Hole Layout (mm)


FC4A-J8C1

$* 8.5 \mathrm{~mm}$ when the clamp is pulled out.

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