International
IOR Rectifier

AUTOMOTIVE GRADE

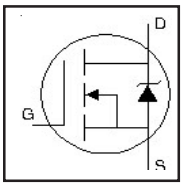
PD - 97778

AUIRFIZ34N

HEXFET® Power MOSFET

Features

- Advanced Planar Technology
- Low On-Resistance
- Isolated Package
- High Voltage Isolation = 2.5KVRMS ⑤
- Sink to Lead Creepage Distantce = 4.8mm
- 175°C Operating Temperature
- Fully Avalanche Rated
- Lead-Free, RoHS Compliant
- Automotive Qualified*



$V_{(BR)DSS}$	55V
$R_{DS(on)}$ max.	40mΩ
I_D	21A

TO-220AB Full-Pak
AUIRFIZ34N

G	D	S
Gate	Drain	Source

Description

Specifically designed for Automotive applications, this cellular design of HEXFET® Power MOSFETs utilizes the latest processing techniques to achieve low on-resistance per silicon area. This benefit combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in Automotive and a wide variety of other applications.

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

	Parameter	Max.	Units
I_D @ T_C = 25°C	Continuous Drain Current, V_{GS} @ 10V	21	A
I_D @ T_C = 100°C	Continuous Drain Current, V_{GS} @ 10V	15	
I_{DM}	Pulsed Drain Current ①⑥	100	
P_D @T_C = 25°C	Power Dissipation	37	W
	Linear Derating Factor	0.24	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy (Thermally Limited)②⑥	110	mJ
I_{AR}	Avalanche Current ①⑥	16	A
E_{AR}	Repetitive Avalanche Energy ①	3.7	mJ
dv/dt	Peak Diode Recovery dv/dt ③⑥	5.0	V/ns
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 175	°C
	Soldering Temperature, for 10 seconds (1.6mm from case)	300	
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case ⑦	——	4.1	°C/W
$R_{\theta JA}$	Junction-to-Ambient	——	65	

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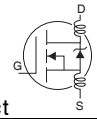
*Qualification standards can be found at http://www.irf.com/

Static Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	55	—	—	V	V_{GS} = 0V, I_D = 250µA
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.052	—	V/°C	Reference to 25°C, I_D = 1mA ⑥
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	—	40	mΩ	V_{GS} = 10V, I_D = 11A ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	V_{DS} = V_{GS}, I_D = 250µA
gfs	Forward Transconductance	6.5	—	—	S	V_{DS} = 25V, I_D = 16A ⑥
I_{DSS}	Drain-to-Source Leakage Current	—	—	25	µA	V_{DS} = 55V, V_{GS} = 0V
		—	—	250		V_{DS} = 44V, V_{GS} = 0V, T_J = 150°C
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V_{GS} = 20V
	Gate-to-Source Reverse Leakage	—	—	-100		V_{GS} = -20V

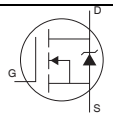
Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge	—	—	34		I_D = 16A
Q_{gs}	Gate-to-Source Charge	—	—	68	nC	V_{DS} = 44V
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	—	14		V_{GS} = 10V, See Fig. 6&13 ④⑥
$t_{d(on)}$	Turn-On Delay Time	—	7.0	—		V_{DD} = 28V
t_r	Rise Time	—	49	—		I_D = 16A
$t_{d(off)}$	Turn-Off Delay Time	—	31	—	ns	R_G = 18Ω
t_f	Fall Time	—	40	—		R_D = 1.8Ω, See Fig. 10 ④⑥
L_D	Internal Drain Inductance	—	4.5	—		Between lead,
					nH	6mm (0.25in.)
L_S	Internal Source Inductance	—	7.5	—		from package
						and center of die contact
C_{iss}	Input Capacitance	—	700	—		V_{GS} = 0V
C_{oss}	Output Capacitance	—	240	—	pF	V_{DS} = 25V
C_{rss}	Reverse Transfer Capacitance	—	100	—		f = 1.0MHz, See Fig. 5 ⑥
C	Drain to Sink Capacitance	—	12	—		f = 1.0MHz



Diode Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current	—	—	21		MOSFET symbol
	(Body Diode)				A	showing the
I_{SM}	Pulsed Source Current	—	—	100		integral reverse
	(Body Diode) ①					p-n junction diode.
V_{SD}	Diode Forward Voltage	—	—	1.6	V	T_J = 25°C, I_S = 11A, V_{GS} = 0V ④
t_{rr}	Reverse Recovery Time	—	57	86	ns	T_J = 25°C, I_F = 16A
Q_{rr}	Reverse Recovery Charge	—	130	200	nC	di/dt = 100A/µs ④⑥
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				



Notes:

① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)

② V_{DD} = 25V, starting T_J = 25°C, L = 610µH R_G = 25Ω, I_{AS} = 16A. (See Figure 12)

③ I_{SD} ≤ 16A, di/dt ≤ 420A/µs, V_{DD} ≤ $V_{(BR)DSS}$, T_J ≤ 175°C.

④ Pulse width ≤ 300µs; duty cycle ≤ 2%.

⑤ t=60s, f=60Hz

⑥ Uses IRFZ34N data and test conditions.

⑦ R_θ is measured at Tj at approximately 90°C.

Qualification Information†

Qualification Level		Automotive (per AEC-Q101)	
		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		TO-220 Fullpak	N/A
ESD	Machine Model	Class M2 (+/- 200V)†† AEC-Q101-002	
	Human Body Model	Class H1A (+/- 500V)†† AEC-Q101-001	
	Charged Device Model	Class C5 (+/- 2000V)†† AEC-Q101-005	
RoHS Compliant		Yes	

† Qualification standards can be found at International Rectifier's web site: http//www.irf.com/

†† Exceptions to AEC-Q101 requirements are noted in the qualification report.

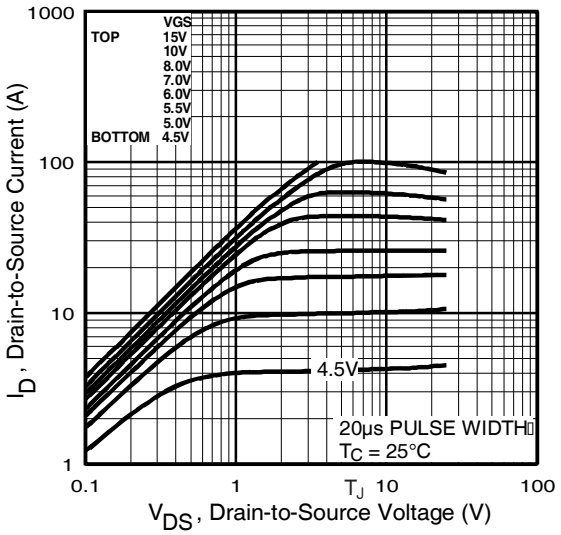


Fig 1. Typical Output Characteristics

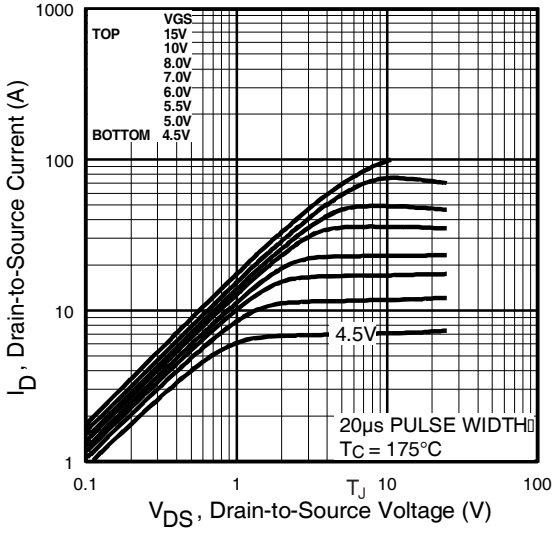


Fig 2. Typical Output Characteristics

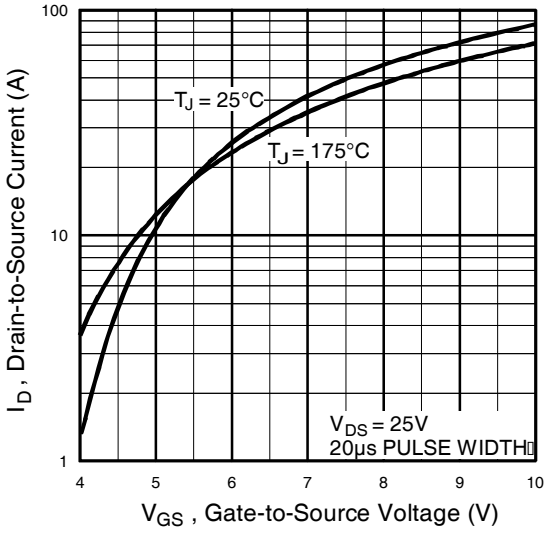


Fig 3. Typical Transfer Characteristics

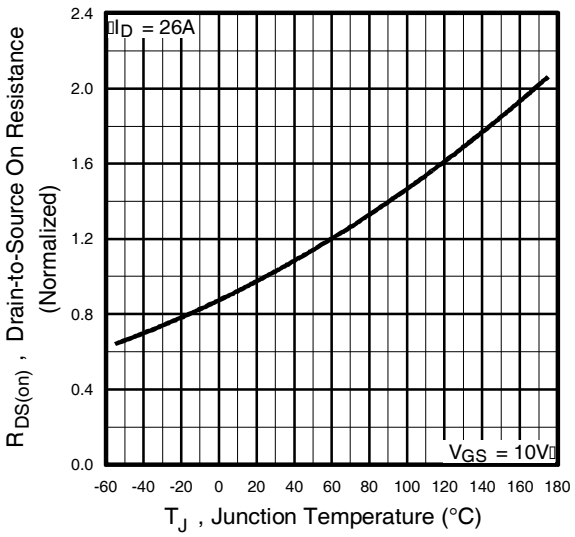


Fig 4. Normalized On-Resistance Vs. Temperature

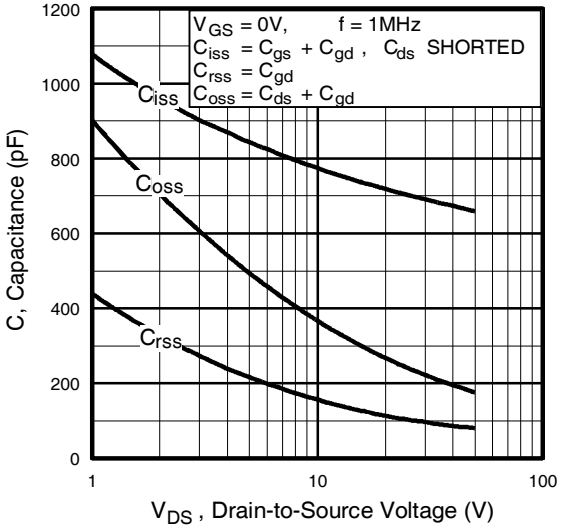


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

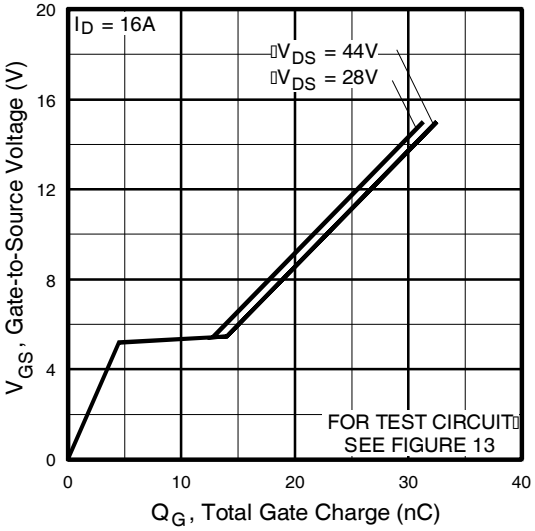


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

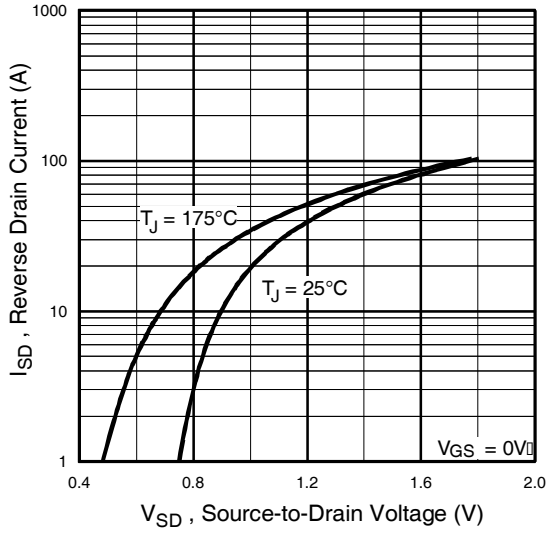


Fig 7. Typical Source-Drain Diode Forward Voltage

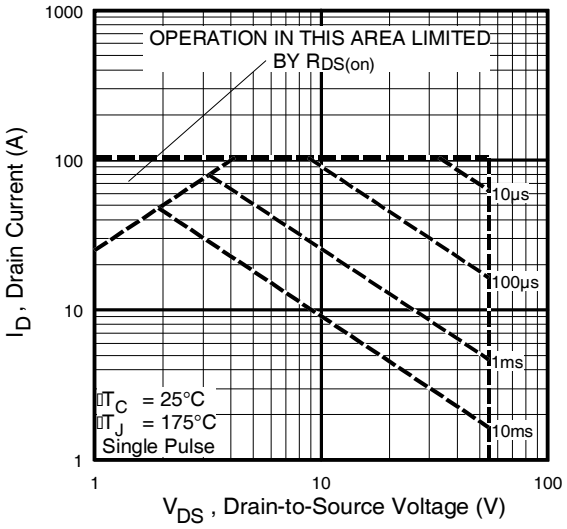


Fig 8. Maximum Safe Operating Area

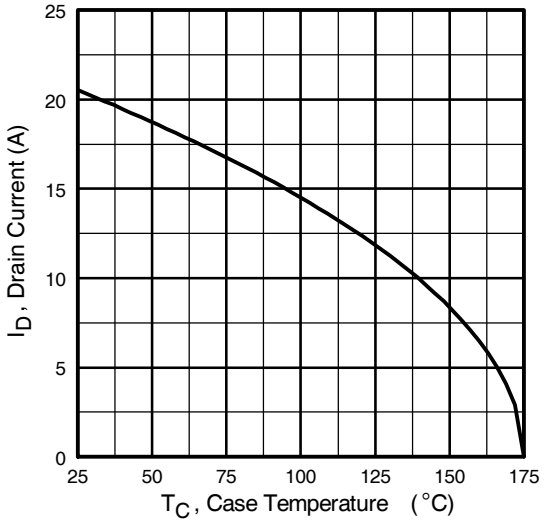


Fig 9. Maximum Drain Current Vs. Case Temperature

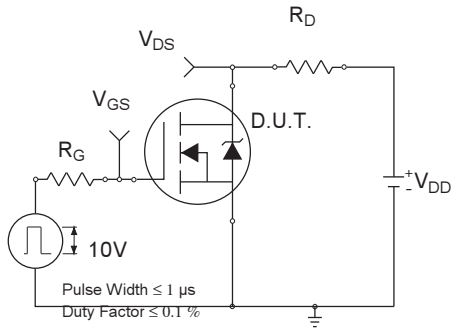


Fig 10a. Switching Time Test Circuit

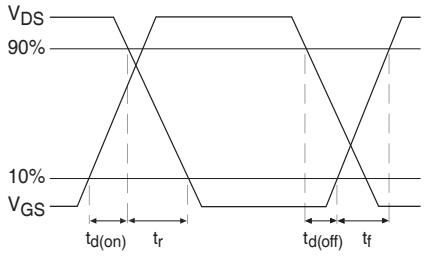


Fig 10b. Switching Time Waveforms

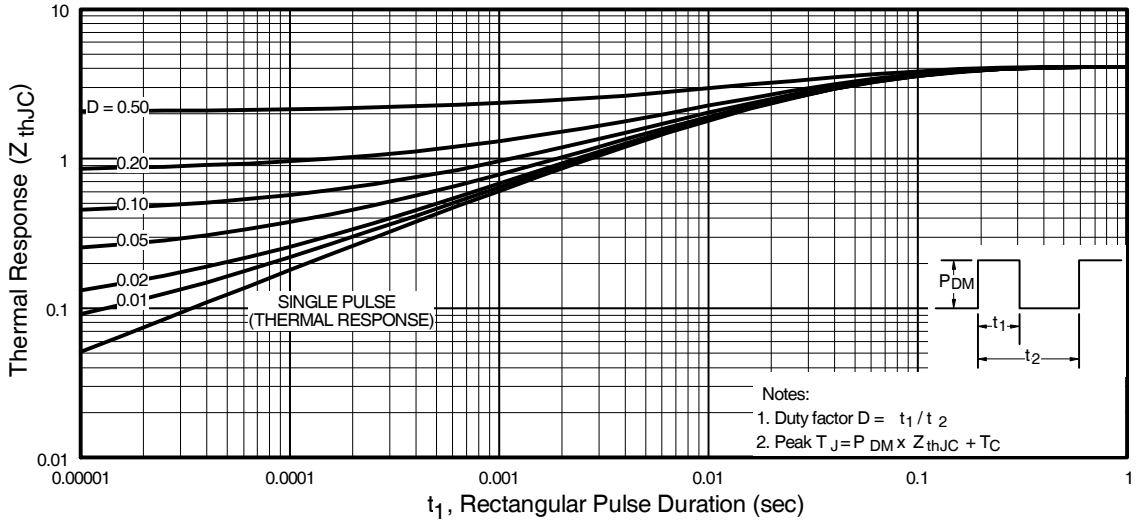


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

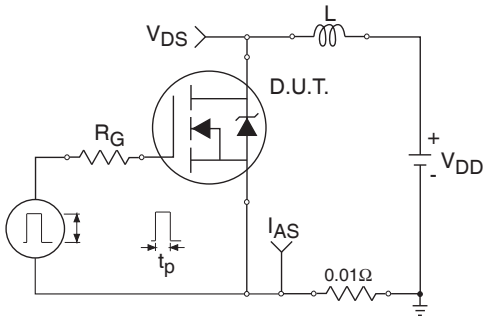


Fig 12a. Unclamped Inductive Test Circuit

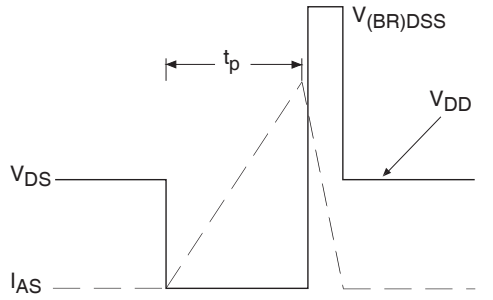


Fig 12b. Unclamped Inductive Waveforms

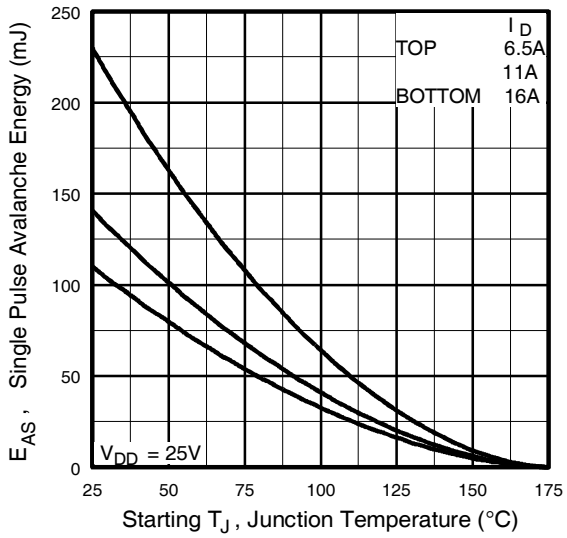


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

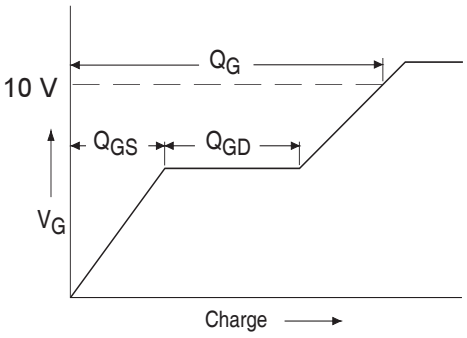


Fig 13a. Basic Gate Charge Waveform

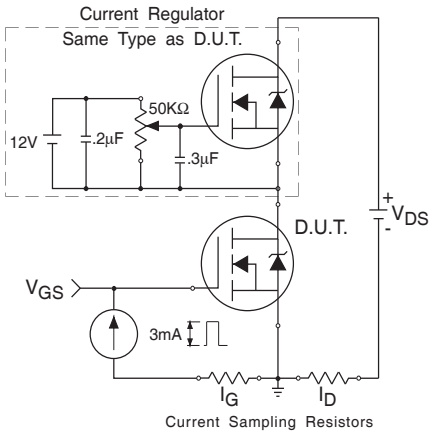
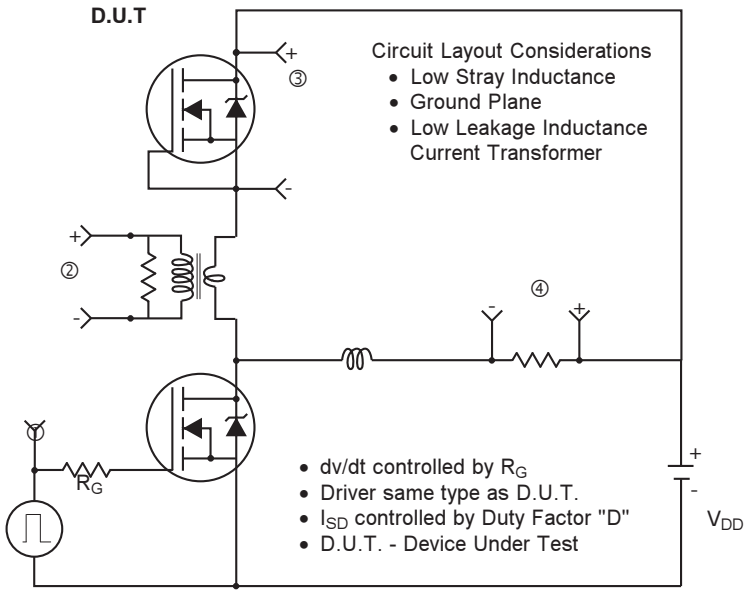
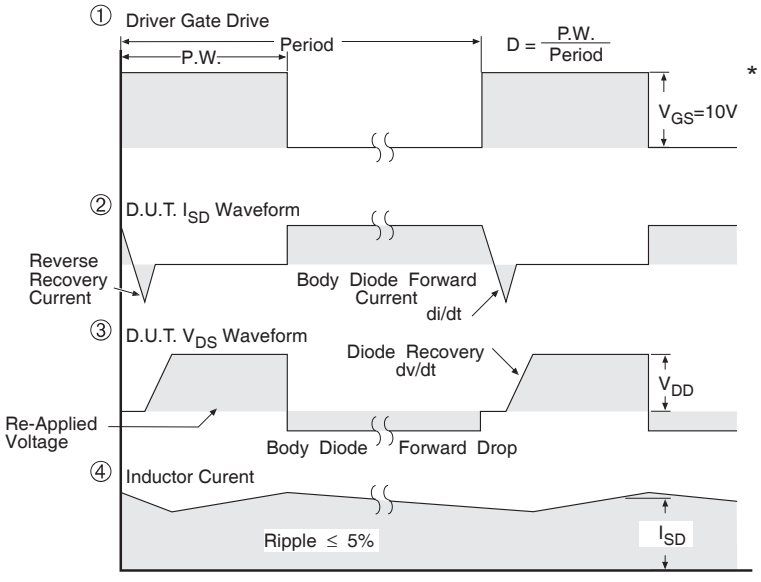


Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit



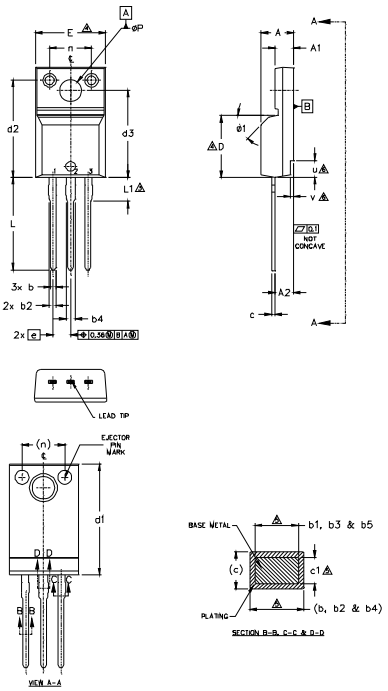


* V_{GS} = 5V for Logic Level Devices

Fig 14. For N-Channel HEXFETS

TO-220AB Full-Pak Package Outline

Dimensions are shown in millimeters (inches)



SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.57	4.83	.180	.190	
A1	2.57	2.83	.101	.111	
A2	2.51	2.93	.099	.115	
b	0.61	0.94	.024	.037	
b1	0.61	0.89	.024	.035	5
b2	0.76	1.27	.030	.050	
b3	0.76	1.22	.030	.048	5
b4	1.02	1.52	.040	.060	
b5	1.02	1.47	.040	.058	5
c	0.33	0.63	.013	.025	
c1	0.33	0.58	.013	.023	5
D	8.66	9.80	.341	.386	4
d1	15.80	16.13	.622	.635	
d2	13.97	14.22	.550	.560	
d3	12.30	12.93	.484	.509	
E	9.63	10.75	.379	.423	4
e	2.54 BSC		.100 BSC		
L	13.20	13.72	.520	.540	
L1	3.37	3.67	.122	.145	3
n	6.05	6.60	.238	.260	
øP	3.05	3.45	.120	.136	
u	2.40	2.50	.094	.098	6
v	0.40	0.50	.016	.020	6
ø1	–	45°	–	45°	

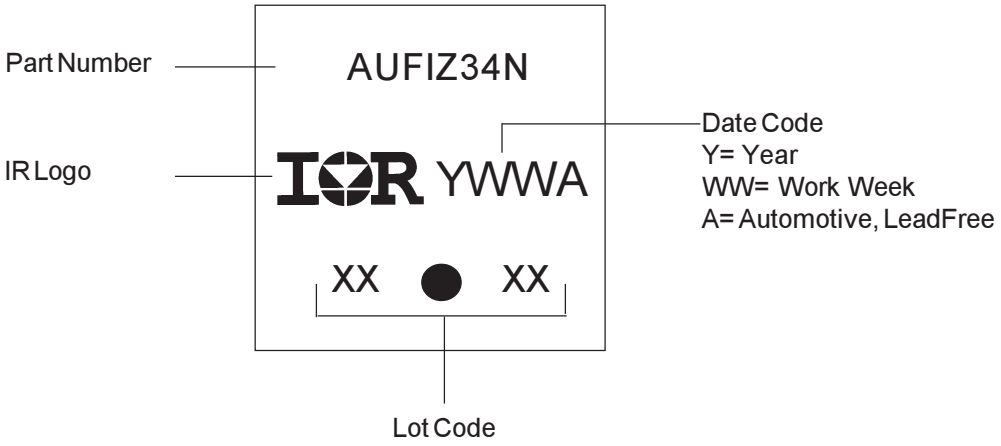
NOTES:
1.0 DIMENSIONING AND TOLERANCING AS PER ASME Y14.5 M- 1994.
2.0 DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3.0 LEAD DIMENSION AND FINISH UNCONTROLLED IN L1.
4.0 DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTER MOST EXTREMES OF THE PLASTIC BODY.
5.0 DIMENSION b1, b3, b5 & c1 APPLY TO BASE METAL ONLY.
6.0 STEP OPTIONAL ON PLASTIC BODY DEFINED BY DIMENSIONS u & v.
7.0 CONTROLLING DIMENSION : INCHES.

LEAD ASSIGNMENTS

HEXFET
1.- GATE
2.- DRAIN
3.- SOURCE

IGBTs, CoPACK
1.- GATE
2.- COLLECTOR
3.- EMITTER

TO-220AB Full-Pak Part Marking Information



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

Ordering Information

Base part number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRFIZ34N	TO-220 Fullpak	Tube	50	AUIRFIZ34N

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