AUTOMOTIVE GRADE

PD - 96340

AUIRG4BC30S-S

AUIRG4BC30S-SL Standard Speed IGBT



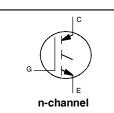
INSULATED GATE BIPOLAR TRANSISTOR

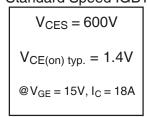
Features

- Standard: optimized for minimum saturation voltage and low operating frequencies (< 1kHz)
- · Lead-Free, RoHS Compliant
- Automotive Qualified *

Benefits

· Typical Applications: PTC Heater, Discharge Switch & Relay Replacements







G	С	E
Gate	Collector	Emitter

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified

	Parameter	Max.	Units
V _{CES}	Collector-to-Emitter Breakdown Voltage	600	V
I _C @ T _C = 25°C	Continuous Collector Current	34	
I _C @ T _C = 100°C	Continuous Collector Current	18	Α
I _{CM}	Pulsed Collector Current ①	68	
I _{LM}	Clamped Inductive Load Current ②	68	
V_{GE}	Gate-to-Emitter Voltage	±20	V
E _{ARV}	Reverse Voltage Avalanche Energy 3	10	mJ
$P_D @ T_C = 25^{\circ}C$	Maximum Power Dissipation	100	w
P _D @ T _C = 100°C	Maximum Power Dissipation	42	
T _J	Operating Junction and	-55 to +150	
T _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm) from case)	

Thermal Resistance

	Parameter	Тур.	Max.	Units
R _{eJC}	Junction-to-Case		1.2	
R _{θCS}	Case-to-Sink, Flat, Greased Surface	0.50		°C/W
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount		40	
Wt	Weight	1.44		g (oz)

^{*} When mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994.

International

TOR Rectifier

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter		Тур.	Max.	Units	Conditions	
V _{(BR)CES}	Collector-to-Emitter Breakdown Voltage		_		V	$V_{GE} = 0V$, $I_C = 250\mu A$	
V _{(BR)ECS}	Emitter-to-Collector Breakdown Voltage 4	18	_	_	V	$V_{GE} = 0V, I_{C} = 1.0A$	
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	_	0.75	_	V/°C	$V_{GE} = 0V$, $I_C = 1.0mA$	
		_	1.40	1.6		$I_C = 18A$ V_G	_E = 15V
V _{CE(ON)}	Collector-to-Emitter Saturation Voltage	_	1.84	_	V	I _C = 34A See	Fig. 2, 5
, ,			1.45	_]	I _C = 18A , T _J = 150°C	
V _{GE(th)}	Gate Threshold Voltage	3.0	_	6.0		$V_{CE} = V_{GE}$, $I_C = 250\mu A$	
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	_	-11	_	mV/°C	$V_{CE} = V_{GE}$, $I_C = 250\mu A$	
g _{fe}	Forward Transconductance ®	6.0	11	_	S	$V_{CE} = 100V, I_{C} = 18A$	
I _{CES}	Zero Gate Voltage Collector Current		_	250	μA	$V_{GE} = 0V, V_{CE} = 600V$	
ICES			_	2.0	μ,	$V_{GE} = 0V, V_{CE} = 10V, T_{J} = 25$	°C
			_	1000		$V_{GE} = 0V, V_{CE} = 600V, T_{J} = 100$	50°C
I _{GES}	Gate-to-Emitter Leakage Current	_	_	±100	nA	$V_{GE} = \pm 20V$	

Switching Characteristics @ $T_J = 25$ °C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
Qg	Total Gate Charge (turn-on)	_	50	75		I _C = 18A
Q _{ge}	Gate - Emitter Charge (turn-on)	_	7.3	11	nC	V _{CC} = 400V See Fig. 8
Q _{gc}	Gate - Collector Charge (turn-on)	_	17	26		$V_{GE} = 15V$
t _{d(on)}	Turn-On Delay Time	_	22	_		
t _r	Rise Time	_	18	_	ns	$T_J = 25^{\circ}C$
t _{d(off)}	Turn-Off Delay Time	_	540	810	113	$I_C = 18A, V_{CC} = 480V$
t _f	Fall Time	_	390	590		$V_{GE} = 15V$, $R_G = 23\Omega$
Eon	Turn-On Switching Loss	_	0.26			Energy losses include "tail"
E _{off}	Turn-Off Switching Loss	_	3.45	_	mJ	See Fig. 9, 10, 14
E _{ts}	Total Switching Loss	_	3.71	5.6		
t _{d(on)}	Turn-On Delay Time	_	21			$T_{J} = 150^{\circ}C,$
t _r	Rise Time	_	19	_	ns	$I_C = 18A$, $V_{CC} = 480V$
t _{d(off)}	Turn-Off Delay Time	_	790	_	115	$V_{GE} = 15V$, $R_G = 23\Omega$
t _f	Fall Time	_	760	_		Energy losses include "tail"
E _{ts}	Total Switching Loss	_	6.55	_	mJ	See Fig. 11, 14
LE	Internal Emitter Inductance	_	7.5		nΗ	Measured 5mm from package
C _{ies}	Input Capacitance	_	1100	_		$V_{GE} = 0V$
C _{oes}	Output Capacitance	_	72	_	рF	V _{CC} = 30V See Fig. 7
C _{res}	Reverse Transfer Capacitance	_	13	_		f = 1.0MHz

Notes:

- \odot Repetitive rating; $V_{GE} = 20V$, pulse width limited by max. junction temperature (See fig. 13b).
- $@~V_{CC}$ = 80%(V_{CES}), V_{GE} = 20V, L = 10µH, R_G = 23 Ω , (See fig. 13a).
- 3 Repetitive rating; pulse width limited by maximum junction temperature.
- 4 Pulse width $\leq 80\mu s$; duty factor $\leq 0.1\%$.
- ⑤ Pulse width 5.0µs, single shot.

International TOR Rectifier

AUIRG4BC30S-S/SL

Qualification Information[†]

		Automotive (per AEC-Q101) ††			
Qualification	Level	Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.			
			MSL1 †††		
Moisture Sens	sitivity Level		(per IPC/JEDEC J-STD-020)		
		TO-262	N/A		
	Machine Model	Class M4 (400V)			
		AEC-Q101-002			
505	Human Body Model	Class H1C (2000V)			
ESD	ESD		AEC-Q101-001		
	Charged Device Model	Class C5 (1000V)			
		AEC-Q101-005			
RoHS Compli	ant	Yes			

[†] Qualification standards can be found at International Rectifier's web site: http://www.irf.com

^{††} Exceptions to AEC-Q101 requirements are noted in the qualification report.

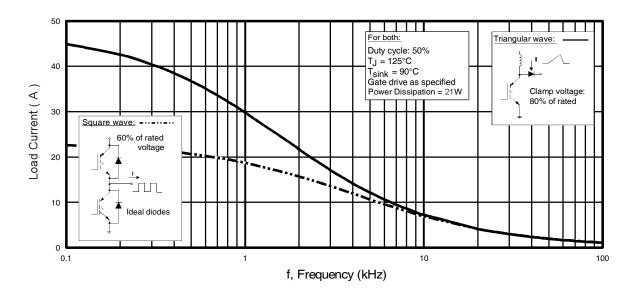


Fig. 1 - Typical Load Current vs. Frequency (Load Current = I_{RMS} of fundamental)

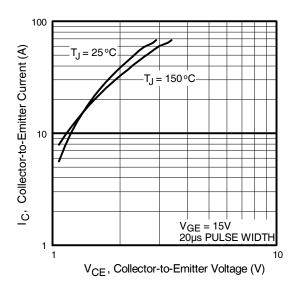


Fig. 2 - Typical Output Characteristics

4

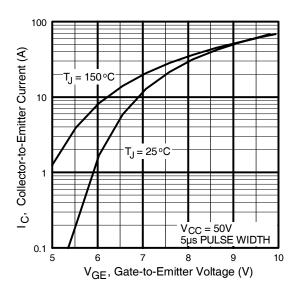
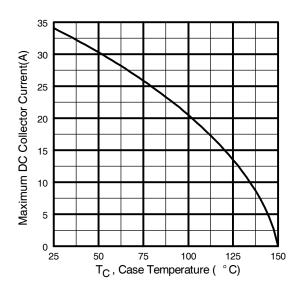


Fig. 3 - Typical Transfer Characteristics www.irf.com

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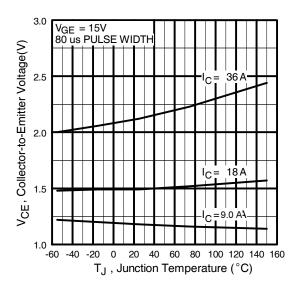


Fig. 4 - Maximum Collector Current vs. Case Temperature

Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

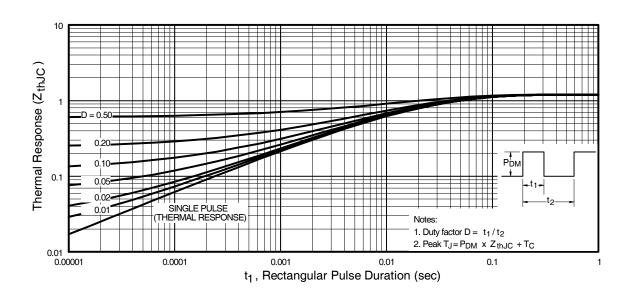
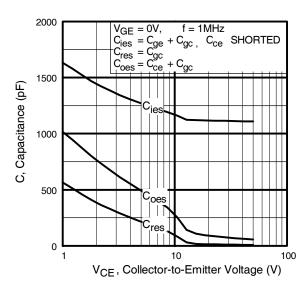


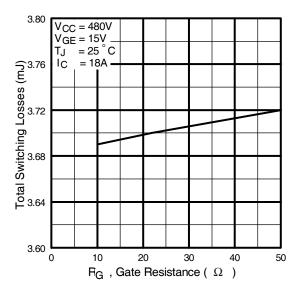
Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



20 V_{CC} = 400V | C = 18A | V_{CC} = 18A | V

Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage



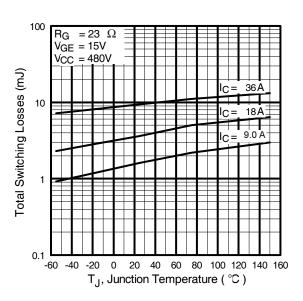


Fig. 9 - Typical Switching Losses vs. Gate Resistance

Fig. 10 - Typical Switching Losses vs. Junction Temperature

International IOR Rectifier

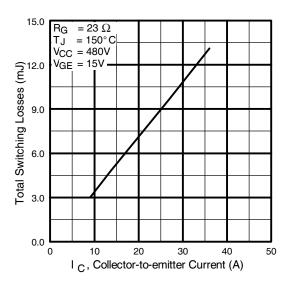


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

AUIRG4BC30S-S/SL

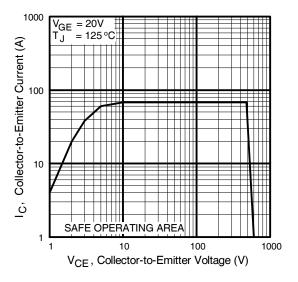
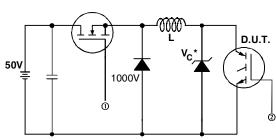


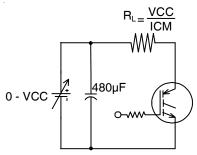
Fig. 12 - Turn-Off SOA

International IOR Rectifier



* Driver same type as D.U.T.; Vc = 80% of Vce(max)
 * Note: Due to the 50V power supply, pulse width and inductor will increase to obtain rated ld.

Fig. 13a - Clamped Inductive Load Test Circuit



Pulsed Collector Current Test Circuit

Fig. 13b - Pulsed Collector Current Test Circuit

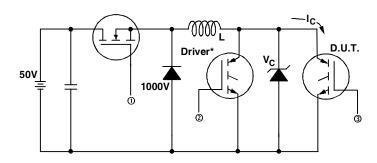


Fig. 14a - Switching Loss Test Circuit

* Driver same type as D.U.T., VC = 480V

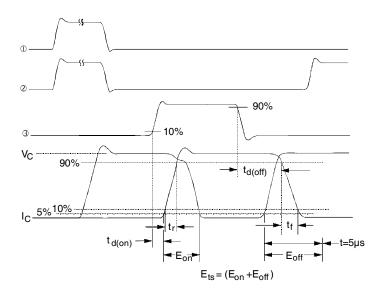


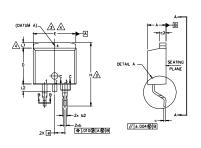
Fig. 14b - Switching Loss Waveforms

International IOR Rectifier

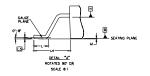
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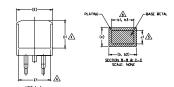
D²Pak (TO-263AB) Package Outline

Dimensions are shown in millimeters (inches)









- 1. DIMENSIONING AND TOLERANCING PER ASME Y14,5M-1994
- 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- 3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
- 4. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.
- 5. DIMENSION 61 AND c1 APPLY TO BASE METAL ONLY.
- 6. DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
- 7, CONTROLLING DIMENSION; INCH.
- 8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-263AB

S Y M B O L	DIMENSIONS					
B	MILLIM	ETERS	INC	HES	NOT ES	
Ĺ	MIN.	MAX.	MIN.	MAX.	Š	
Α	4.06	4.83	.160	.190		
Α1	0.00	0.254	.000	.010		
b	0.51	0.99	.020	.039		
ь1	0.51	0.89	.020	.035	5	
b2	1,14	1.78	.045	.070		
b3	1,14	1,73	.045	.068	5	
С	0.38	0.74	.015	.029		
с1	0,38	0.58	.015	.023	5	
c2	1,14	1.65	.045	,065		
D	8.38	9.65	.330	.380	3	
D1	6.86	-	.270		4	
Ε	9.65	10,67	.380	.420	3,4	
E1	6.22	-	.245		4	
е	2.54	BSC	.100	BSC		
Н	14,61	15,88	.575	.625		
L	1.78	2.79	.070	.110		
L1	-	1.65	-	.066	4	
L2	1,27	1.78	-	.070		
L3	0.25	BSC	.010	BSC		
L4	4,78	5.28	.188	.208		

LEAD ASSIGNMENTS

HEXFET

1.- GATE 2. 4.- DRAIN 3.- SOURCE

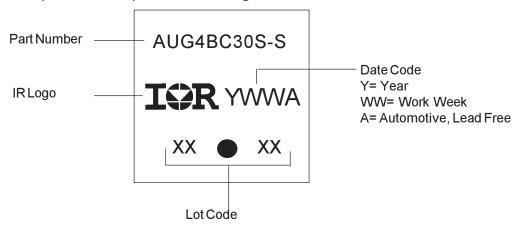
IGBTs. CoPACK

1.- GATE 2, 4.- COLLECTOR 3.- EMITTER

DIODES

- * PART DEPENDENT.

D²Pak (TO-263AB) Part Marking Information



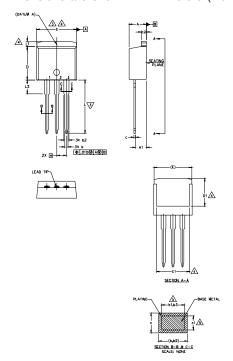
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International

TOR Rectifier

TO-262 Package Outline

Dimensions are shown in millimeters (inches)

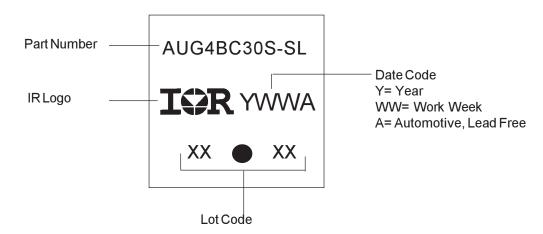


NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
- 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- (3) DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
- A THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.
- 5 DIMENSION 61 AND 61 APPLY TO BASE METAL ONLY.
 - 6. CONTROLLING DIMENSION; INCH.
 - 7.— OUTLINE CONFORM TO JEDEC TO-262 EXCEPT A1(max.), b(min.) AND D1(min.) WHERE DIMENSIONS DERIVED THE ACTUAL PACKAGE OUTLINE.

		310143	DIMEN	S Y M	
N O T E S	HES	INC	ETERS	MILLIM	B
AX. S	MAX.	MIN.	MAX.	MIN.	L
90	.190	.160	4.83	4,06	Α
19	.119	.080	3.02	2.03	A1
39	.039	.020	0.99	0.51	ь
35 5 LEAD ASSIGNMENTS	,035	.020	0,89	0.51	ь1
70	.070	.045	1.78	1,14	ь2
	.068	.045	1.73	1,14	b3
29 HEXFET	.029	.015	0.74	0.38	c
23 5 1 GATE	.023	.015	0.58	0.38	c1
	.065	.045	1.65	1,14	c2
80 3 3 SOURCE 4 DRAIN	,380	.330	9,65	8,38	D
- 4 DRAIN	-	.270	-	6,86	D1
20 3,4	.420	.380	10.67	9.65	Ε
4 IGBTs, CoPACK		.245	-	6,22	E1
4 2475	BSC	.100	BSC	2.54	e
55 1 GATE 2 COLLECTOR	.555	.530	14.10	13.46	L
65 4 3 EMITTER	.065	-	1,65	-	L1
4. COLLECTOR	.146	.140	3.71	3.56	L2

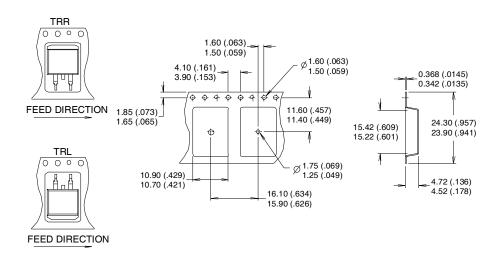
TO-262 Part Marking Information

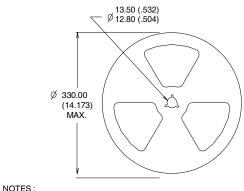


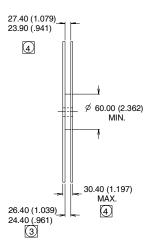
Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

D²Pak Tape & Reel Information

Dimensions are shown in millimeters (inches)







COMFORMS TO EIA-418.

CONTROLLING DIMENSION: MILLIMETER.

DIMENSION MEASURED @ HUB.

INCLUDES FLANGE DISTORTION @ OUTER EDGE.

International

TOR Rectifier

Ordering Information

Base part number	Package	Standard Pack		Complete Part Number				
		Form	Quantity					
AUIRG4BC30S-SL	TO-262	Tube	50	AUIRG4BC30S-SL				
AUIRG4BC30S-S	D2Pak	Tube	50	AUIRG4BC30S-S				
		Tape and Reel Left	800	AUIRG4BC30SSTRL				
		Tape and Reel Right	800	AUIRG4BC30SSTRR				

International TOR Rectifier

AUIRG4BC30S-S/SL

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 IGW40N120H3FKSA1
 VS-CPV364M4KPBF
 NGTB25N120FL2WAG
 NGTG40N120FL2WG

 RJH60F3DPQ-A0#T0
 APT40GR120B2SCD10
 APT15GT120BRG
 APT20GT60BRG
 NGTB75N65FL2WAG
 NGTG15N120FL2WG

 IXA30RG1200DHGLB
 IXA40RG1200DHGLB
 APT70GR65B2DU40
 NTE3320
 QP12W05S-37A
 IHFW40N65R5SXKSA1
 APT70GR120J

 APT35GP120JDQ2
 XD15H120CX1
 XD25H120CX0
 XP15PJS120CL1B1
 IGW30N60H3FKSA1
 STGWA8M120DF3
 IGW08T120FKSA1

 IGW75N60H3FKSA1
 FGH60N60SMD_F085
 FGH75T65UPD
 STGWA15H120F2
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 IHW20N120R5XKSA1

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 IKP20N60TXKSA1
 IHW20N65R5XKSA1
 APT70GR120JD60
 AOD5B60D
 APT70GR120L
 STGWT60H65FB

 STGWT60H65DFB
 STGWT40V60DF
 STGB10NB37LZT4
 STGWT60H65DFB
 STGWT60H65DFB