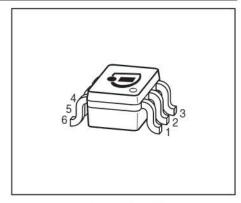
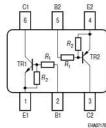


### NPN/PNP Silicon Digital Transistor Array

- Switching circuit, inverter, interface circuit, driver circuit
- Two (galvanic) internal isolated NPN/PNP Transistors in one package
- Built in bias resistor NPN and PNP (R<sub>1</sub>=2.2 kΩ, R<sub>2</sub>=47 kΩ)
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101

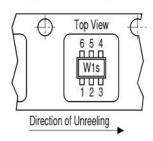






### Tape loading orientation

Junction - soldering point<sup>1)</sup>



Marking on SOT-363 package (for example W1s) corresponds to pin 1 of device

Position in tape: pin 1 opposite of feed hole side

EHA07193

Туре	Marking	Pin Configuration					Package	
BCR08PN	WFs	1=E1	2=B1	3=C2	4=E2	5=B2	6=C1	SOT363

### Maximum Ratings for NPN and PNP Types

Parameter	Symbol	Value	Unit	
Collector-emitter voltage	V <sub>CEO</sub>	50	V	
Collector-base voltage	V <sub>CBO</sub>	50		
Input forward voltage	V <sub>i(fwd)</sub>	20		
Input reverse voltage	$V_{i(rev)}$	5		
DC collector current	I <sub>C</sub>	100	mA	
Total power dissipation, T <sub>S</sub> = 115 °C	P <sub>tot</sub>	250	mW	
Junction temperature	$T_{\rm j}$	150	°C	
Storage temperature	T <sub>stq</sub>	-65 150		
Thermal Resistance	-			

 $<sup>^{1}</sup>$ For calculation of  $R_{thJA}$  please refer to Application Note AN077 (Thermal Resistance Calculation)

K/W

≤ 140

 $R_{\rm thJS}$ 



**Electrical Characteristics** at  $T_A$ =25°C, unless otherwise specified

Parameter	Symbol	eV	Unit			
		min.	typ.	max.		
DC Characteristics for NPN and PNP Types	S			10		
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	50	-	<b></b>	٧	
$I_{\rm C} = 100 \ \mu {\rm A}, \ I_{\rm B} = 0$	(d) £					
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	50	-	-		
$I_{\rm C} = 10 \ \mu {\rm A}, \ I_{\rm E} = 0$		v				
Collector cutoff current	/ <sub>CBO</sub>		=	100	nΑ	
$V_{CB} = 40 \text{ V}, I_{E} = 0$						
Emitter cutoff current	/ <sub>EBO</sub>	<i>5</i> -1	=	164	μA	
$V_{\text{EB}} = 5 \text{ V}, I_{\text{C}} = 0$						
DC current gain 1)	h <sub>FE</sub>	70	-	-	23	
$I_{\rm C} = 5 \text{ mA}, V_{\rm CE} = 5 \text{ V}$		·V			e.	
Collector-emitter saturation voltage1)	V <sub>CEsat</sub>	2	2	0.3	V	
$I_{\rm C}$ = 10 mA, $I_{\rm B}$ = 0.5 mA						
Input off voltage	$V_{i(off)}$	0.4	-	8.0		
$I_{\rm C}$ = 100 $\mu$ A, $V_{\rm CE}$ = 5 V	92 W					
Input on Voltage	V <sub>i(on)</sub>	0.5	=	1.1		
$I_{\rm C}$ = 2 mA, $V_{\rm CE}$ = 0.3 V		×	-1	0	6	
Input resistor	R <sub>1</sub>	1.5	2.2	2.9	kΩ	
Resistor ratio	$R_1/R_2$	0.042	0.047	0.052	<del>-</del> 2	
AC Characteristics for NPN and PNP Types	S					
Transition frequency	$f_{T}$	-	170	<b>14</b> 65	MHz	
$I_{\rm C}$ = 10 mA, $V_{\rm CE}$ = 5 V, $f$ = 100 MHz						
Collector-base capacitance	C <sub>cb</sub>	-	2	Na (	pF	
V <sub>CB</sub> = 10 V, f = 1 MHz						

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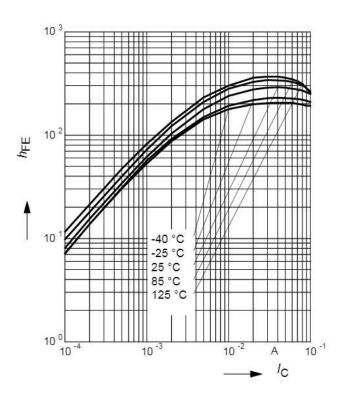
<sup>1)</sup> Pulse test: t < 300μs; D < 2%



### **NPN Type**

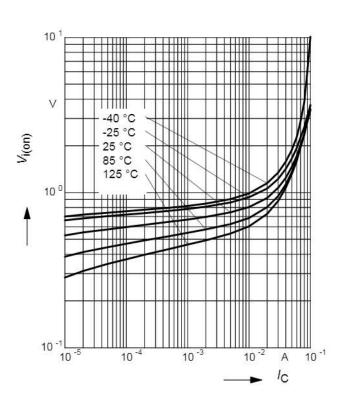
### DC Current Gain $h_{FE} = f(I_C)$

 $V_{CE}$  = 5V (common emitter configuration)



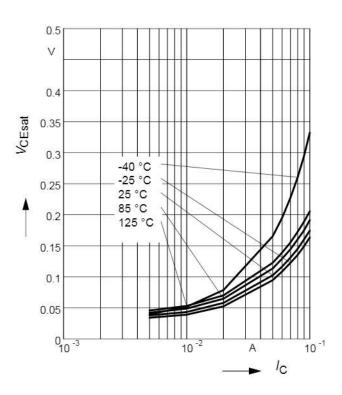
### Input on Voltage $V_{i(on)} = f(I_C)$

 $V_{CE} = 0.3V$  (common emitter configuration)



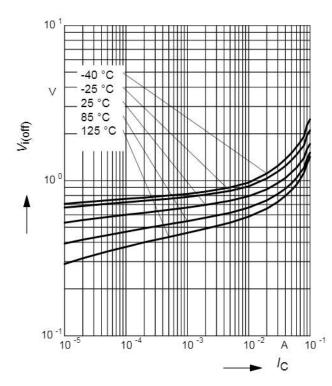
### Collector-Emitter Saturation Voltage

 $V_{CEsat} = f(I_{C}), I_{C}/I_{B} = 20$ 



### Input off voltage $V_{i(off)} = f(I_C)$

 $V_{CE}$  = 5V (common emitter configuration)

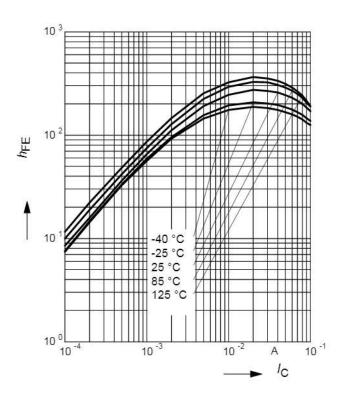




### **PNP Type**

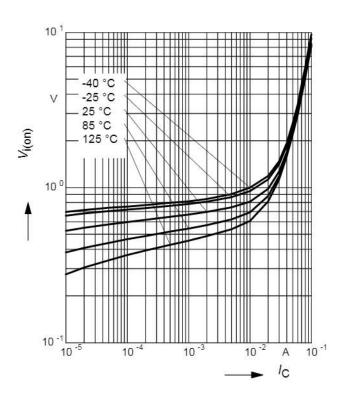
### DC Current Gain $h_{FE} = f(I_C)$

 $V_{CE}$  = 5V (common emitter configuration)



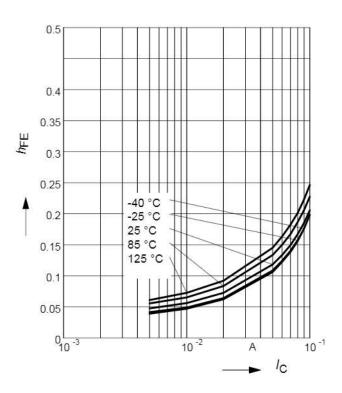
### Input on Voltage $V_{i(on)} = f(I_C)$

 $V_{CE} = 0.3V$  (common emitter configuration)



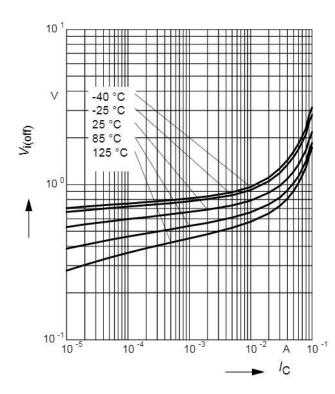
### Collector-Emitter Saturation Voltage

 $V_{CEsat} = f(I_{C}), I_{C}/I_{B} = 20$ 



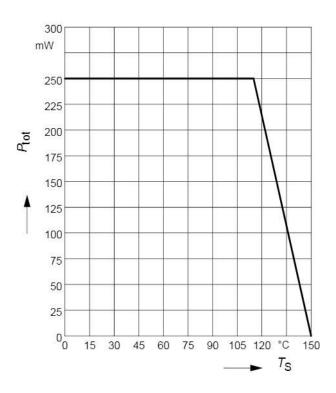
### Input off voltage $V_{i(off)} = f(I_C)$

 $V_{CE}$  = 5V (common emitter configuration)





### Total power dissipation $P_{tot} = f(T_S)$

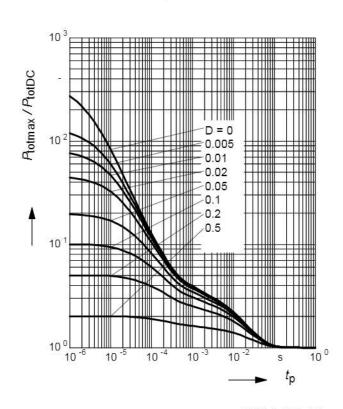


### Permissible Pulse Load $R_{thJS} = f(t_p)$

# 10<sup>3</sup> K/W 10<sup>2</sup> RthJS 10 <sup>1</sup> 10 ° 0.01 0.005 D = 0

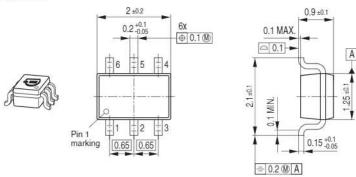
### Permissible Pulse Load

$$P_{\text{totmax}} / P_{\text{totDC}} = f(t_{p})$$

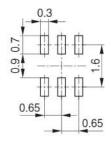




### Package Outline

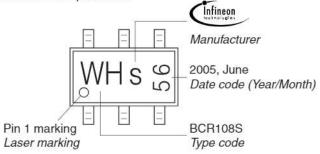


#### Foot Print



### Marking Layout (Example)

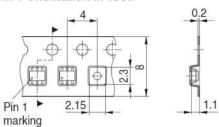
Small variations in positioning of Date code, Type code and Manufacture are possible.



### Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel

For symmetric types no defined Pin 1 orientation in reel.



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