

# BCR410W

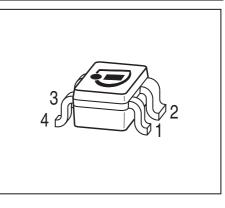
### **Active Bias Controller**

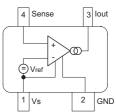
#### Characteristics

- Supplies stable bias current from 1.8V operating voltage on
- Low voltage drop: 110mV for 10mA collector currrent

#### **Application notes**

- Stabilizing bias current of NPN transistors and FET's from 100µA to 20mA
- Ideal supplement for Sieget and other transistors





- Pb-free (RoHS compliant) package<sup>1)</sup>
- Qualified according AEC Q101



Туре	Marking	Pin Configuration				Package
BCR410W	W8s	1= Vs	2=GND	3=lout	4=Sense	SOT343

#### **Maximum Ratings**

Parameter	Symbol	Value	Unit	
Supply voltage	V <sub>S</sub>	18	V	
Output current	/ <sub>out</sub>	0.5	mA	
Total power dissipation, $T_{\rm S}$ = 110 °C	P <sub>tot</sub>	100	mW	
Junction temperature	T <sub>j</sub>	150	°C	
Storage temperature	T <sub>stg</sub>	-65 150		

#### **Thermal Resistance**

Junction - soldering point <sup>2)</sup>	R <sub>thJS</sub>	≤ 470	K/W

<sup>1</sup>Pb-containing package may be available upon special request

 $^2 \rm For}$  calculation of  ${\it R}_{\rm thJA}$  please refer to Application Note Thermal Resistance



Parameter	Symbol	Values			Unit
		min.	typ.	max.	1
DC Characteristics	•	,		•	•
Additional current consumption	<i>I</i> 0	-	200	400	μA
$V_{\rm S}$ = 3 V					
DC Characteristics with stabilized NPN	-Transistors	•		•	F
Lowest sufficient battery voltage	V <sub>Smin</sub>	-	1.8	-	V
Voltage drop	V <sub>drop</sub>	-	110	-	mV
<i>I</i> <sub>C</sub> = 10 mA					
Change of $I_{\rm C}$ versus $h_{\rm FE}$	$\Delta I_{\rm C}/I_{\rm C}$	-	tbd	-	$\Delta h_{FE}$
h <sub>FE</sub> = 50					h <sub>FE</sub>
Change of $I_{\rm C}$ versus $V_{\rm S}$	$\Delta I_{\rm C}/I_{\rm C}$	-	2	-	%/V
$V_{\rm S}$ = 3 V					
Change of $I_{\rm C}$ versus $T_{\rm A}$	$\Delta I_{\rm C}/I_{\rm C}$	-	0.15	-	%/K

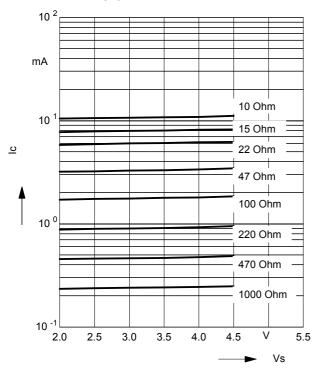
# **Electrical Characteristics** at $T_A = 25^{\circ}C$ , unless otherwise specified



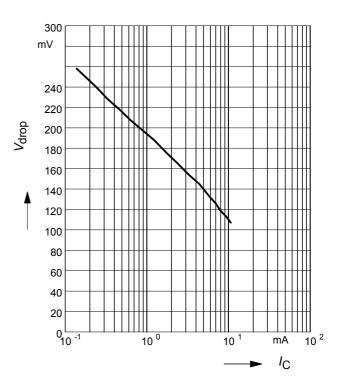
**Collector Current**  $I_{\rm C} = f(V_{\rm S})$ 

of stabilized NPN Transistor

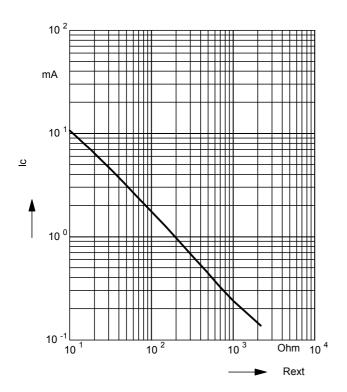
Parameter  $R_{ext.}$  ( $\Omega$ )



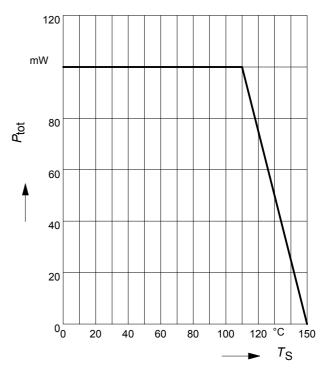
**Voltage drop**  $V_{drop} = f(I_C)$ 



**Collector current**  $I_{\rm C} = f(R_{\rm ext.})$ of stabilized NPN Transistor

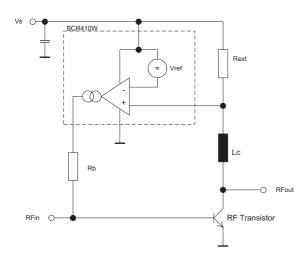


Total power dissipation  $P_{\text{tot}} = f(T_{\text{S}})$ 

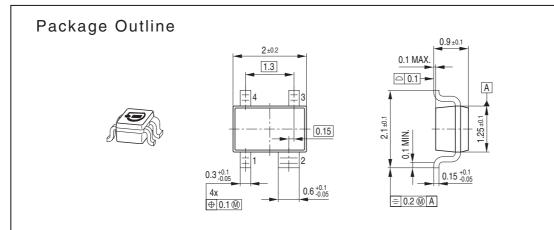




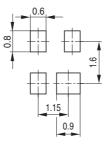
# **Application Circuit:**



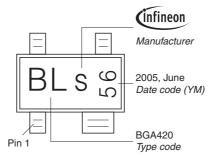




## Foot Print

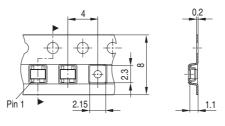


## Marking Layout (Example)



# Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel





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