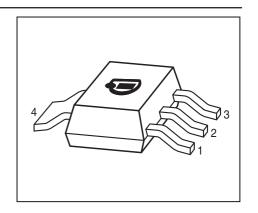


## **PNP Silicon High-Voltage Transistors**

- Suitable for video output stages in TV sets and switching power supplies
- High breakdown voltage
- Low collector-emitter saturation voltage
- Complementary types: BFN38 (NPN)
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101







Туре	Marking	Pin Configuration					Package	
BFN39	BFN39	1=B	2=C	3=E	4=C	=.	-	SOT223

### **Maximum Ratings**

Parameter	Symbol	Value	Unit	
Collector-emitter voltage	V <sub>CEO</sub>	300	V	
Collector-base voltage	$V_{\mathrm{CBO}}$	300		
Emitter-base voltage	V <sub>EBO</sub>	5		
Collector current	I <sub>C</sub>	200	mA	
Peak collector current, $t_p \le 10 \text{ ms}$	I <sub>CM</sub>	500		
Base current	l <sub>B</sub>	100		
Peak base current	l <sub>BM</sub>	200		
Total power dissipation-	P <sub>tot</sub>	1.5	W	
<i>T</i> <sub>S</sub> ≤ 124 °C				
Junction temperature	$T_{i}$	150	°C	
Storage temperature	$T_{\rm sta}$	-65 150		

### **Thermal Resistance**

Parameter	Symbol	Value	Unit
Junction - soldering point <sup>1)</sup>	R <sub>thJS</sub>	≤ 17	K/W

1

 $<sup>^{1}</sup>$ For calculation of  $R_{thJA}$  please refer to Application Note AN077 (Thermal Resistance Calculation)



**Electrical Characteristics** at  $T_A = 25$ °C, unless otherwise specified

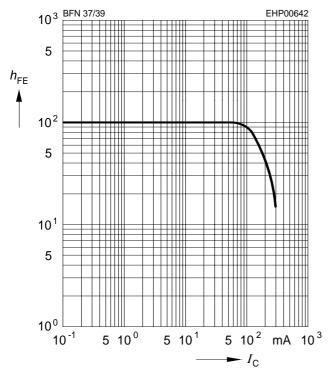
Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics	•				•
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	300	-	-	V
$I_{\rm C}$ = 1 mA, $I_{\rm B}$ = 0					
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	300	-	-	
$I_{\rm C} = 100 \ \mu \text{A}, I_{\rm E} = 0$					
Emitter-base breakdown voltage	$V_{(BR)EBO}$	5	-	-	
$I_{\rm E} = 100 \ \mu A, I_{\rm C} = 0$					
Collector-base cutoff current	I <sub>CBO</sub>				μA
$V_{\text{CB}} = 250 \text{ V}, I_{\text{E}} = 0$		-	-	0.1	
$V_{\text{CB}} = 250 \text{ V}, I_{\text{E}} = 0 , T_{\text{A}} = 150 ^{\circ}\text{C}$		-	-	20	
Emitter-base cutoff current	I <sub>EBO</sub>	-	-	100	nA
$V_{\rm EB} = 5  \text{V},  I_{\rm C} = 0$					
DC current gain <sup>1)</sup>	h <sub>FE</sub>				-
$I_{\rm C}$ = 1 mA, $V_{\rm CE}$ = 10 V		25	-	-	
$I_{\rm C}$ = 10 mA, $V_{\rm CE}$ = 10 V		40	-	-	
$I_{\rm C}$ = 30 mA, $V_{\rm CE}$ = 10 V		30	-	-	
Collector-emitter saturation voltage <sup>1)</sup>	V <sub>CEsat</sub>	-	-	0.5	V
$I_{\rm C}$ = 20 mA, $I_{\rm B}$ = 2 mA					
Base emitter saturation voltage <sup>1)</sup>	V <sub>BEsat</sub>	-	-	0.9	
$I_{\rm C}$ = 20 mA, $I_{\rm B}$ = 2 mA					
AC Characteristics					
Transition frequency	f <sub>T</sub>	-	100	_	MHz
$I_{\rm C}$ = 20 MHz, $V_{\rm CE}$ = 10 V, $f$ = 100 MHz					
Collector-base capacitance	C <sub>cb</sub>	-	2.5	_	pF
$V_{CB} = 30 \text{ V}, f = 1 \text{ MHz}$					

<sup>&</sup>lt;sup>1</sup>Pulse test:  $t < 300\mu s$ ; D < 2%



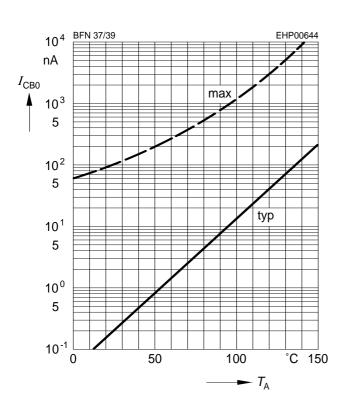
## **DC** current gain $h_{FE} = f(I_C)$

$$V_{CE}$$
 = 10 V



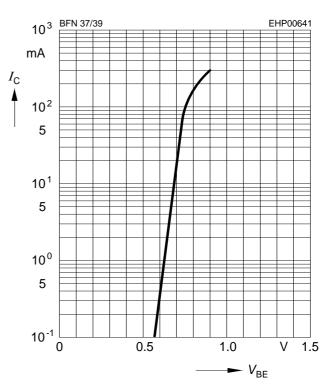
# Collector cutoff current $I_{CBO} = f(T_A)$

$$V_{\rm CBO}$$
 = 200 V



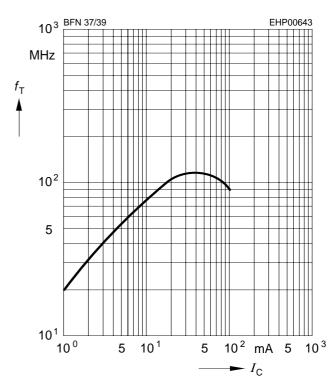
# Collector current $I_{C} = f(V_{BE})$

$$V_{CE} = 10V$$



# Transition frequency $f_T = f(I_C)$

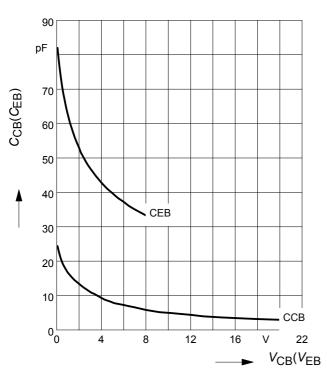
$$V_{CE}$$
 = 10 V

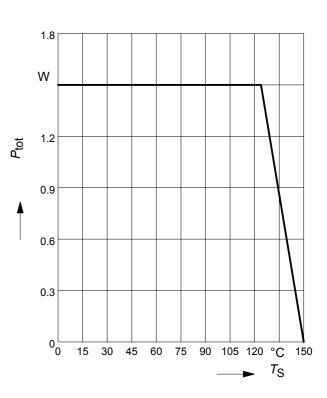




# Collector-base capacitance $C_{cb} = f(V_{CB})$ Emitter-base capacitance $C_{eb} = f(V_{EB})$

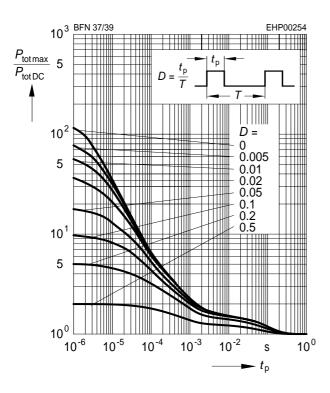
# Total power dissipation $P_{tot} = f(T_S)$





## **Permissible Pulse Load**

$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_{\text{p}})$$





# Package Outline 1.6±0.1 $6.5 \pm 0.2$ 0.1 MAX 3±0.1 $\tilde{\Omega}$ $3.5 \pm 0.2$ 7±0.3 2 2.3 0.7±0.1 0.28 ±0.04 4.6 0...10° ⊕ 0.25 M A = 0.25 M B Foot Print 3.5 1.2 1.1 Marking Layout (Example) **(**infineon Manufacturer 2005, 24 CW Date code (YYWW) 0524 16 BCP52-16 Type code Pin 1 Packing Reel ø180 mm = 1.000 Pieces/Reel Reel ø330 mm = 4.000 Pieces/Reel 0.3 MAX. $\oplus$ 7.55

6.8

5

1.75



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