## RF ESD Protection Diodes

- ESD / transient protection of RF antenna / interfaces or ultra high speed data lines acc. to: IEC61000-4-2 (ESD): $\pm 20 \mathrm{kV}$ (contact) IEC61000-4-4 (EFT): 40 A (5/50 ns) IEC61000-4-5 (surge): $10 \mathrm{~A}(8 / 20 \mu \mathrm{~s})$
- Ultra low capacitance of 1 pF typ.
( 0.5 pF per diode)
- Low clamping voltage

- Pb-free (ROHS compliant) package


## Applications in anti-parallel configuration

- For low RF signal levels without superimposed

DC voltage: e.g. GPS, WLAN, Bluetooth

## Applications in rail-to-rail configuration

- For high RF signal levels or low RF signal levels with superimposed DC voltage: e.g. HDMI, S-ATA, Gbit Ethernet


## ESD1P0RFW

ESD1P0RFS


| Type | Package | Configuration | Marking |
| :--- | :--- | :--- | :--- |
| ESD1P0RFS | SOT363 | 2 channels | E6s |
| ESD1P0RFW | SOT323 | 1 channel | E6s |

ESD1P0RF...

Maximum Ratings at $T_{\mathrm{A}}=25^{\circ} \mathrm{C}$, unless otherwise specified

| Parameter | Symbol | Value | Unit |
| :--- | :--- | :---: | :--- |
| ESD contact discharge ${ }^{1)}$ | $V_{\mathrm{ESD}}$ | 20 | kV |
| Peak pulse current $\left(t_{\mathrm{p}}=8 / 20 \mu \mathrm{~s}\right)^{2)}$ | $I_{\mathrm{pp}}$ | 10 | A |
| Operating temperature range | $T_{\mathrm{op}}$ | $-55 \ldots 150$ | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | $T_{\text {stg }}$ | $-65 \ldots 150$ |  |

Electrical Characteristics at $T_{\mathrm{A}}=25^{\circ} \mathrm{C}$, unless otherwise specified

| Parameter | Symbol | Values |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | min. | typ. | max. |  |
| Characteristics |  |  |  |  |  |
| Reverse working voltage ${ }^{\text {3 }}$ | $V_{\text {RWM }}$ | - | - | 70 | V |
| Reverse current $V_{\mathrm{R}}=70 \mathrm{~V}$ | $I_{\text {R }}$ | - | - | 100 | nA |
| Forward clamping voltage ${ }^{2}$ ) $\begin{aligned} & I_{\mathrm{PP}}=3 \mathrm{~A}, t_{\mathrm{p}}=8 / 20 \mu \mathrm{~s} \\ & I_{\mathrm{PP}}=10 \mathrm{~A}, t_{\mathrm{p}}=8 / 20 \mu \mathrm{~s} \end{aligned}$ | $V_{\text {FC }}$ | - | $\begin{gathered} 4 \\ 12 \end{gathered}$ | $\begin{gathered} 7 \\ 15 \end{gathered}$ | V |
| Line capacitance ${ }^{4)}$ $\begin{aligned} & V_{\mathrm{R}}=0 \mathrm{~V}, f=1 \mathrm{MHz} \\ & V_{\mathrm{R}}=0 \mathrm{~V}, f=1 \mathrm{MHz} \text {, for Application example } 4 \end{aligned}$ | $C_{\text {T }}$ | - | $\begin{gathered} 1 \\ 0.5 \end{gathered}$ | $\begin{gathered} 1.5 \\ 0.75 \end{gathered}$ | pF |
| Series inductance (per diode) <br> SOT323 <br> SOT363 | $L_{S}$ | - | $\begin{aligned} & 1.4 \\ & 1.6 \end{aligned}$ |  | nH |

${ }^{1} V_{\text {ESD }}$ according to IEC61000-4-2, only valid in anti-parallel or rail-to-rail connection.
Please refer to the application examples.
${ }^{2} / \mathrm{pp}$ according to IEC61000-4-5, only valid in anti-parallel or rail-to-rail connection.
Please refer to the application examples.
${ }^{3}$ Only valid in rail-to-rail configuration $V_{\mathrm{CC}} \geq V_{\mathrm{RWM}}$
${ }^{4}$ Total capacitance line to ground (2 diodes in parallel)

ESD1P0RF...

Forward clamping voltage $V_{\mathrm{FC}}=f\left(I_{\mathrm{PP}}\right)$ $t_{\mathrm{p}}=8 / 20 \mu \mathrm{~s}$


Forward current $I_{F}=f\left(V_{F}\right)$
$T_{\mathrm{A}}=$ Parameter
in anti-parallel configuration


Reverse current $I_{\mathrm{R}}=f\left(V_{\mathrm{R}}\right)$
$T_{\mathrm{A}}=$ Parameter
in rail-to-rail configuration


Line capacitance $C_{\top}=f(f)$
$V_{\mathrm{R}}=0 \mathrm{~V}$


Insertion loss $\left|\mathrm{S}_{21}\right|^{2}=f(f)$
$V_{\mathrm{R}}=0 \mathrm{~V}$, line to ground, $\mathrm{Z}=50 \Omega$


## 1. Application example ESD1P0RFW

1 channel, anti-parallel configuration


## 2. Application example ESD1PORFW

1 channel, rail-to-rail configuration


Pin 2 should be connected to the positive supply voltage and pin 1 should be connected directly to a connected directly to a ground plane on the board. lamped input voltage at I/O port is limited to Vcc + VF a positive transients and 0 V VF at negative transients (VF ... diode forward voltage drop).

## 3. Application example ESD1PORFS

2 channel, anti-parallel configuration
2 protected signal lines, superimposed DC voltage up to + -VF (diode forward voltage)


Pins 1, 2 and 4, 5 should be connected in parallel directly to a ground plane on the board. Clamped input voltage at I/O port is limited to $\pm \mathrm{VCL}$ (clamping voltage) at positive resp. negative transients.

## 4. Application example ESD1P0RFS

1 channel, low capacitance anti-parallel configuration


Pins 1 and 5 should be connected directly to a ground plane on the board. Pins 3, 6 are not connected. Clamped input voltage at I . O port is limited to $+-2 \times$ VCL (clamping voltage) at positive resp. negative transients.

RF line on PCB

Package Outline


Foot Print


Marking Layout (Example)


## Standard Packing

Reel $\varnothing 180 \mathrm{~mm}=3.000$ Pieces/Reel
Reel $\varnothing 330 \mathrm{~mm}=10.000$ Pieces/Reel


## Package Outline



Foot Print


## Marking Layout (Example)

Small variations in positioning of
Date code, Type code and Manufacture are possible.


## Standard Packing

Reel $\varnothing 180 \mathrm{~mm}=3.000$ Pieces/Reel
Reel $\varnothing 330 \mathrm{~mm}=10.000$ Pieces/Reel
For symmetric types no defined Pin 1 orientation in reel.


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