

# 6<sup>th</sup> Generation CoolSiC<sup>™</sup>

### 650V SiC Schottky Diode

The CoolSiC<sup>TM</sup> generation 6 (G6) is the leading edge technology from Infineon for the SiC Schottky barrier diodes. The Infineon proprietary innovative G5 technology was enhanced in G6 by introducing further advancements like a novel Schottky metal system. The result is a family of products with improved efficiency over all load conditions, resulting from a lower figure of merit ( $Q_c \times V_f$ ). The CoolSiC<sup>TM</sup> Schottky diode 650 V G6 has been designed to complement our 600 V and 650 V CoolMOS<sup>TM</sup> 7 families, meeting the most stringent application requirements in this voltage range.

Table 1 Key per	formance parar	neters	
Parameter	Value	Unit	
V <sub>RRM</sub>	650	V	
$Q_{c} (V_{R} = 400 \text{ V})$	14.7	nC	
$E_{c} (V_{R} = 400 \text{ V})$	2.7	μJ	
$I_F \ (T_C \le 140 \ ^{\circ}\text{C}, D = 1)$	10	A	
$V_F (I_F = 10 \text{ A}, T_j = 25 \text{ °C})$	1.25	V	

Table 2	Package information

Type / ordering Code	Package	Marking
IDH10G65C6	PG-TO220-2	D1065C6

### Features

- Best in class forward voltage (1.25 V)
- Best in class figure of merit  $(Q_c \times V_F)$
- High dv/dt ruggedness (150 V/ns)

### **Benefits**

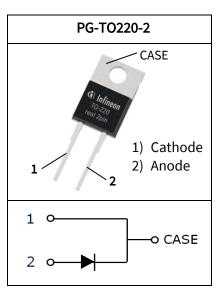
- System efficiency improvement
- System cost and size savings due to the reduced cooling requirements
- Enabling higher frequency and increased power density

### **Potential Applications**

- Power factor correction in SMPS
- Solar inverter
- Uninterruptible power supply

### **Product Validation**

• Qualified for industrial applications according to the relevant tests of JEDEC (J-STD20 and JESD22)







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	Maximum ratings Thermal characteristics Electrical characteristics Static characteristics AC characteristics Diagrams Simplified forward characteristic Package outlines



### Maximum ratings 1

#### Table 3 **Maximum ratings**

Daramatar	Cumhal	Values			11	Nete/Testessedition
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note/Test condition
		-	-	10		$T_C \le 140 ^{\circ}\text{C}, D = 1$
Continuous forward current	$I_F$	-	-	13		$T_c \le 125 ^{\circ}\text{C}, D = 1$
		-	-	24		$T_{c} \leq 25 ^{\circ}\text{C}, D = 1$
Surge-repetitive forward current, sine halfwave <sup>1</sup>	I <sub>F,RM</sub>	-	-	44	A	$T_c = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$
Surge non-repetitive forward	,	-	-	55		$T_c = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$
current, sine halfwave	I <sub>F,SM</sub>	-	-	44		$T_c = 150 ^{\circ}\text{C}, t_p = 10 \text{ms}$
Non-repetitive peak forward current	I <sub>F,max</sub>	-	-	600		$T_c = 25 \text{ °C}, t_p = 10  \mu\text{s}$
i <sup>2</sup> t value	∫i²dt	-	-	15	A <sup>2</sup> c	$T_c = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$
I <sup>-</sup> t value	JI-al	-	-	10	– A <sup>2</sup> s	$T_c = 150 ^{\circ}\text{C}, t_p = 10 \text{ms}$
Repetitive peak reverse voltage	V <sub>RRM</sub>	-	-	650	V	<i>T<sub>c</sub></i> = 25 °C
Diode dv/dt ruggedness	dv/dt	-	-	150	V/ns	$V_R = 0480 \text{ V}$
Power dissipation	P <sub>tot</sub>	-	-	72	W	$T_c = 25^{\circ}\text{C}, R_{thJC,max}$
Operating and storage temperature	$\begin{bmatrix} T_j \\ T_{stg} \end{bmatrix}$	-55	-	175	°C	-
Mounting torque	-	_	-	70	Ncm	M3 screw

#### **Thermal characteristics** 2

#### Thermal characteristics (PG-TO-220-2) Table 4

Daramatar	Symphol	Values			11014	Note /Test condition
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note/Test condition
Thermal resistance, junction- case	$R_{ m thJC}$	-	1.3	2.1		-
Thermal resistance, junction- ambient	R <sub>thJA</sub>	_	-	62	K/W	leaded
Soldering temperature, wavesoldering only allowed at leads	$T_{sold}$	-	-	260	°C	1.6 mm (0.063 in.) from case for 10 s

<sup>&</sup>lt;sup>1</sup> The surge-repetitive forward current test was performed with 1000 pulses (half-wave rectified sine with the 10 ms period). **Final Datasheet** 3



## 3 Electrical characteristics

## 3.1 Static characteristics

### Table 5Static characteristics

Parameter	Cumphiel	Values			11	Nata /Task say dition
	Symbol	Min.	Тур.	Max.	Unit	Note/Test condition
DC blocking voltage	V <sub>DC</sub>	650	_	_		<i>T<sub>j</sub></i> = 25 °C
Diode forward voltage	V <sub>F</sub>	_	1.25	1.35	V	<i>I<sub>F</sub></i> = 10 A, <i>T<sub>j</sub></i> = 25 °C
		_	1.5	_		<i>I<sub>F</sub></i> = 10 A, <i>T<sub>j</sub></i> = 150 °C
Reverse current I <sub>R</sub>	I <sub>R</sub>	_	1.0	33		$V_R$ = 420 V, $T_j$ = 25 °C
		_	33	_	μA	$V_R$ = 420 V, $T_j$ = 125 °C
		-	77	-		$V_R$ = 420 V, $T_j$ = 150 °C

### 3.2 AC characteristics

### Table 6AC characteristics

Darameter	Cumhal	Values			11	Nete/Test Condition
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note/Test Condition
Total capacitive charge			14.7		nC	$V_R$ = 400 V, $T_j$ = 150 °C,
Total capacitive charge	Qc	-	14.7	-		$di/dt = 200 \text{ A}/\mu \text{s}, I_F \leq I_{F,MAX}$
Total capacitance	С	-	495 –	5 –		$V_R = 1 \text{ V}, f = 1 \text{ MHz},$
						<i>T<sub>j</sub></i> = 25 °C
			29 –	~ 5	$V_R$ = 300 V, $f$ = 1 MHz,	
		_		-	pF	<i>T<sub>j</sub></i> = 25 °C
		- 28	20			$V_R$ = 600 V, $f$ = 1 MHz,
			28	-		<i>T<sub>j</sub></i> = 25 °C



## 4 Diagrams

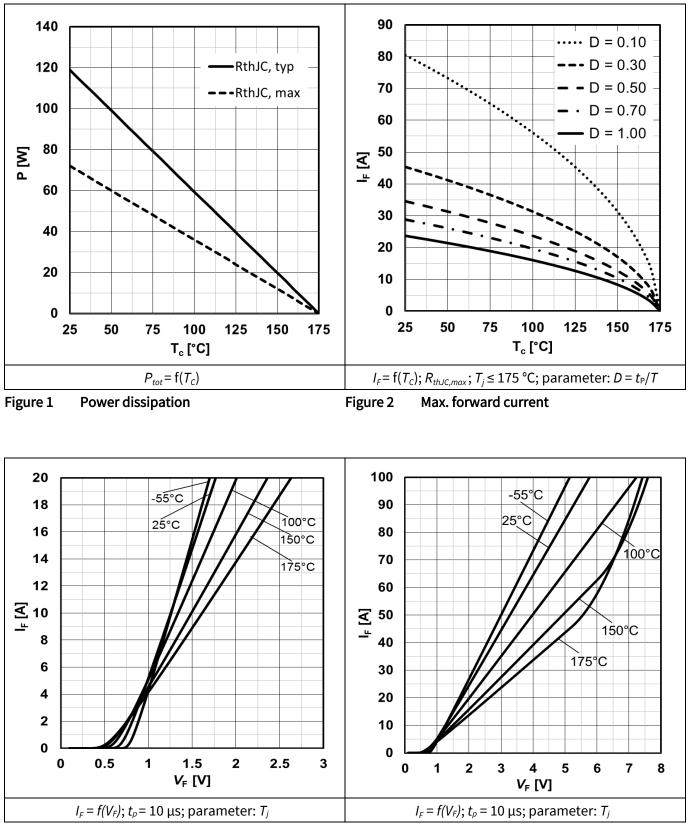




Figure 4 Typ. forward characteristics in surge current

### 6<sup>th</sup> Generation CoolSiC<sup>™</sup> IDH10G65C6

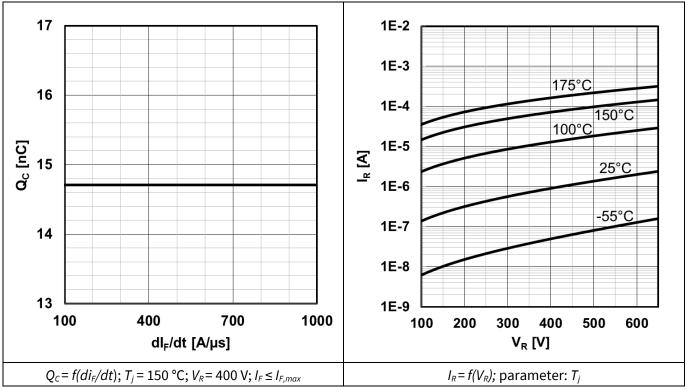
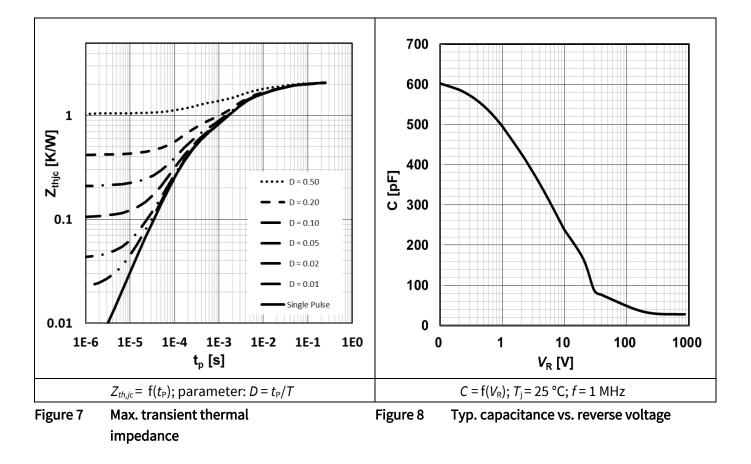


Figure 5 Typ. cap. charge vs. current slope

Figure 6 Typ. reverse current vs. reverse voltage



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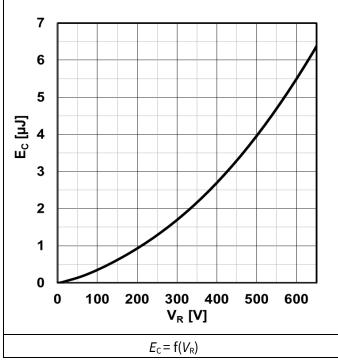
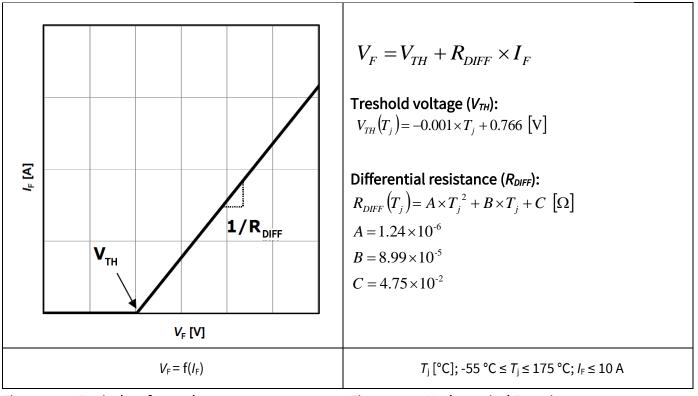


Figure 9 Typ. capacitance stored energy

## 5 Simplified forward characteristic



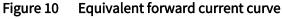


Figure 11 Mathematical Equation



## 6 Package outlines

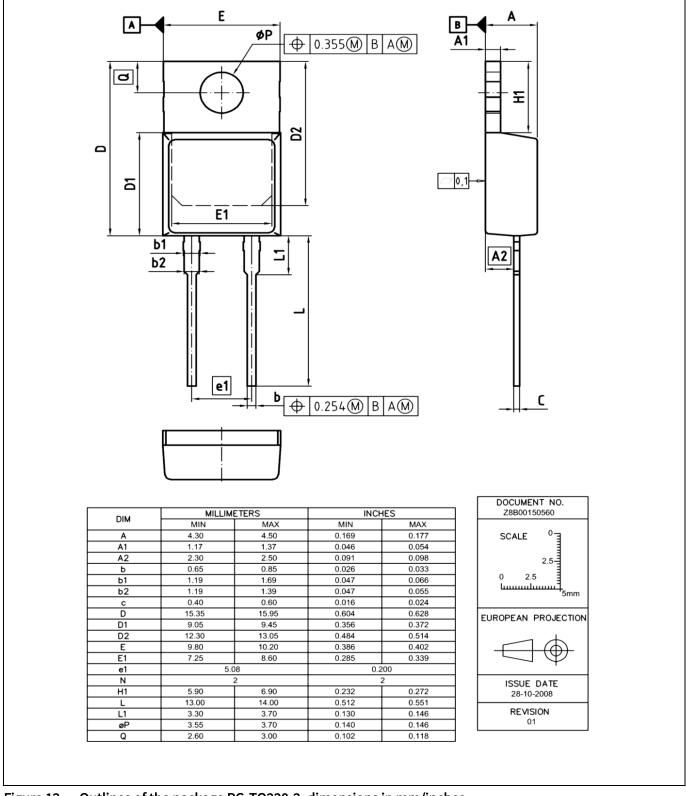


Figure 12 Outlines of the package PG-TO220-2, dimensions in mm/inches



### **Revision History**

### Major changes since the last revision

Revision	Date	Subject (major changes since last revision)	
2.0	2017-05-23	Release of final version	

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Document reference

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