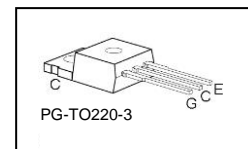
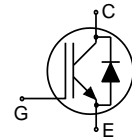


Low Loss DuoPack : IGBT in TRENCHSTOP™ and Fieldstop technology with soft, fast recovery anti-parallel Emitter Controlled HE diode



Features:

- Very low $V_{CE(sat)}$ 1.5V (typ.)
- Maximum Junction Temperature 175°C
- Short circuit withstand time 5 μ s
- Designed for :
 - Variable Speed Drive for washing machines, air conditioners and induction cooking
 - Uninterrupted Power Supply
- TRENCHSTOP™ and Fieldstop technology for 600V applications offers :
 - very tight parameter distribution
 - high ruggedness, temperature stable behavior
- NPT technology offers easy parallel switching capability due to positive temperature coefficient in $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel Emitter Controlled HE diode
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



| Type | V_{CE} | I_C | $V_{CE(sat), T_j=25^\circ C}$ | $T_{j,max}$ | Marking Code | Package |
|-----------|----------|-------|-------------------------------|-------------|--------------|-------------|
| IKP10N60T | 600V | 10A | 1.5V | 175°C | K10T60 | PG-TO-220-3 |

Maximum Ratings

| Parameter | Symbol | Value | Unit |
|--|--------------|------------|---------|
| Collector-emitter voltage, $T_j \geq 25^\circ C$ | V_{CE} | 600 | V |
| DC collector current, limited by $T_{j,max}$ $T_C = 25^\circ C$ $T_C = 100^\circ C$ | I_C | 24 18 | A |
| Pulsed collector current, t_p limited by $T_{j,max}$ | $I_{C,puls}$ | 30 | |
| Turn off safe operating area, $V_{CE} = 600V$, $T_j = 175^\circ C$, $t_p = 1\mu s$ | - | 30 | |
| Diode forward current, limited by $T_{j,max}$ $T_C = 25^\circ C$ $T_C = 100^\circ C$ | I_F | 24 18 | |
| Diode pulsed current, t_p limited by $T_{j,max}$ | $I_{F,puls}$ | 30 | |
| Gate-emitter voltage | V_{GE} | ± 20 | V |
| Short circuit withstand time ²⁾ $V_{GE} = 15V$, $V_{CC} \leq 400V$, $T_j \leq 150^\circ C$ | t_{SC} | 5 | μs |
| Power dissipation $T_C = 25^\circ C$ | P_{tot} | 110 | W |
| Operating junction temperature | T_j | -40...+175 | °C |
| Storage temperature | T_{stg} | -55...+150 | |
| Soldering temperature, wavesoldering, 1.6 mm (0.063 in.) from case for 10s | | 260 | |

¹ J-STD-020 and JESD-022

²⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

| Parameter | Symbol | Conditions | Max. Value | Unit |
|---|-------------|------------|------------|------|
| Characteristic | | | | |
| IGBT thermal resistance, junction – case | R_{thJC} | | 1.35 | K/W |
| Diode thermal resistance, junction – case | R_{thJCD} | | 1.9 | |
| Thermal resistance, junction – ambient | R_{thJA} | | 62 | |

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

| Parameter | Symbol | Conditions | Value | | | Unit |
|--------------------------------------|---------------|--|-------|------|------|---------------|
| | | | min. | typ. | max. | |
| Static Characteristic | | | | | | |
| Collector-emitter breakdown voltage | $V_{(BR)CES}$ | $V_{GE}=0V, I_C=0.2mA$ | 600 | - | - | V |
| Collector-emitter saturation voltage | $V_{CE(sat)}$ | $V_{GE} = 15V, I_C=10A$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$ | - | 1.5 | 2.05 | |
| | | | - | 1.8 | - | |
| Diode forward voltage | V_F | $V_{GE}=0V, I_F=10A$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$ | - | 1.6 | 2.0 | |
| | | | - | 1.6 | - | |
| Gate-emitter threshold voltage | $V_{GE(th)}$ | $I_C=0.3mA, V_{CE}=V_{GE}$ | 4.1 | 4.6 | 5.7 | |
| Zero gate voltage collector current | I_{CES} | $V_{CE}=600V,$ $V_{GE}=0V$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$ | - | - | 40 | μA |
| | | | - | - | 1000 | |
| Gate-emitter leakage current | I_{GES} | $V_{CE}=0V, V_{GE}=20V$ | - | - | 100 | nA |
| Transconductance | g_{fs} | $V_{CE}=20V, I_C=10A$ | - | 6 | - | S |
| Integrated gate resistor | R_{Gint} | | none | | | Ω |

Dynamic Characteristic

| | | | | | | |
|--|-------------|---|---|-----|---|----|
| Input capacitance | C_{iss} | $V_{CE}=25V,$ $V_{GE}=0V,$ $f=1MHz$ | - | 551 | - | pF |
| Output capacitance | C_{oss} | | - | 40 | - | |
| Reverse transfer capacitance | C_{riss} | | - | 17 | - | |
| Gate charge | Q_{Gate} | $V_{CC}=480V, I_C=10A$ $V_{GE}=15V$ | - | 62 | - | nC |
| Internal emitter inductance measured 5mm (0.197 in.) from case | L_E | | - | 7 | - | nH |
| Short circuit collector current ¹⁾ | $I_{C(SC)}$ | $V_{GE}=15V, t_{SC}\leq 5\mu s$ $V_{CC} = 400V,$ $T_j = 25^\circ\text{C}$ | - | 100 | - | A |

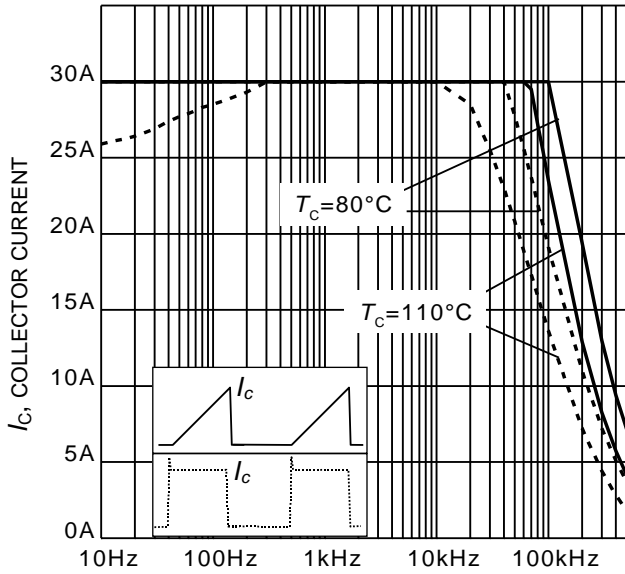
¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

| Parameter | Symbol | Conditions | Value | | | Unit |
|--|--------------|--|-------|------|------|------------------------|
| | | | min. | typ. | max. | |
| IGBT Characteristic | | | | | | |
| Turn-on delay time | $t_{d(on)}$ | $T_j=25^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=10\text{A}$, $V_{GE}=0/15\text{V}$, $r_G=23\Omega$, $L_\sigma=60\text{nH}$, $C_\sigma=40\text{pF}$ | - | 12 | - | ns |
| Rise time | t_r | | - | 8 | - | |
| Turn-off delay time | $t_{d(off)}$ | | - | 215 | - | |
| Fall time | t_f | | - | 38 | - | |
| Turn-on energy | E_{on} | L_σ , C_σ from Fig. E Energy losses include "tail" and diode reverse recovery. | - | 0.16 | - | mJ |
| Turn-off energy | E_{off} | | - | 0.27 | - | |
| Total switching energy | E_{ts} | | - | 0.43 | - | |
| Anti-Parallel Diode Characteristic | | | | | | |
| Diode reverse recovery time | t_{rr} | $T_j=25^\circ\text{C}$, $V_R=400\text{V}$, $I_F=10\text{A}$, $di_F/dt=880\text{A}/\mu\text{s}$ | - | 115 | - | ns |
| Diode reverse recovery charge | Q_{rr} | | - | 0.38 | - | μC |
| Diode peak reverse recovery current | I_{rrm} | | - | 10 | - | A |
| Diode peak rate of fall of reverse recovery current during t_b | di_{rr}/dt | | - | 680 | - | $\text{A}/\mu\text{s}$ |

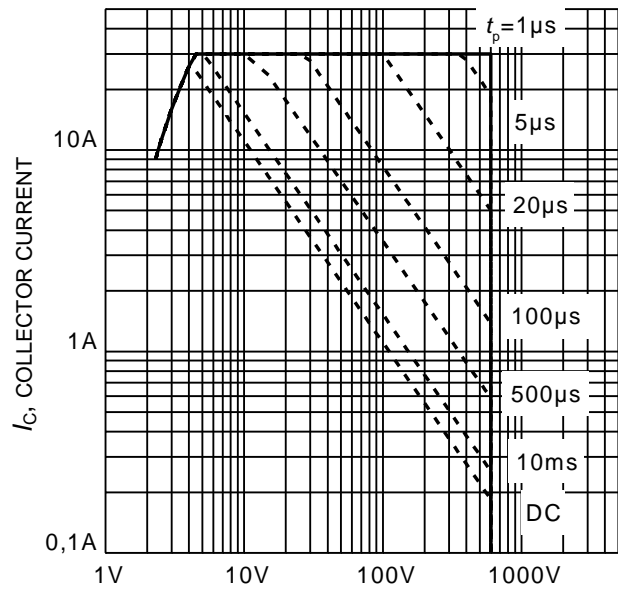
Switching Characteristic, Inductive Load, at $T_j=175^\circ\text{C}$

| Parameter | Symbol | Conditions | Value | | | Unit |
|--|--------------|---|-------|------|------|------------------------|
| | | | min. | typ. | max. | |
| IGBT Characteristic | | | | | | |
| Turn-on delay time | $t_{d(on)}$ | $T_j=175^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=10\text{A}$, $V_{GE}=0/15\text{V}$, $r_G=23\Omega$, $L_\sigma=60\text{nH}$, $C_\sigma=40\text{pF}$ | - | 10 | - | ns |
| Rise time | t_r | | - | 11 | - | |
| Turn-off delay time | $t_{d(off)}$ | | - | 233 | - | |
| Fall time | t_f | | - | 63 | - | |
| Turn-on energy | E_{on} | L_σ , C_σ from Fig. E Energy losses include "tail" and diode reverse recovery. | - | 0.26 | - | mJ |
| Turn-off energy | E_{off} | | - | 0.35 | - | |
| Total switching energy | E_{ts} | | - | 0.61 | - | |
| Anti-Parallel Diode Characteristic | | | | | | |
| Diode reverse recovery time | t_{rr} | $T_j=175^\circ\text{C}$, $V_R=400\text{V}$, $I_F=10\text{A}$, $di_F/dt=880\text{A}/\mu\text{s}$ | - | 200 | - | ns |
| Diode reverse recovery charge | Q_{rr} | | - | 0.92 | - | μC |
| Diode peak reverse recovery current | I_{rrm} | | - | 13 | - | A |
| Diode peak rate of fall of reverse recovery current during t_b | di_{rr}/dt | | - | 390 | - | $\text{A}/\mu\text{s}$ |



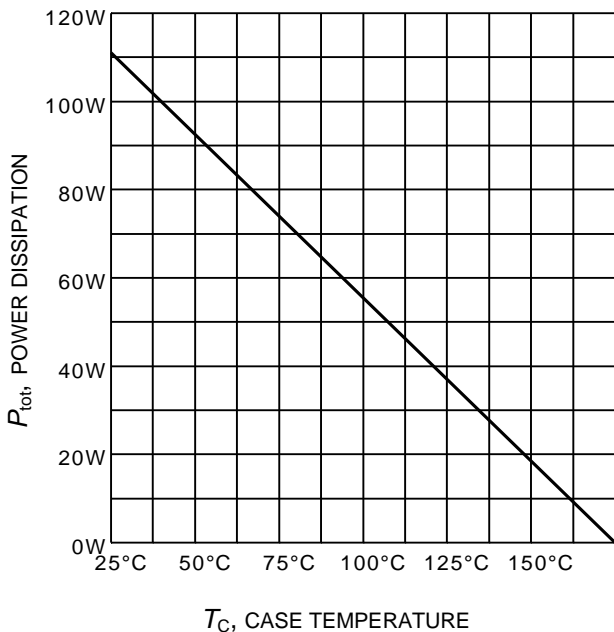
f , SWITCHING FREQUENCY

Figure 1. Collector current as a function of switching frequency
 ($T_j \leq 175^\circ\text{C}$, $D = 0.5$, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $r_G = 23\Omega$)



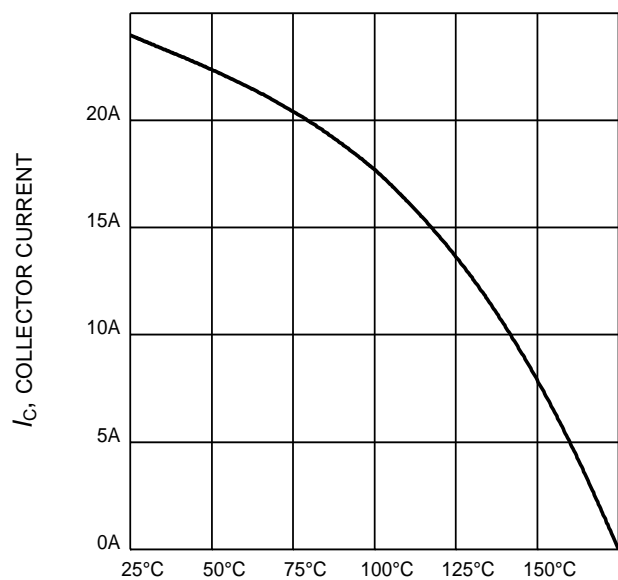
V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area
 ($D = 0$, $T_C = 25^\circ\text{C}$, $T_j \leq 175^\circ\text{C}$;
 $V_{GE} = 0/15\text{V}$)



T_C , CASE TEMPERATURE

Figure 3. Power dissipation as a function of case temperature
 ($T_j \leq 175^\circ\text{C}$)



T_C , CASE TEMPERATURE

Figure 4. Collector current as a function of case temperature
 ($V_{GE} \geq 15\text{V}$, $T_j \leq 175^\circ\text{C}$)

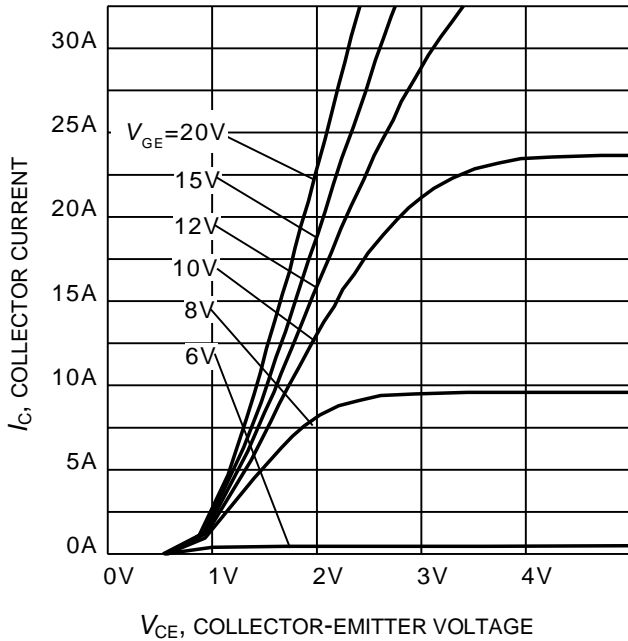


Figure 5. Typical output characteristic
($T_j = 25^\circ\text{C}$)

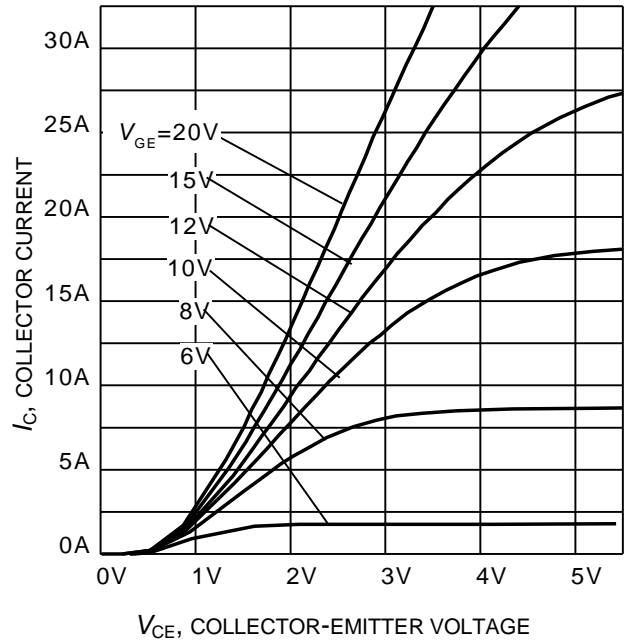


Figure 6. Typical output characteristic
($T_j = 175^\circ\text{C}$)

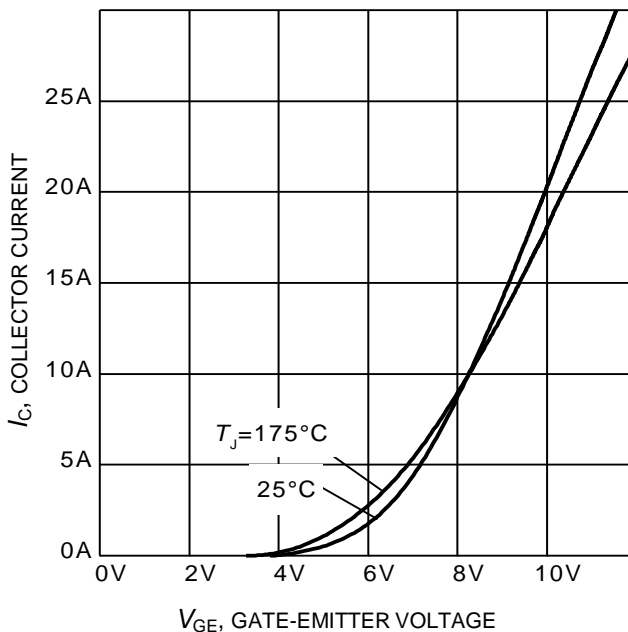


Figure 7. Typical transfer characteristic
($V_{CE} = 20\text{V}$)

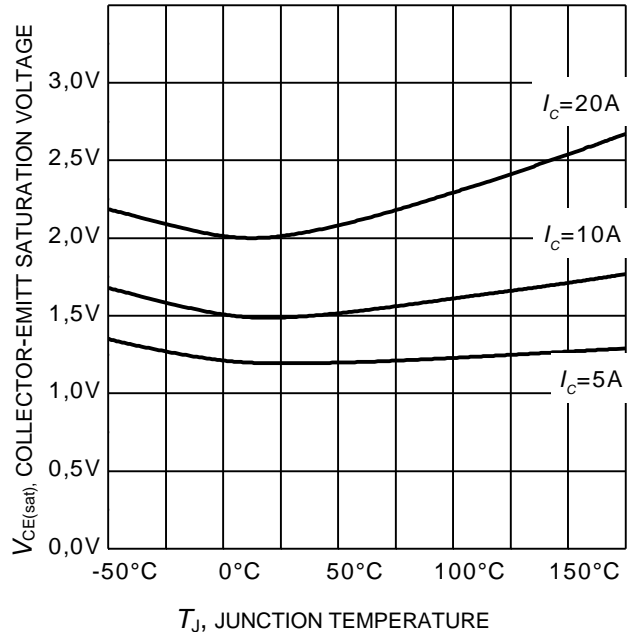
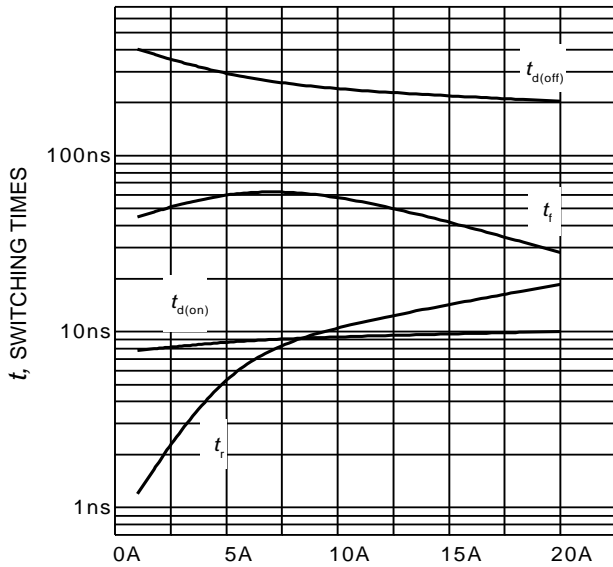
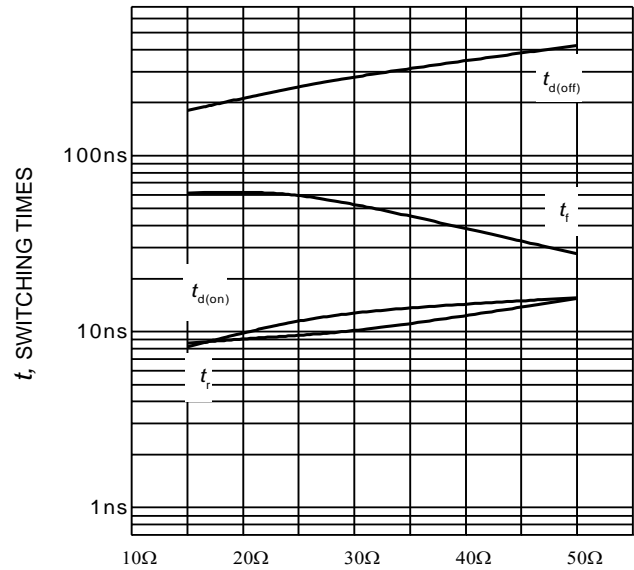


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)



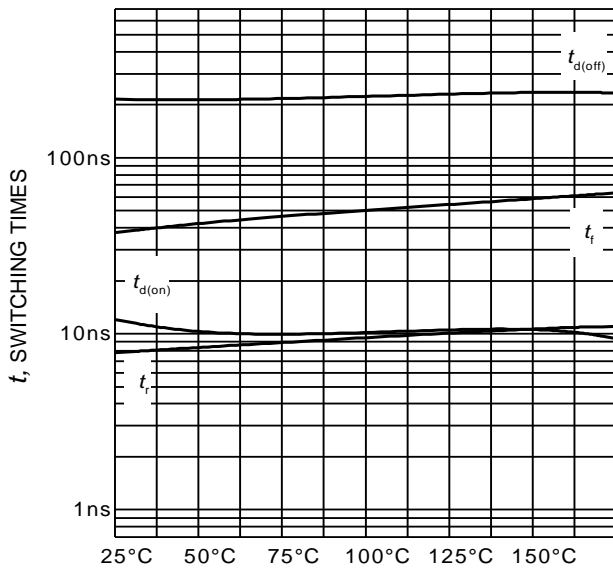
I_C , COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current
(inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $r_G = 23\Omega$,
Dynamic test circuit in Figure E)



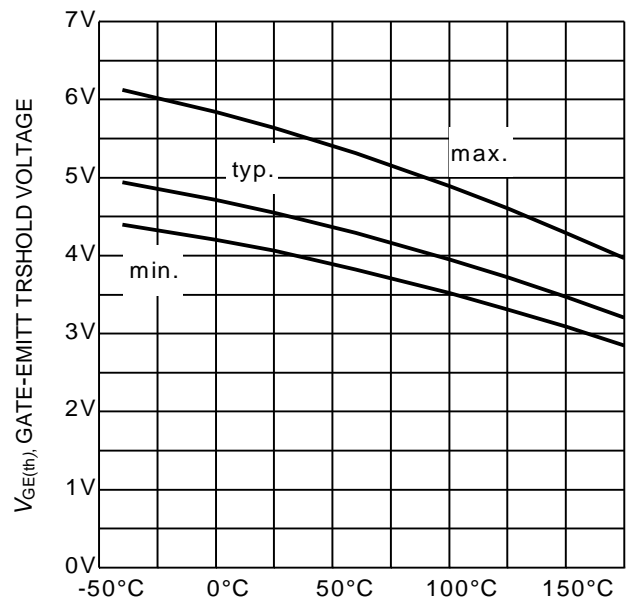
R_G , GATE RESISTOR

Figure 10. Typical switching times as a function of gate resistor
(inductive load, $T_J = 175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$,
Dynamic test circuit in Figure E)



T_J , JUNCTION TEMPERATURE

Figure 11. Typical switching times as a function of junction temperature
(inductive load, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, $r_G = 23\Omega$,
Dynamic test circuit in Figure E)



T_J , JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature
($I_C = 0.3\text{mA}$)

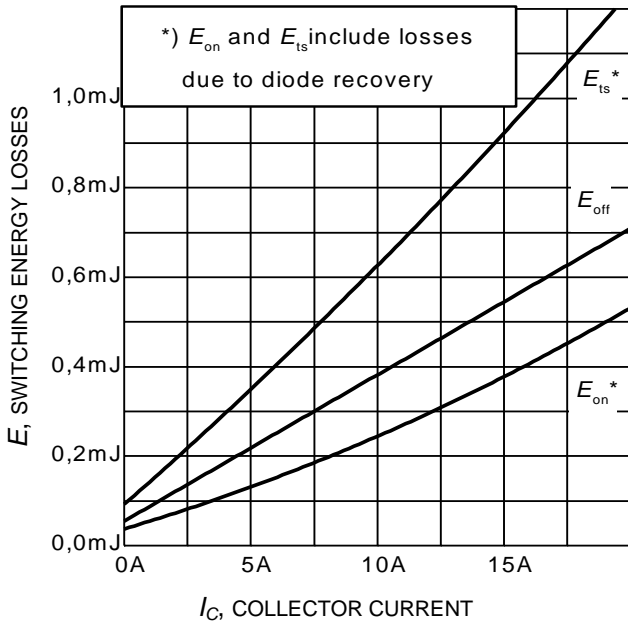


Figure 13. Typical switching energy losses as a function of collector current
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $r_G = 23\Omega$, Dynamic test circuit in Figure E)

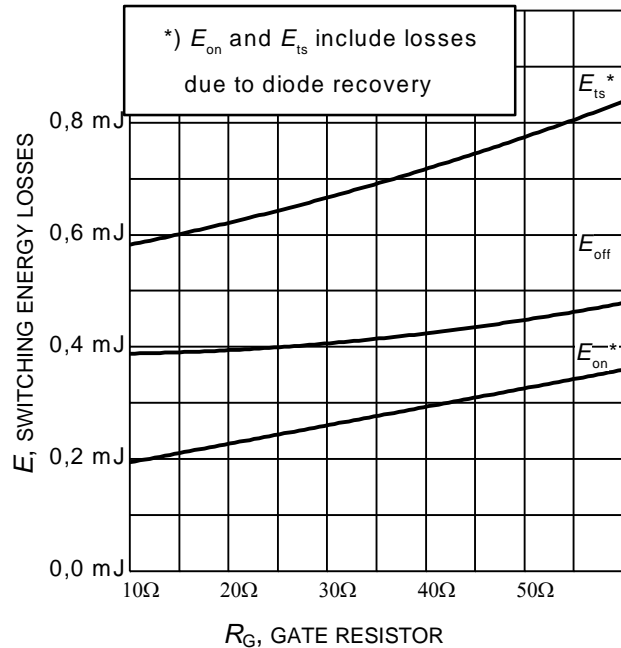


Figure 14. Typical switching energy losses as a function of gate resistor
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, Dynamic test circuit in Figure E)

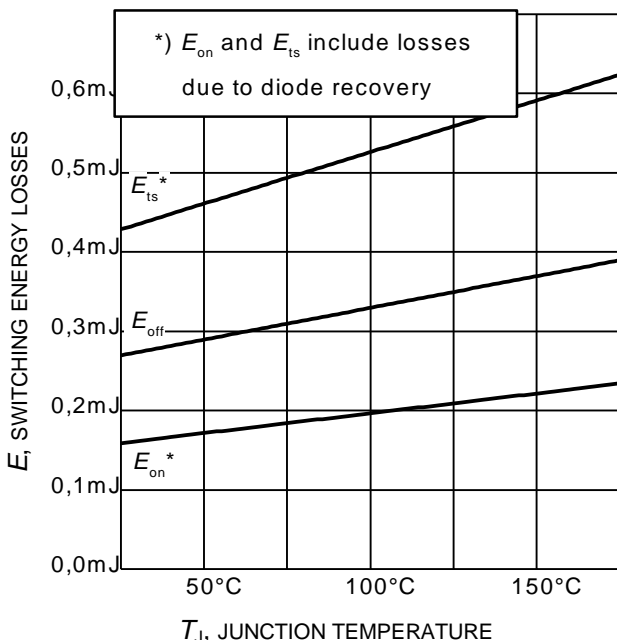


Figure 15. Typical switching energy losses as a function of junction temperature
 (inductive load, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, $r_G = 23\Omega$, Dynamic test circuit in Figure E)

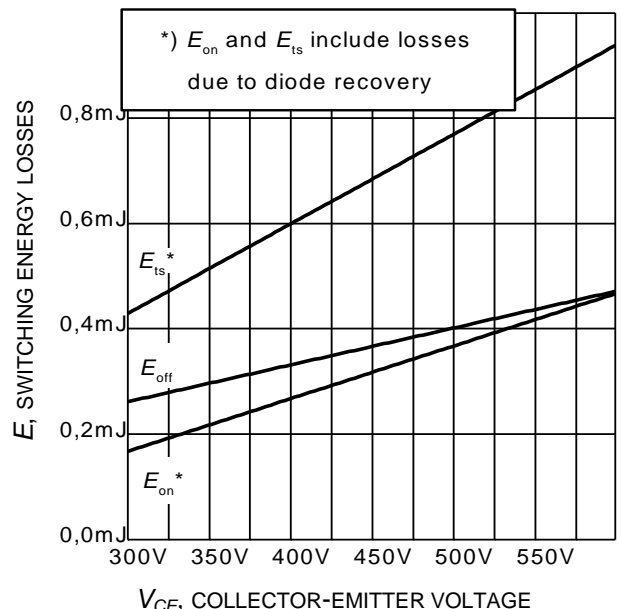


Figure 16. Typical switching energy losses as a function of collector emitter voltage
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, $r_G = 23\Omega$, Dynamic test circuit in Figure E)

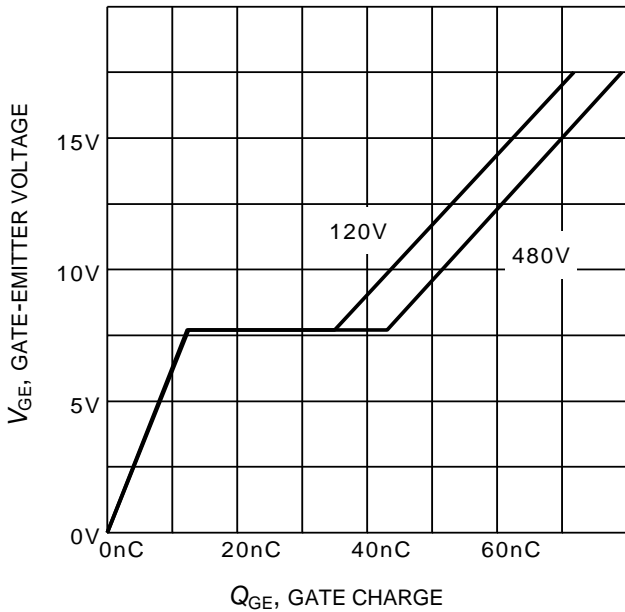


Figure 17. Typical gate charge
($I_C=10\text{ A}$)

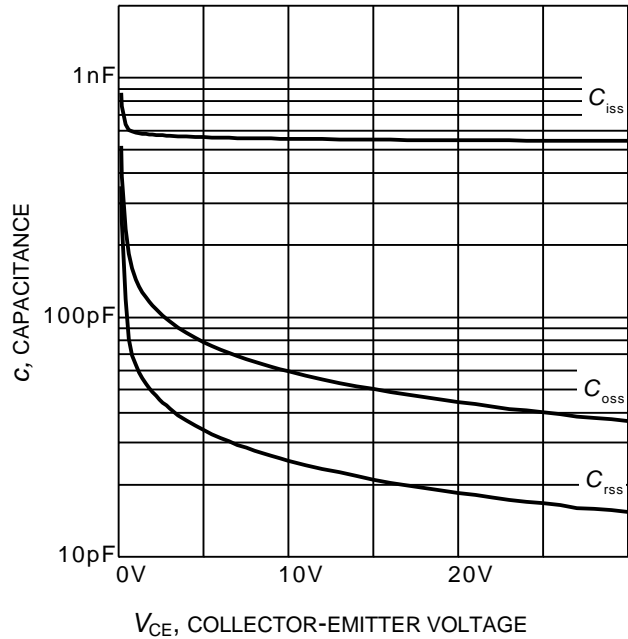


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE}=0\text{V}$, $f = 1\text{ MHz}$)

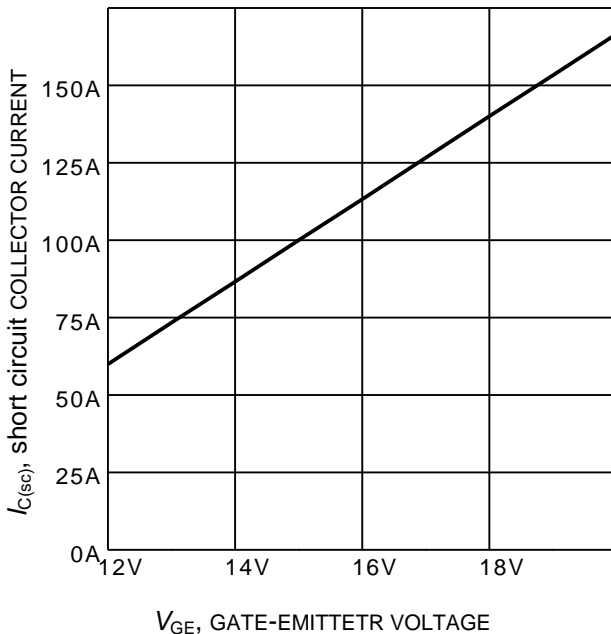


Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 400\text{V}$, $T_J \leq 150^\circ\text{C}$)

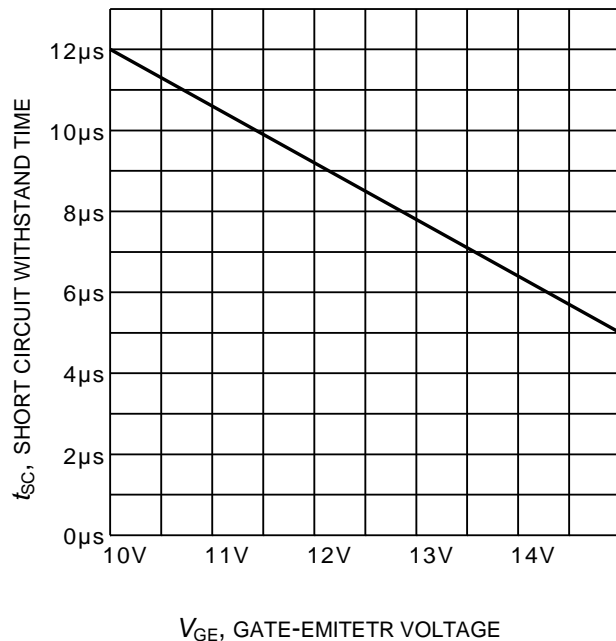


Figure 20. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE}=400\text{V}$, start at $T_J=25^\circ\text{C}$, $T_{Jmax}<150^\circ\text{C}$)

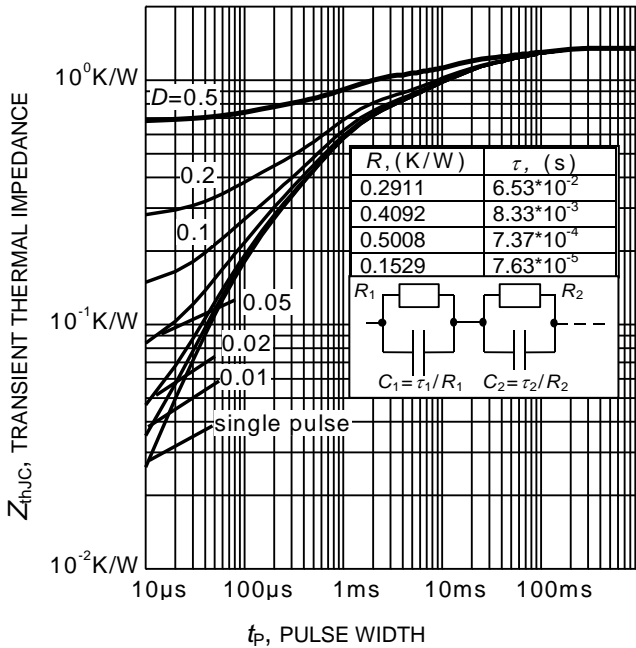


Figure 21. IGBT transient thermal impedance
($D = t_p / T$)

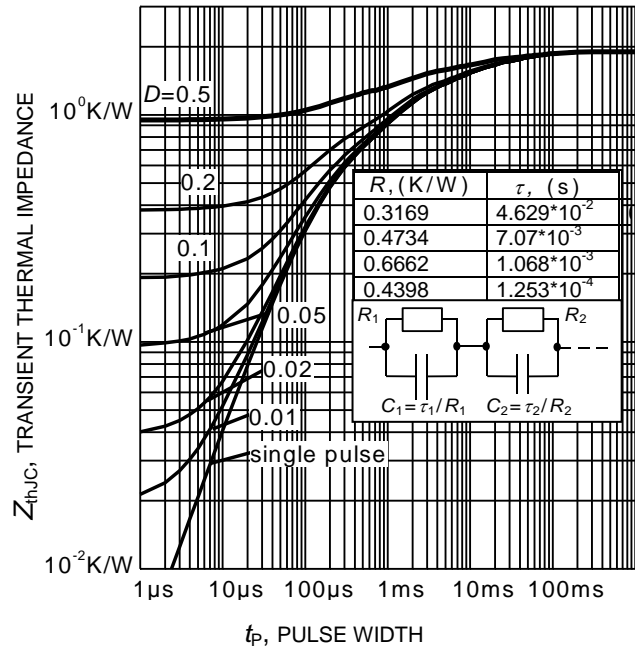


Figure 22. Diode transient thermal impedance as a function of pulse width
($D = t_p / T$)

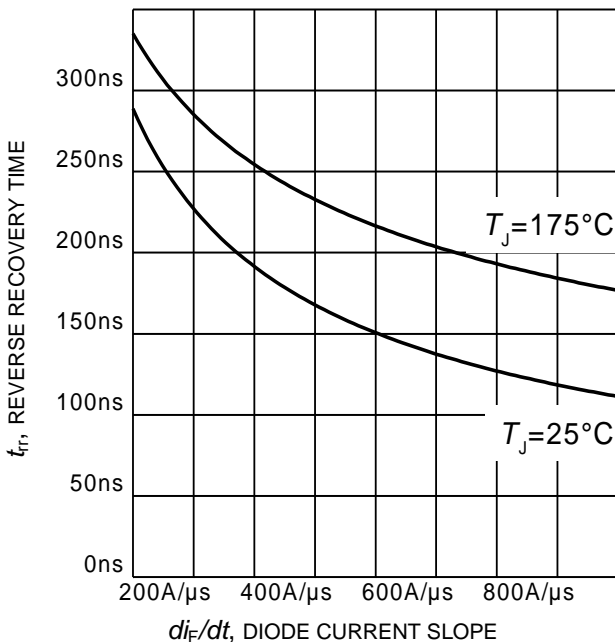


Figure 23. Typical reverse recovery time as a function of diode current slope
($V_R = 400V$, $I_F = 10A$,
Dynamic test circuit in Figure E)

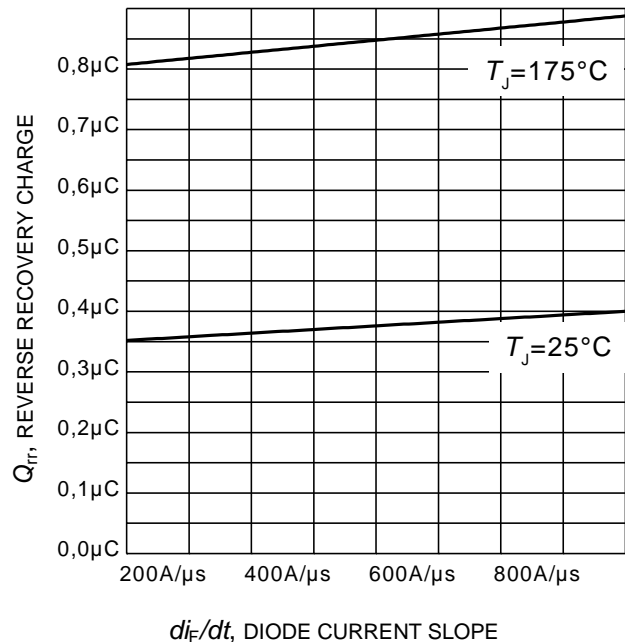
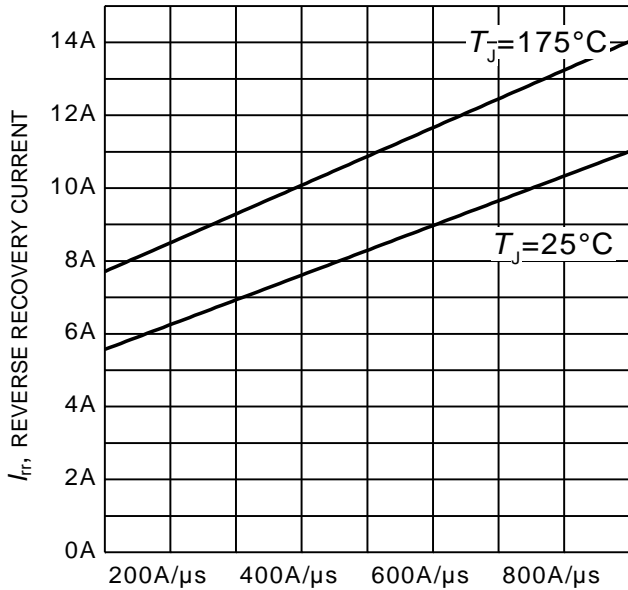


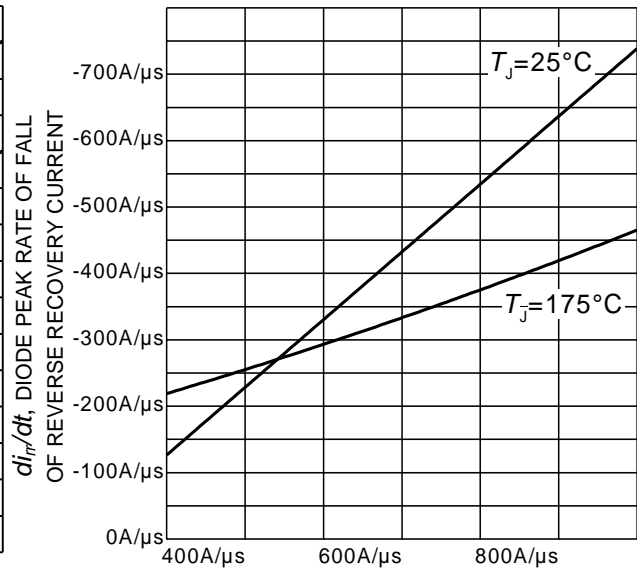
Figure 24. Typical reverse recovery charge as a function of diode current slope
($V_R = 400V$, $I_F = 10A$,
Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE

Figure 25. Typical reverse recovery current as a function of diode current slope

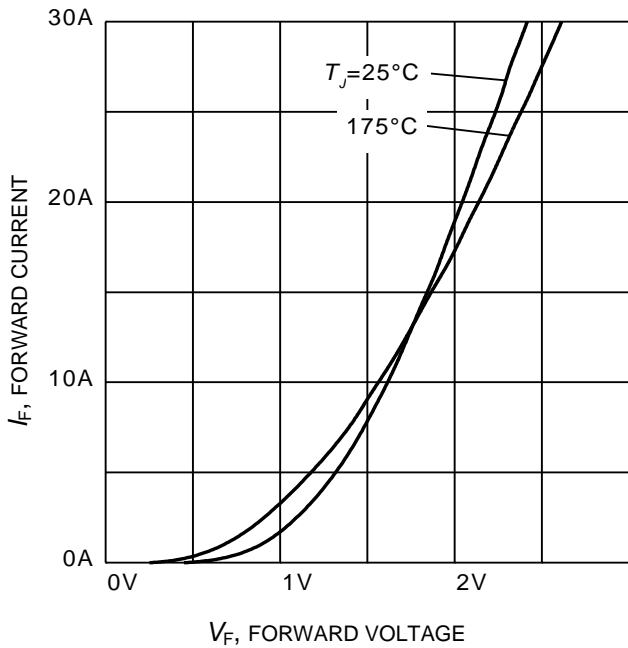
($V_R = 400V$, $I_F = 10A$,
Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE

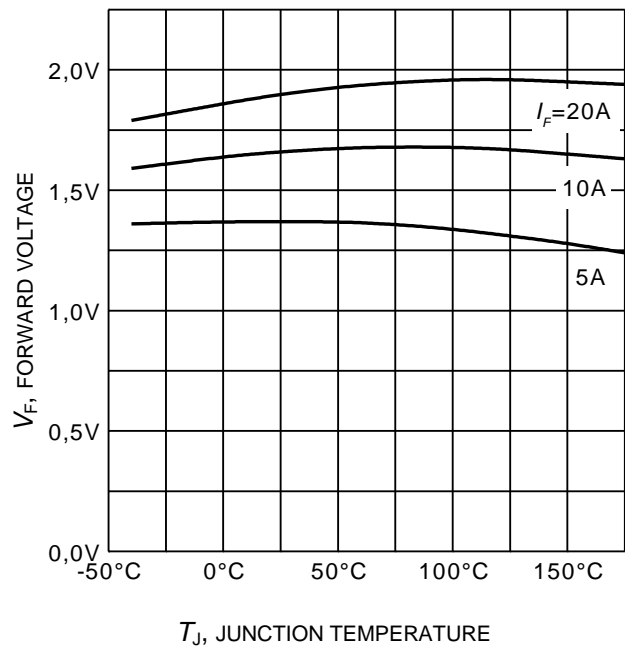
Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope

($V_R = 400V$, $I_F = 10A$,
Dynamic test circuit in Figure E)



V_F , FORWARD VOLTAGE

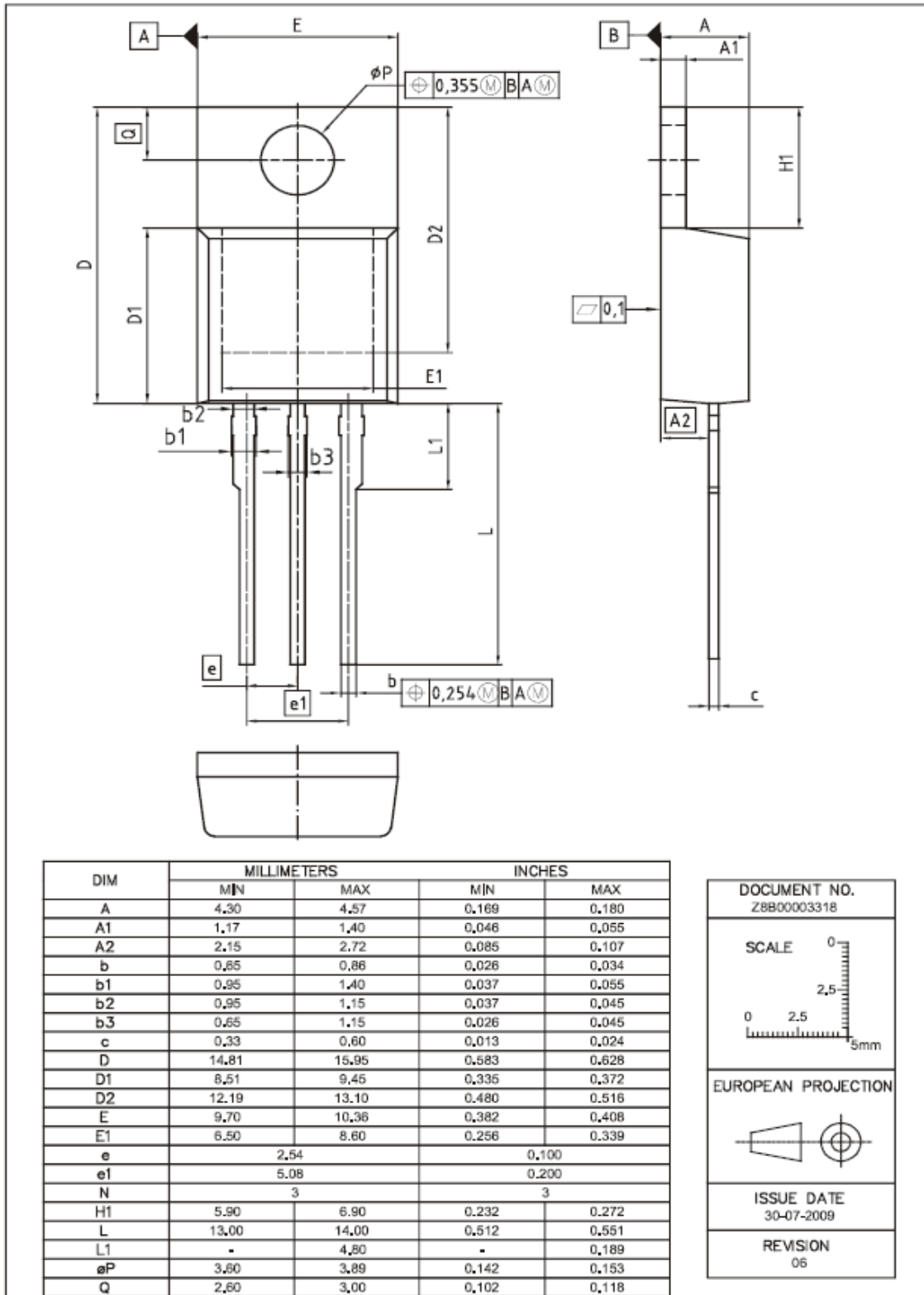
Figure 27. Typical diode forward current as a function of forward voltage



T_J , JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature

PG-TO220-3



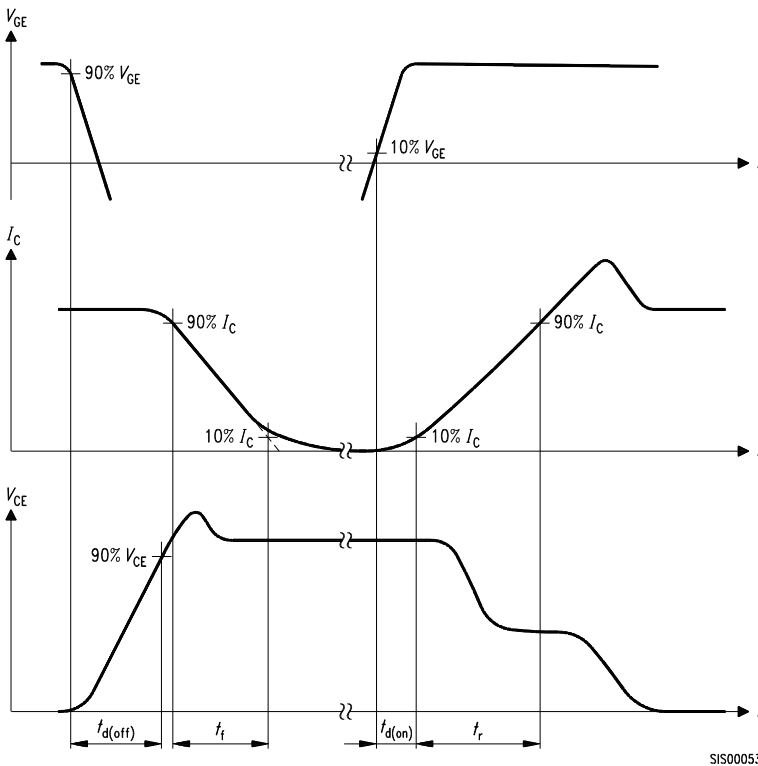


Figure A. Definition of switching times

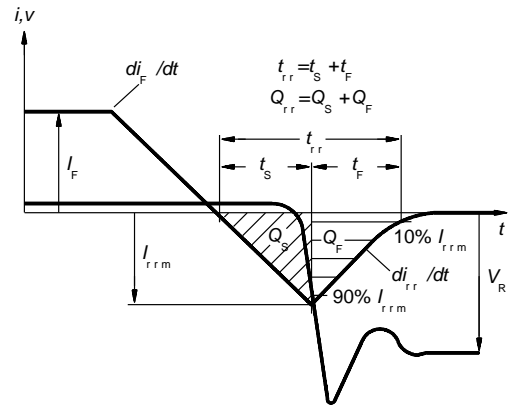


Figure C. Definition of diodes switching characteristics

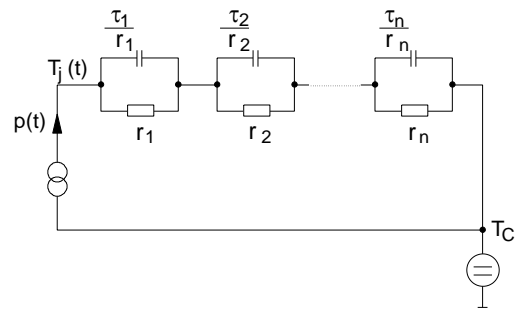


Figure D. Thermal equivalent circuit

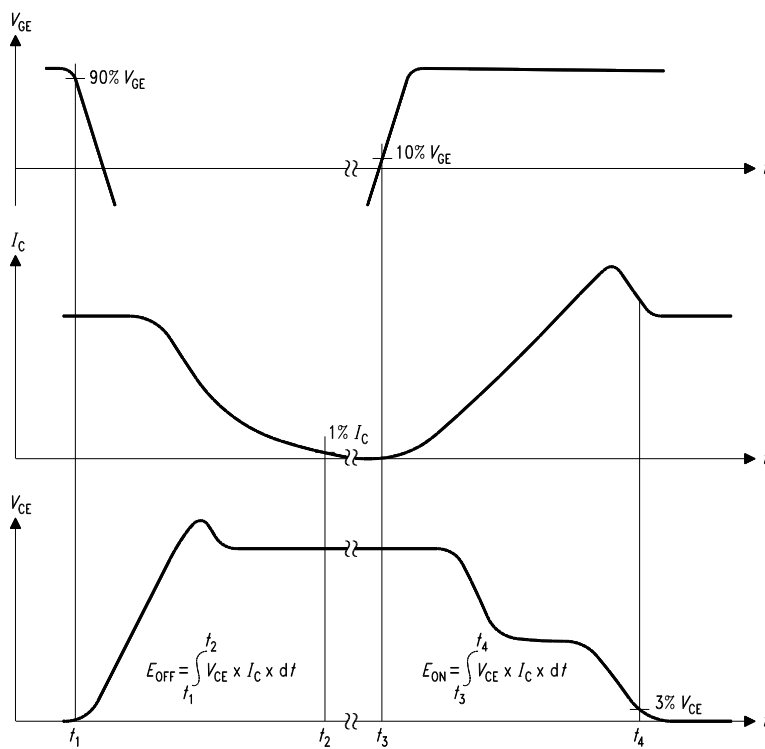


Figure B. Definition of switching losses

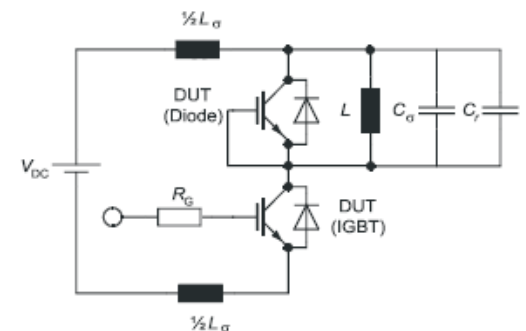


Figure E. Dynamic test circuit
Parasitic inductance L_{σ} ,
Parasitic capacitor C_{σ} ,
Relief capacitor C_r
(only for ZVT switching)

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[APT35GP120JDQ2](#) [XD15H120CX1](#) [XD25H120CX0](#) [XP15PJS120CL1B1](#) [IGW30N60H3FKSA1](#) [STGWA8M120DF3](#) [IGW08T120FKSA1](#)
[IGW75N60H3FKSA1](#) [FGH60N60SMD_F085](#) [FGH75T65UPD](#) [STGWA15H120F2](#) [IKA10N60TXKSA1](#) [IHW20N120R5XKSA1](#)
[RJH60D2DPP-M0#T2](#) [IKP20N60TXKSA1](#) [IHW20N65R5XKSA1](#) [APT70GR120JD60](#) [AOD5B60D](#) [APT70GR120L](#) [STGWT60H65FB](#)
[STGWT60H65DFB](#) [STGWT40V60DF](#) [STGWT20V60DF](#) [STGB10NB37LZT4](#)