

## IGBT

Low  $V_{CE(sat)}$  IGBT in TRENCHSTOP™ 5 technology copacked with RAPID 2 fast and soft antiparallel diode

## IKW30N65NL5

650V DuoPack IGBT and diode  
Low  $V_{CE(sat)}$  series fifth generation

Data sheet

Low  $V_{CE(sat)}$  series fifth generation

## Low $V_{CE(sat)}$ IGBT in TRENCHSTOP™ 5 technology copacked with RAPID 2 fast and soft antiparallel diode

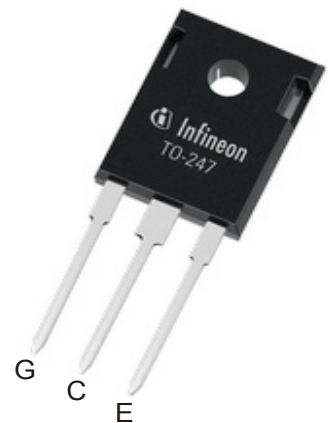
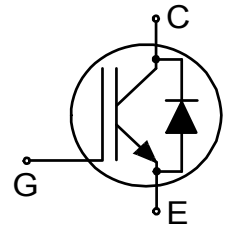
### Features and Benefits:

Low  $V_{CE(sat)}$  L5 technology offering

- Very low collector-emitter saturation voltage  $V_{CEsat}$
- Best-in-Class tradeoff between conduction and switching losses
- 650V breakdown voltage
- Low gate charge  $Q_G$
- Maximum junction temperature 175°C
- Qualified according to JEDEC for target applications
- Pb-free lead plating
- RoHS compliant
- Complete product spectrum and PSpice models:  
<http://www.infineon.com/igbt/>

### Applications:

- Uninterruptible power supplies
- Solar photovoltaic inverters
- Welding machines



### Key Performance and Package Parameters

Type	$V_{CE}$	$I_C$	$V_{CEsat}, T_{vj}=25^{\circ}C$	$T_{vjmax}$	Marking	Package
IKW30N65NL5	650V	30A	1.05V	175°C	K30ENL5	PG-TO247-3

**Table of Contents**

Description .....	2
Table of Contents .....	3
Maximum Ratings .....	4
Thermal Resistance .....	4
Electrical Characteristics .....	5
Electrical Characteristics Diagrams .....	7
Package Drawing .....	14
Testing Conditions .....	15
Revision History .....	16
Disclaimer .....	17

Low  $V_{CE(sat)}$  series fifth generation

## Maximum Ratings

For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25^{\circ}\text{C}$	$V_{CE}$	650	V
DC collector current, limited by $T_{vjmax}$ $T_c = 25^{\circ}\text{C}$ value limited by bondwire $T_c = 100^{\circ}\text{C}$	$I_C$	85.0 62.0	A
Pulsed collector current, $t_p$ limited by $T_{vjmax}^{1)}$	$I_{Cpuls}$	120.0	A
Turn off safe operating area $V_{CE} \leq 650\text{V}$ , $T_{vj} \leq 175^{\circ}\text{C}$ , $t_p = 1\mu\text{s}^{1)}$	-	120.0	A
Diode forward current, limited by $T_{vjmax}$ $T_c = 25^{\circ}\text{C}$ value limited by bondwire $T_c = 100^{\circ}\text{C}$	$I_F$	50.0 34.0	A
Diode pulsed current, $t_p$ limited by $T_{vjmax}^{1)}$	$I_{Fpuls}$	120.0	A
Gate-emitter voltage Transient Gate-emitter voltage ( $t_p \leq 10\mu\text{s}$ , $D < 0.010$ )	$V_{GE}$	$\pm 20$ $\pm 30$	V
Power dissipation $T_c = 25^{\circ}\text{C}$ Power dissipation $T_c = 100^{\circ}\text{C}$	$P_{tot}$	227.0 114.0	W
Operating junction temperature	$T_{vj}$	-40...+175	$^{\circ}\text{C}$
Storage temperature	$T_{stg}$	-55...+150	$^{\circ}\text{C}$
Soldering temperature, <sup>2)</sup> wave soldering 1.6mm (0.063in.) from case for 10s		260	$^{\circ}\text{C}$
Mounting torque, M3 screw Maximum of mounting processes: 3	$M$	0.6	Nm

## Thermal Resistance

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b><math>R_{th}</math> Characteristics</b>						
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		-	-	0.66	K/W
Diode thermal resistance, junction - case	$R_{th(j-c)}$		-	-	0.95	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		-	-	40	K/W

<sup>1)</sup> Defined by design. Not subject to production test.

<sup>2)</sup> Package not recommended for surface mount applications.

Low  $V_{CE(sat)}$  series fifth generation**Electrical Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0\text{V}, I_C = 0.20\text{mA}$	650	-	-	V
Collector-emitter saturation voltage	$V_{CEsat}$	$V_{GE} = 15.0\text{V}, I_C = 30.0\text{A}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 100^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$	-	1.05	1.35	V
			-	1.05	-	
			-	1.04	-	
Diode forward voltage	$V_F$	$V_{GE} = 0\text{V}, I_F = 30.0\text{A}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 100^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$	-	1.65	2.20	V
			-	1.70	-	
			-	1.68	-	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 0.40\text{mA}, V_{CE} = 20\text{V}$	4.2	5.0	5.8	V
Zero gate voltage collector current	$I_{CES}$	$V_{CE} = 650\text{V}, V_{GE} = 0\text{V}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	-	-	40	$\mu\text{A}$
			-	400	-	
			-	2000	-	
Gate-emitter leakage current	$I_{GES}$	$V_{CE} = 0\text{V}, V_{GE} = 20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE} = 20\text{V}, I_C = 30.0\text{A}$	-	65.0	-	S

**Electrical Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Dynamic Characteristic</b>						
Input capacitance	$C_{ies}$	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}$ $f = 1000\text{kHz}$	-	4600	-	pF
Output capacitance	$C_{oes}$		-	64	-	
Reverse transfer capacitance	$C_{res}$		-	18	-	
Gate charge	$Q_G$	$V_{CC} = 520\text{V}, I_C = 30.0\text{A},$ $V_{GE} = 15\text{V}$	-	168.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	13.0	-	nH

**Switching Characteristic, Inductive Load**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic, at <math>T_{vj} = 25^{\circ}\text{C}</math></b>						
Turn-on delay time	$t_{d(on)}$	$T_{vj} = 25^{\circ}\text{C},$ $V_{CC} = 400\text{V}, I_C = 30.0\text{A},$ $V_{GE} = 0.0/15.0\text{V},$ $R_{G(on)} = 23.0\Omega, R_{G(off)} = 10.0\Omega,$ $L\sigma = 60\text{nH}, C\sigma = 30\text{pF}$ $L\sigma, C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	59	-	ns
Rise time	$t_r$		-	20	-	ns
Turn-off delay time	$t_{d(off)}$		-	283	-	ns
Fall time	$t_f$		-	67	-	ns
Turn-on energy	$E_{on}$		-	0.56	-	mJ
Turn-off energy	$E_{off}$		-	1.35	-	mJ
Total switching energy	$E_{ts}$		-	1.91	-	mJ

Low  $V_{CE(sat)}$  series fifth generationDiode Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ 

Diode reverse recovery time	$t_{rr}$	$T_{vj} = 25^{\circ}\text{C}$ , $V_R = 400\text{V}$ , $I_F = 30.0\text{A}$ , $di_F/dt = 1500\text{A}/\mu\text{s}$	-	59	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	0.48	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	18.0	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	-2800	-	$\text{A}/\mu\text{s}$

## Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

IGBT Characteristic, at  $T_{vj} = 150^{\circ}\text{C}$ 

Turn-on delay time	$t_{d(on)}$	$T_{vj} = 150^{\circ}\text{C}$ , $V_{CC} = 400\text{V}$ , $I_C = 30.0\text{A}$ , $V_{GE} = 0.0/15.0\text{V}$ , $R_{G(on)} = 23.0\Omega$ , $R_{G(off)} = 10.0\Omega$ , $L\sigma = 60\text{nH}$ , $C\sigma = 30\text{pF}$ $L\sigma$ , $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	54	-	ns
Rise time	$t_r$		-	22	-	ns
Turn-off delay time	$t_{d(off)}$		-	345	-	ns
Fall time	$t_f$		-	169	-	ns
Turn-on energy	$E_{on}$		-	0.69	-	mJ
Turn-off energy	$E_{off}$		-	2.18	-	mJ
Total switching energy	$E_{ts}$		-	2.87	-	mJ

Diode Characteristic, at  $T_{vj} = 150^{\circ}\text{C}$ 

Diode reverse recovery time	$t_{rr}$	$T_{vj} = 150^{\circ}\text{C}$ , $V_R = 400\text{V}$ , $I_F = 30.0\text{A}$ , $di_F/dt = 1500\text{A}/\mu\text{s}$	-	58	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	0.65	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	24.0	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	-4970	-	$\text{A}/\mu\text{s}$

Low  $V_{CE(sat)}$  series fifth generation

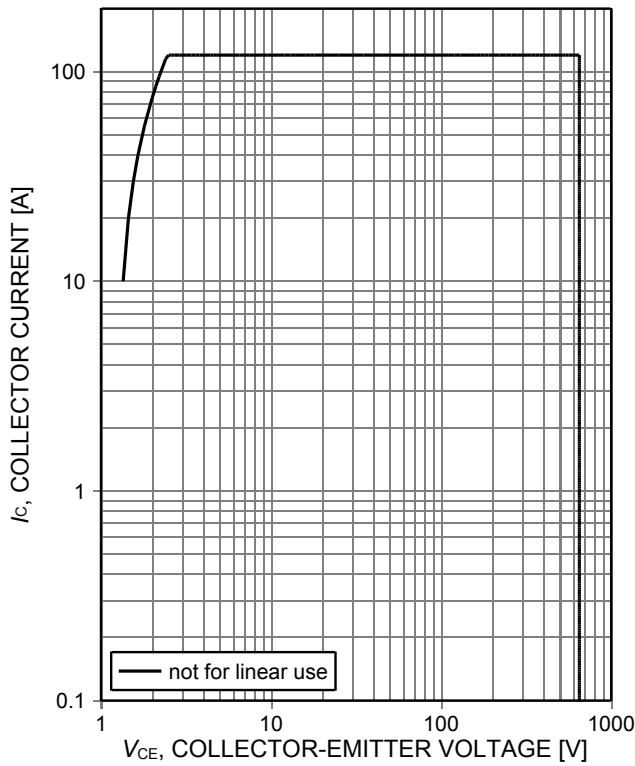


Figure 1. **Forward bias safe operating area**  
 ( $D=0$ ,  $T_C=25^\circ\text{C}$ ,  $T_{vj}\leq 175^\circ\text{C}$ ,  $V_{GE}=15\text{V}$ ,  $t_p=1\mu\text{s}$ ,  
 $I_{Cmax}$  defined by design - not subject to production test)

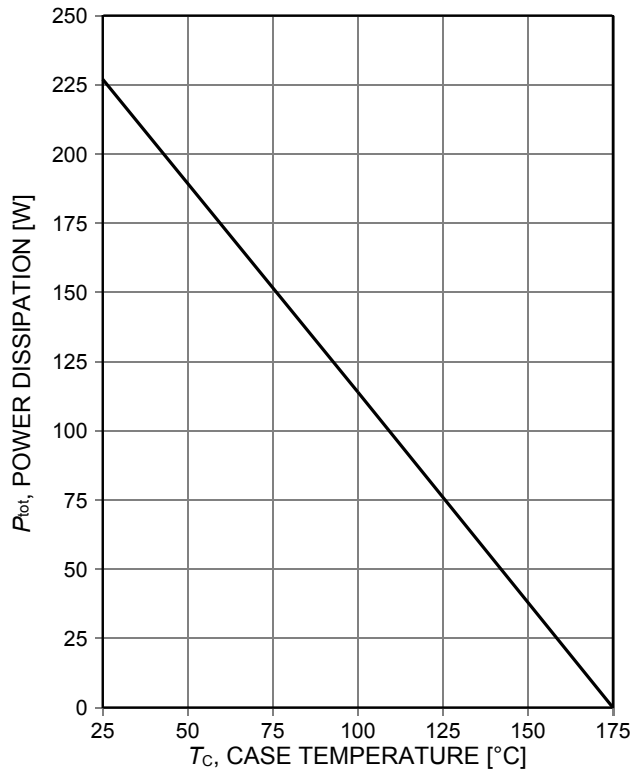


Figure 2. **Power dissipation as a function of case temperature**  
 ( $T_{vj}\leq 175^\circ\text{C}$ )

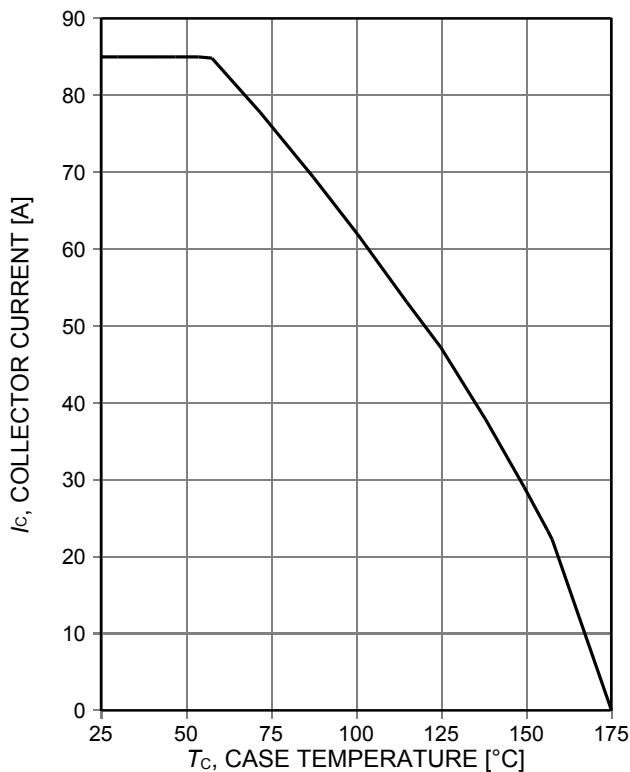


Figure 3. **Collector current as a function of case temperature**  
 ( $V_{GE}\geq 15\text{V}$ ,  $T_{vj}\leq 175^\circ\text{C}$ )

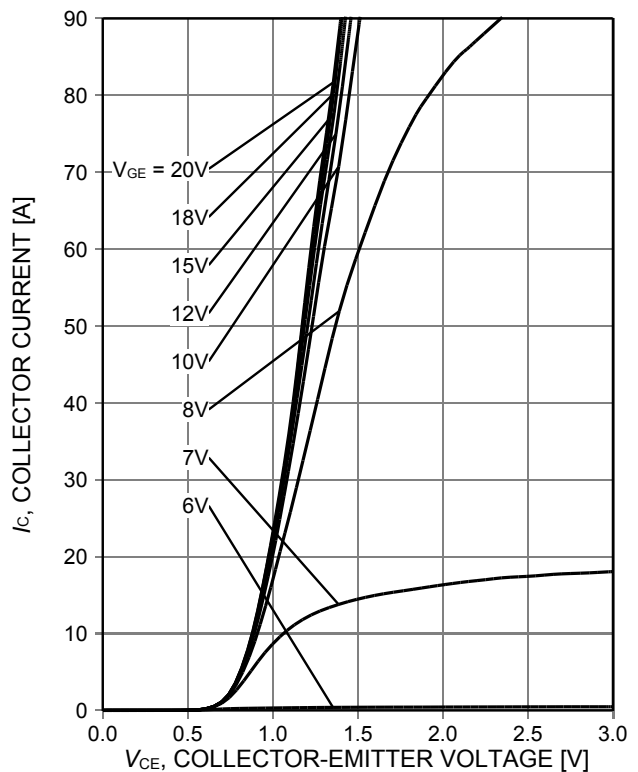


Figure 4. **Typical output characteristic**  
 ( $T_{vj}=25^\circ\text{C}$ )

Low  $V_{CE(sat)}$  series fifth generation

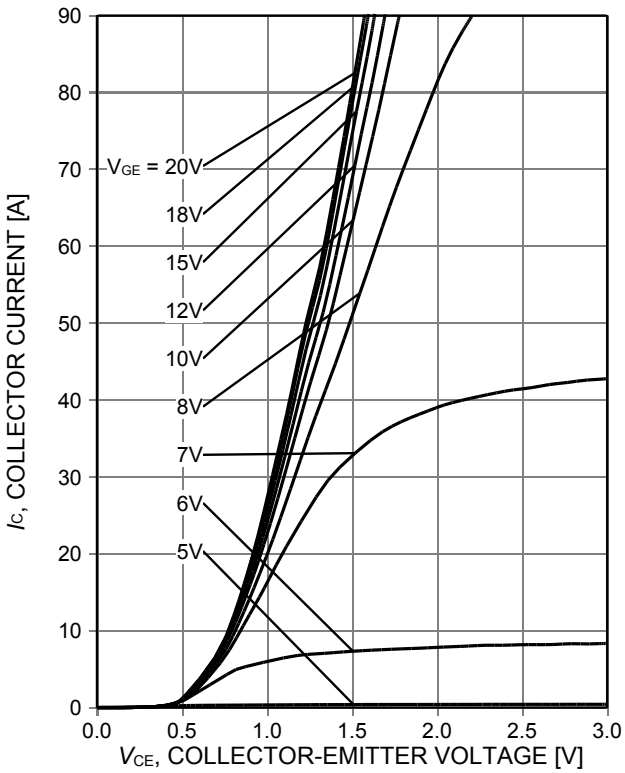


Figure 5. Typical output characteristic ( $T_{vj}=175^\circ\text{C}$ )

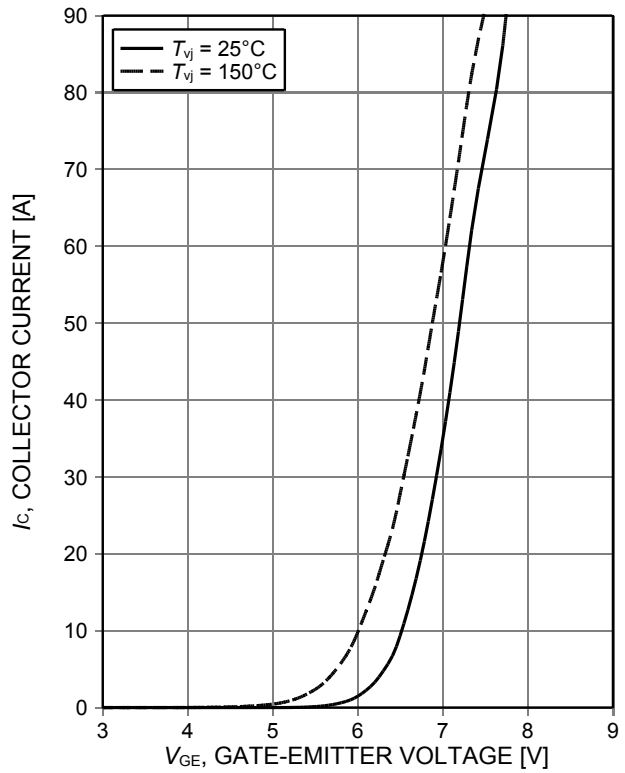


Figure 6. Typical transfer characteristic ( $V_{CE}=20\text{V}$ )

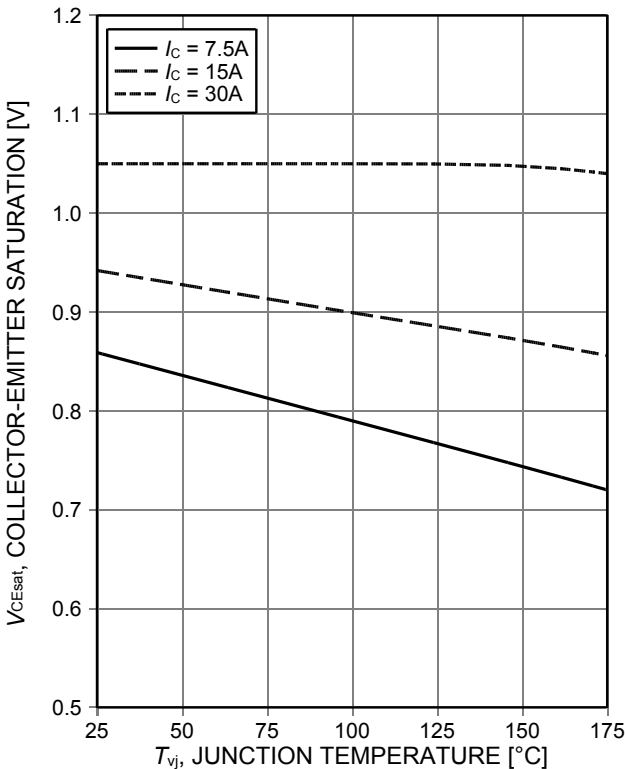


Figure 7. Typical collector-emitter saturation voltage as a function of junction temperature ( $V_{GE}=15\text{V}$ )

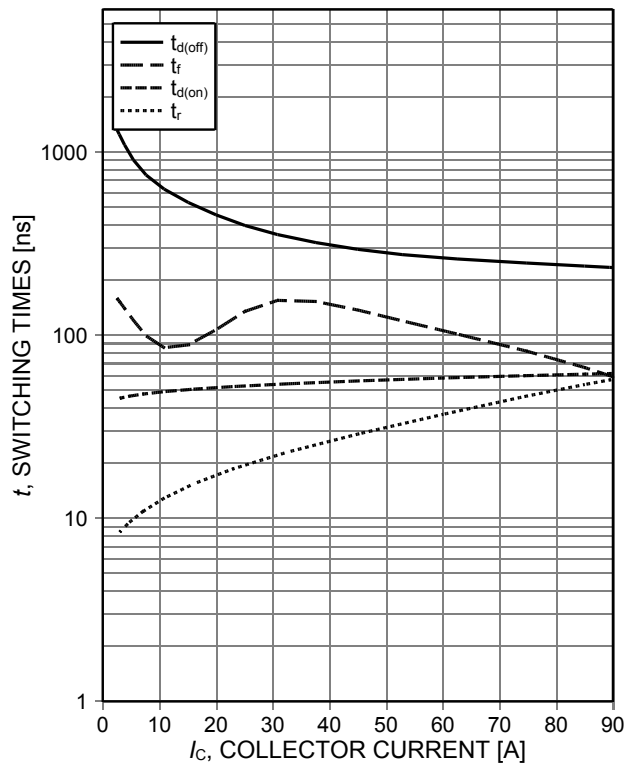


Figure 8. Typical switching times as a function of collector current (inductive load,  $T_{vj}=150^\circ\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $R_{G(on)}=23\Omega$ ,  $R_{G(off)}=10\Omega$ , dynamic test circuit in Figure E)



Low  $V_{CE(sat)}$  series fifth generation

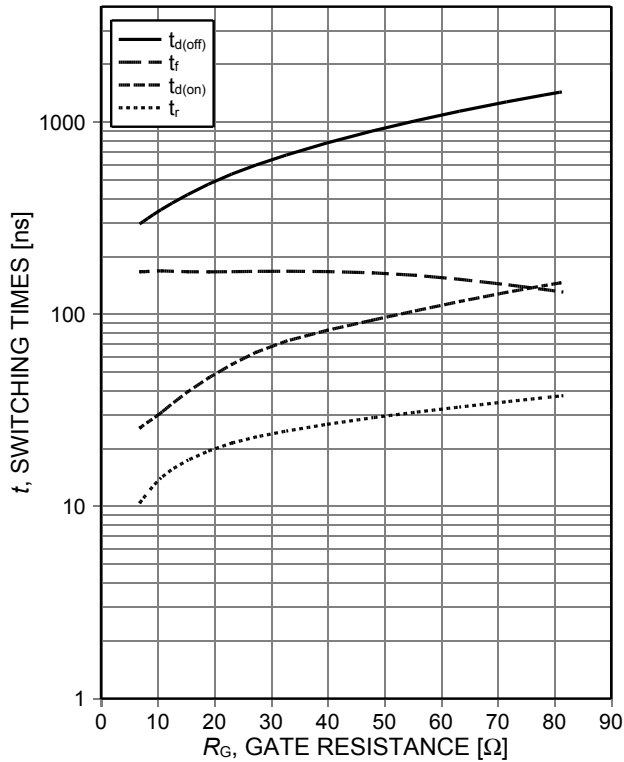


Figure 9. **Typical switching times as a function of gate resistance**  
 (inductive load,  $T_{vj}=150^{\circ}\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=30\text{A}$ , dynamic test circuit in Figure E)

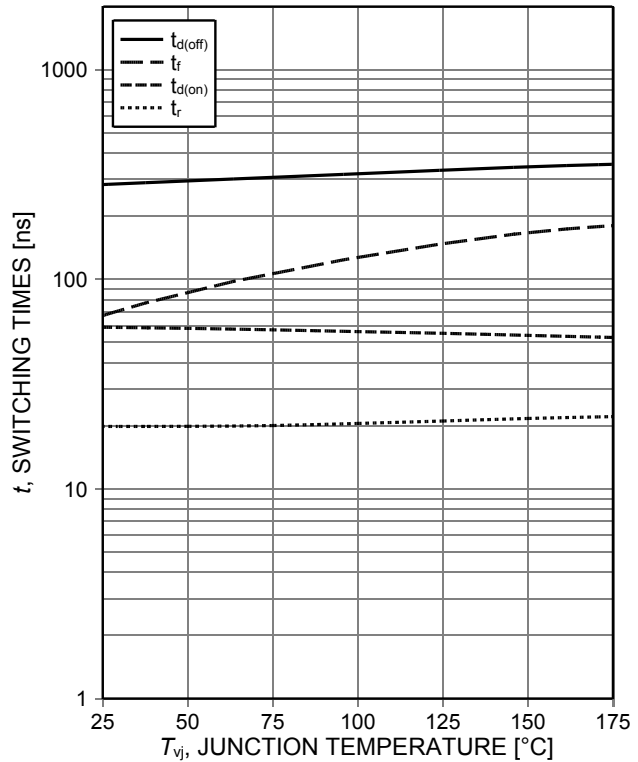


Figure 10. **Typical switching times as a function of junction temperature**  
 (inductive load,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=30\text{A}$ ,  $R_{G(on)}=23\Omega$ ,  $R_{G(off)}=10\Omega$ , dynamic test circuit in Figure E)

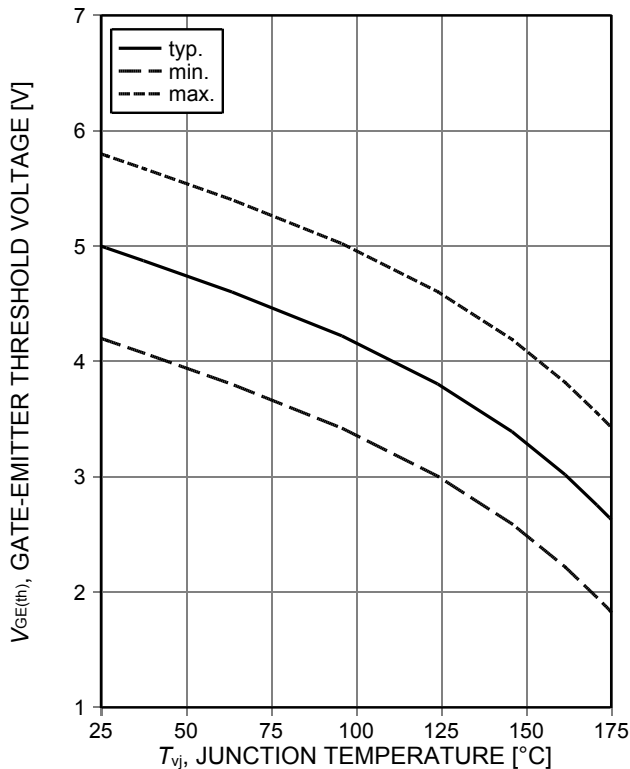


Figure 11. **Gate-emitter threshold voltage as a function of junction temperature**  
 ( $I_C=0.4\text{mA}$ )

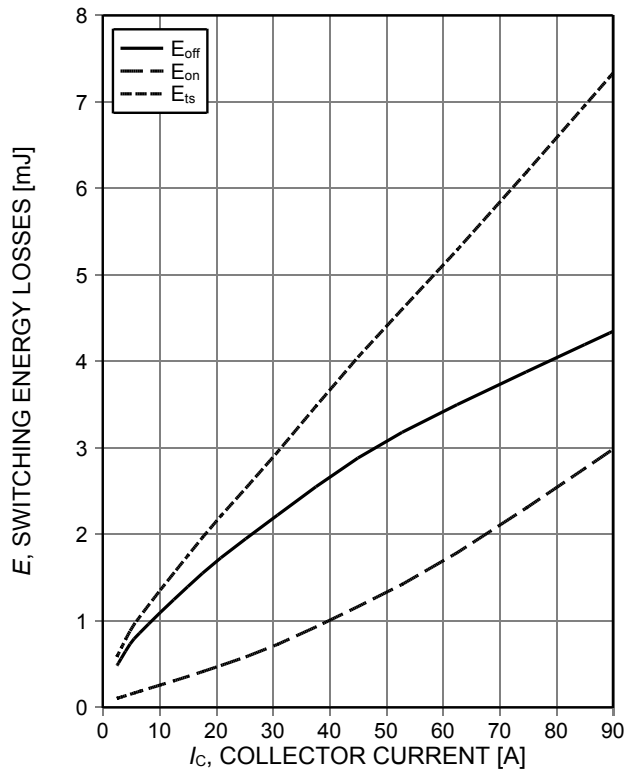


Figure 12. **Typical switching energy losses as a function of collector current**  
 (inductive load,  $T_{vj}=150^{\circ}\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $R_{G(on)}=23\Omega$ ,  $R_{G(off)}=10\Omega$ , dynamic test circuit in Figure E)

Low  $V_{CE(sat)}$  series fifth generation

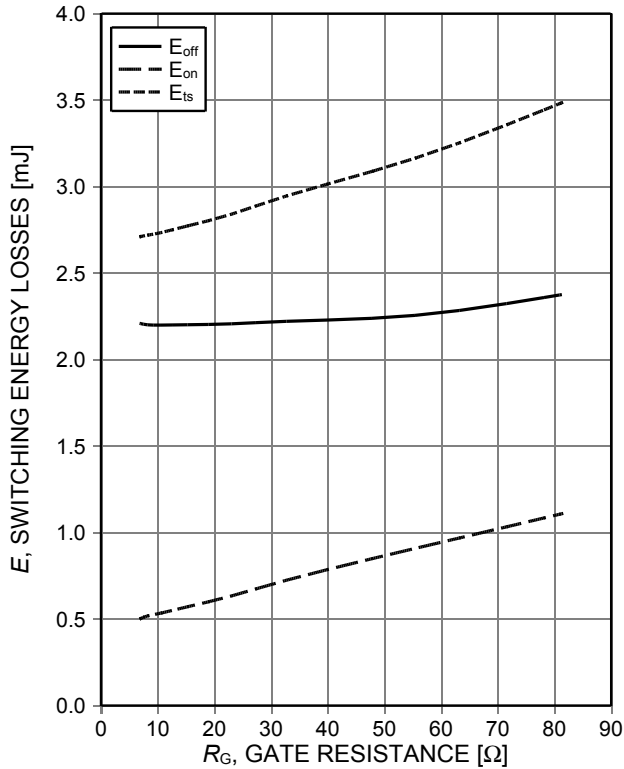


Figure 13. **Typical switching energy losses as a function of gate resistance**  
 (inductive load,  $T_{vj}=150^{\circ}C$ ,  $V_{CE}=400V$ ,  $V_{GE}=0/15V$ ,  $I_C=30A$ , dynamic test circuit in Figure E)

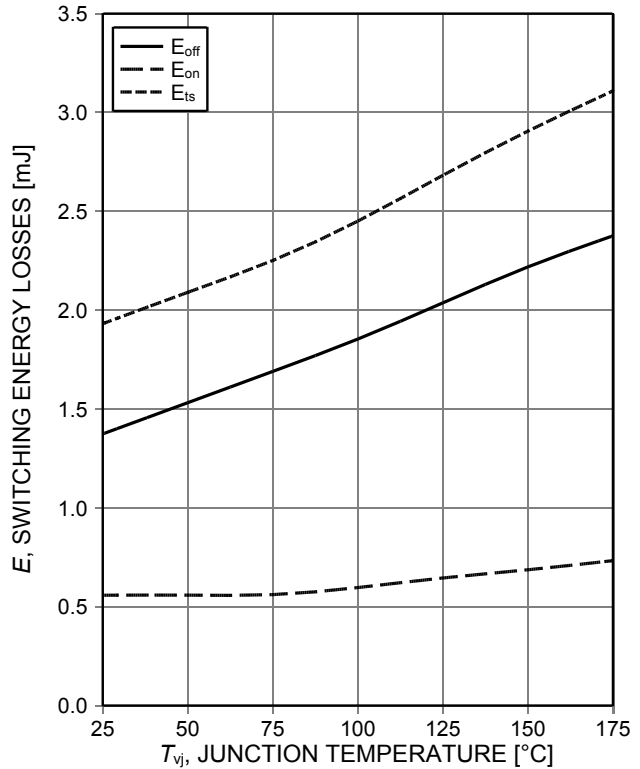


Figure 14. **Typical switching energy losses as a function of junction temperature**  
 (inductive load,  $V_{CE}=400V$ ,  $V_{GE}=0/15V$ ,  $I_C=30A$ ,  $R_{G(on)}=23\Omega$ ,  $R_{G(off)}=10\Omega$ , dynamic test circuit in Figure E)

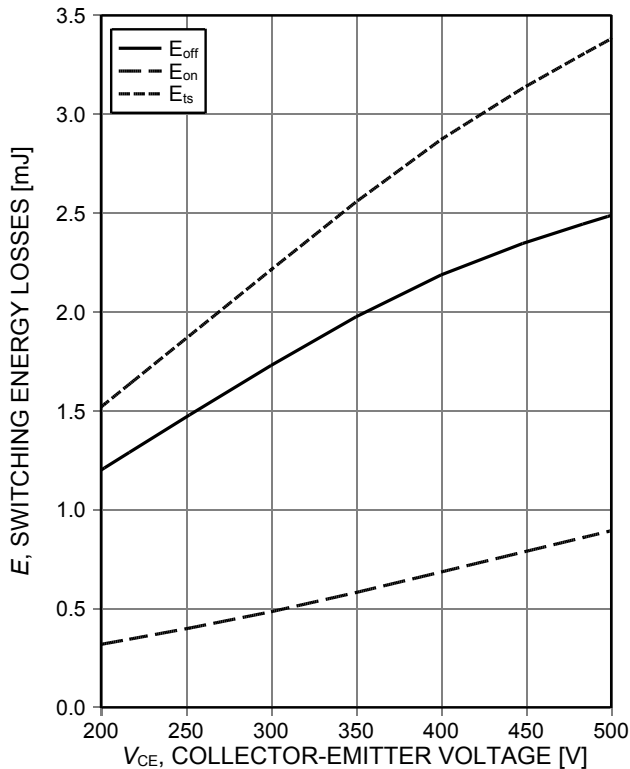


Figure 15. **Typical switching energy losses as a function of collector emitter voltage**  
 (inductive load,  $T_{vj}=150^{\circ}C$ ,  $V_{GE}=0/15V$ ,  $I_C=30A$ ,  $R_{G(on)}=23\Omega$ ,  $R_{G(off)}=10\Omega$ , dynamic test circuit in Figure E)

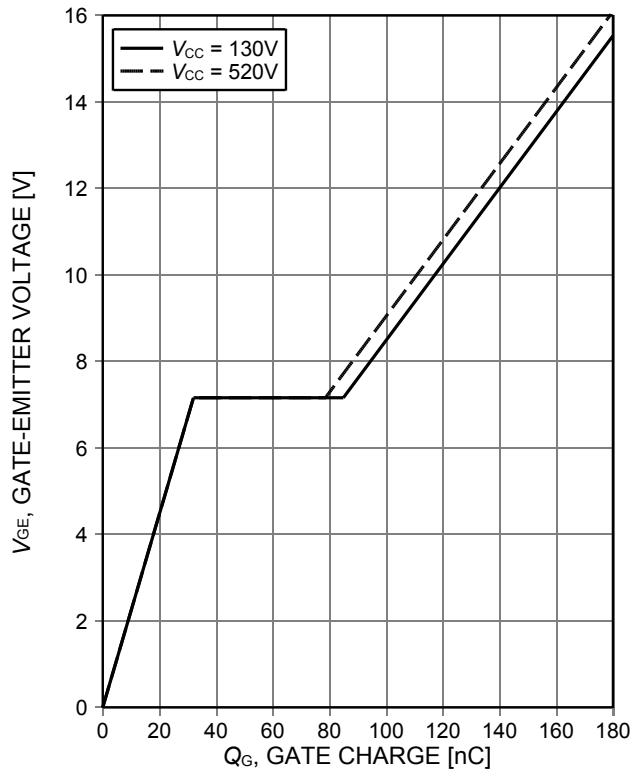


Figure 16. **Typical gate charge**  
 ( $I_C=30A$ )

Low  $V_{CE(sat)}$  series fifth generation

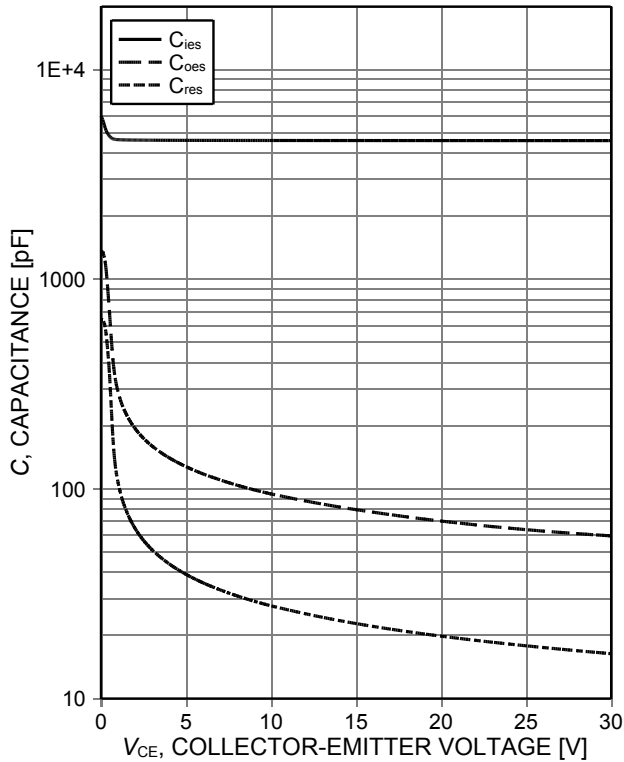


Figure 17. Typical capacitance as a function of collector-emitter voltage ( $V_{GE}=0V$ ,  $f=1MHz$ )

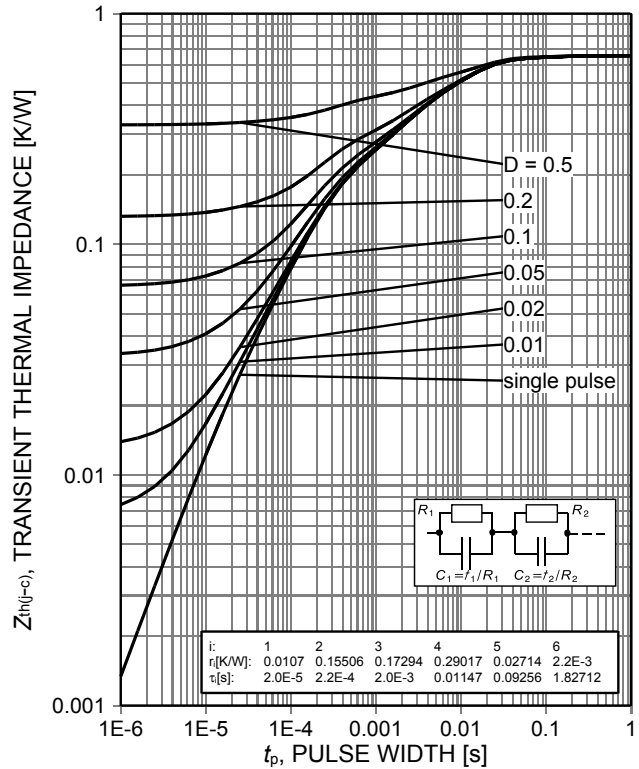


Figure 18. IGBT transient thermal impedance ( $D=t_p/T$ )

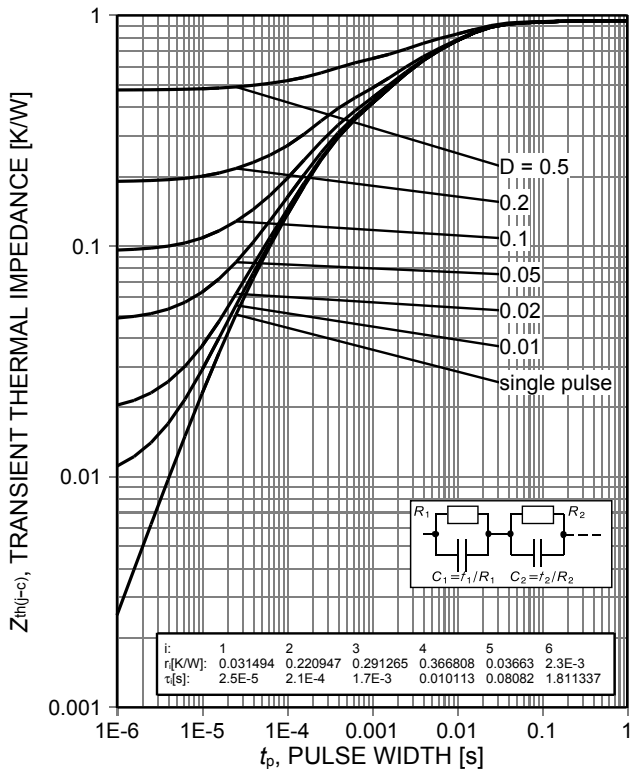


Figure 19. Diode transient thermal impedance as a function of pulse width ( $D=t_p/T$ )

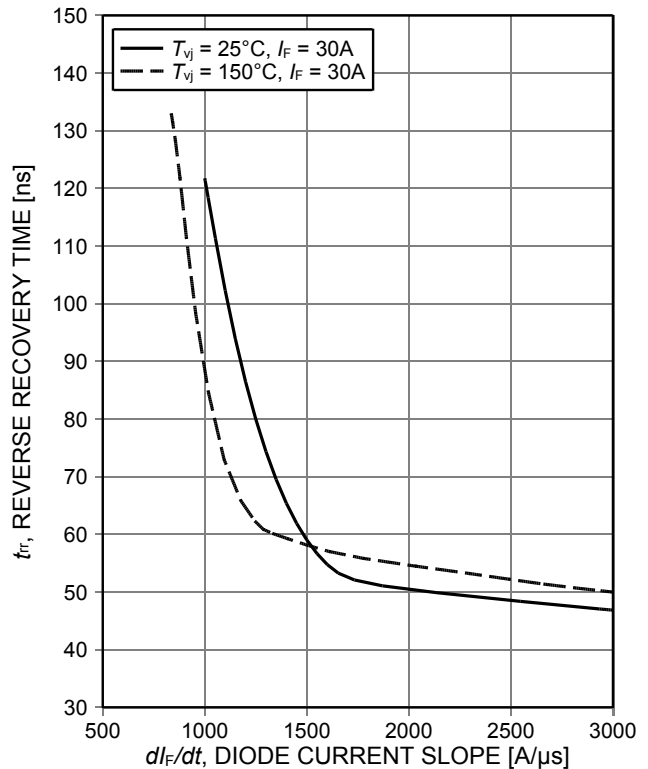


Figure 20. Typical reverse recovery time as a function of diode current slope ( $V_R=400V$ )

Low  $V_{CE(sat)}$  series fifth generation

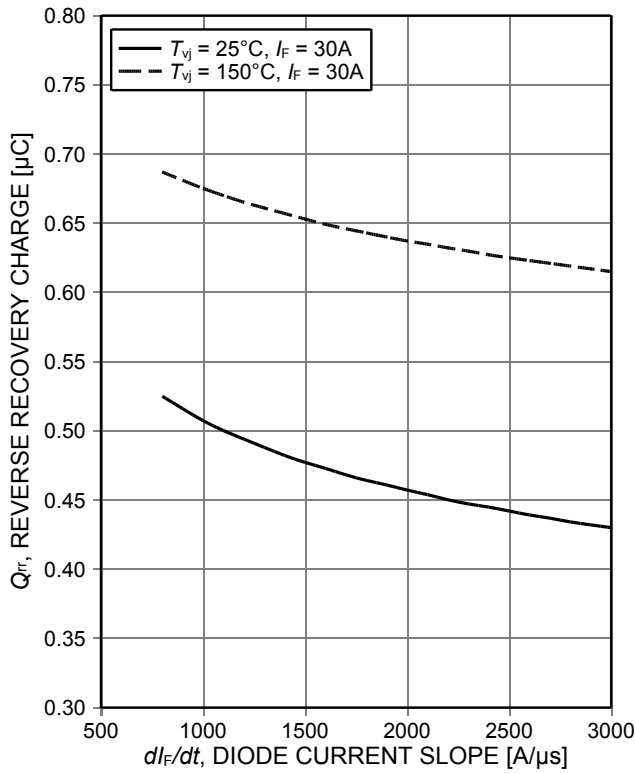


Figure 21. Typical reverse recovery charge as a function of diode current slope ( $V_R=400V$ )

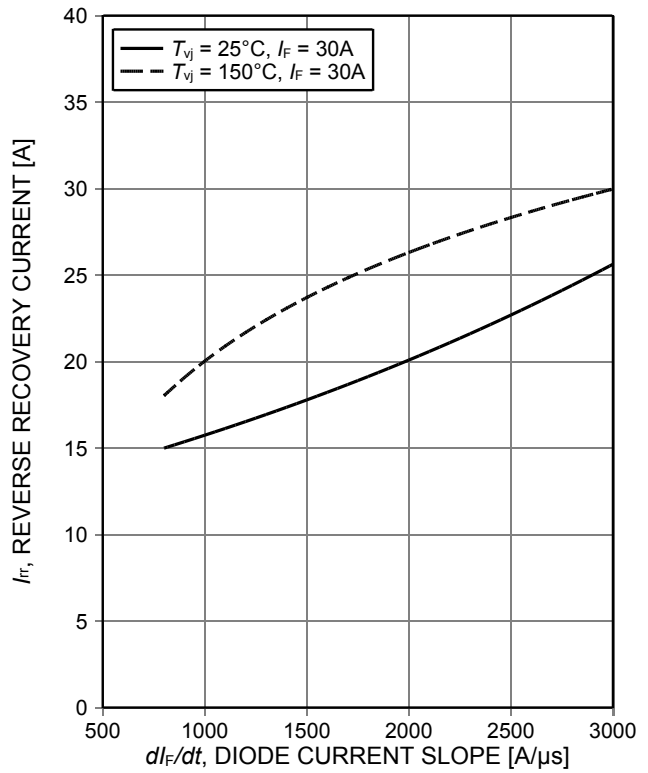


Figure 22. Typical reverse recovery current as a function of diode current slope ( $V_R=400V$ )

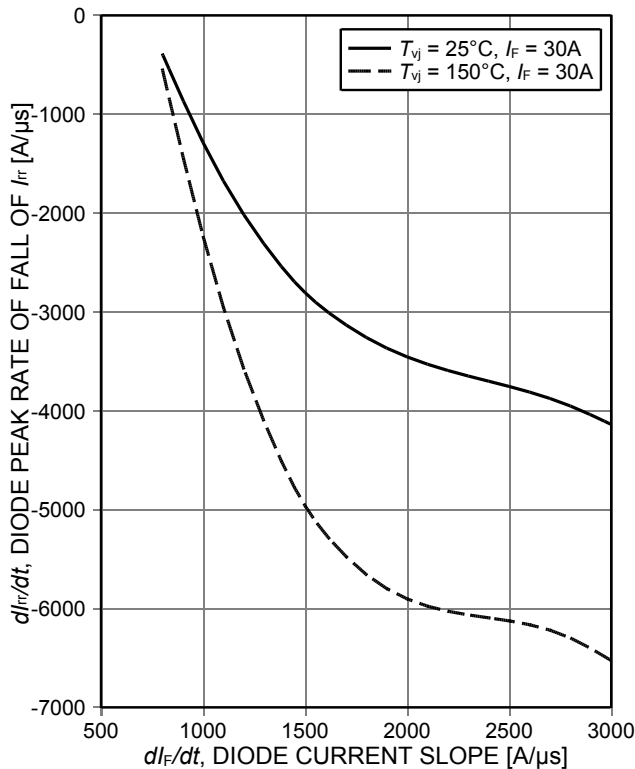


Figure 23. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope ( $V_R=400V$ )

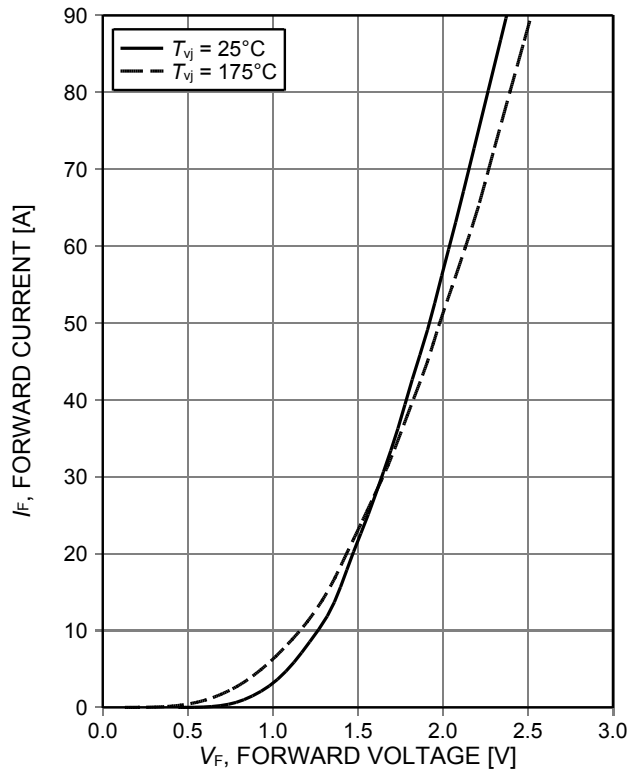


Figure 24. Typical diode forward current as a function of forward voltage

Low  $V_{CE(sat)}$  series fifth generation

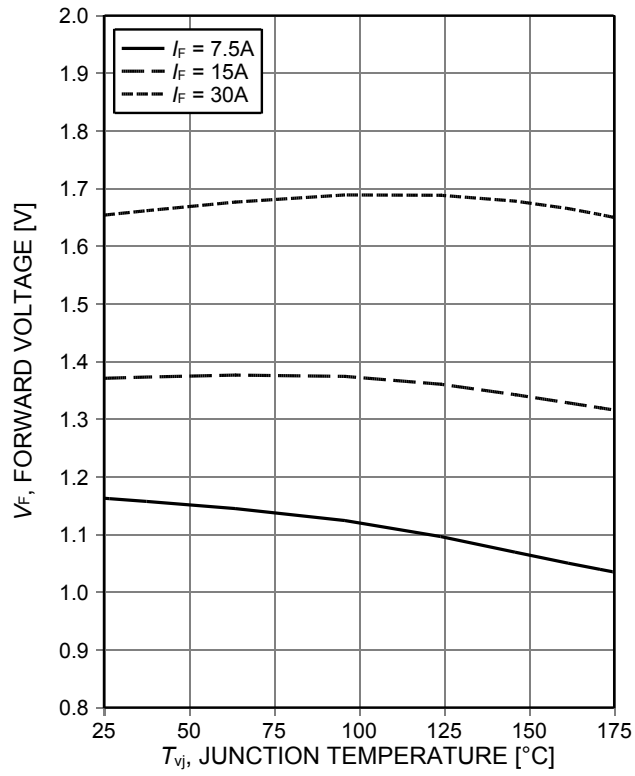
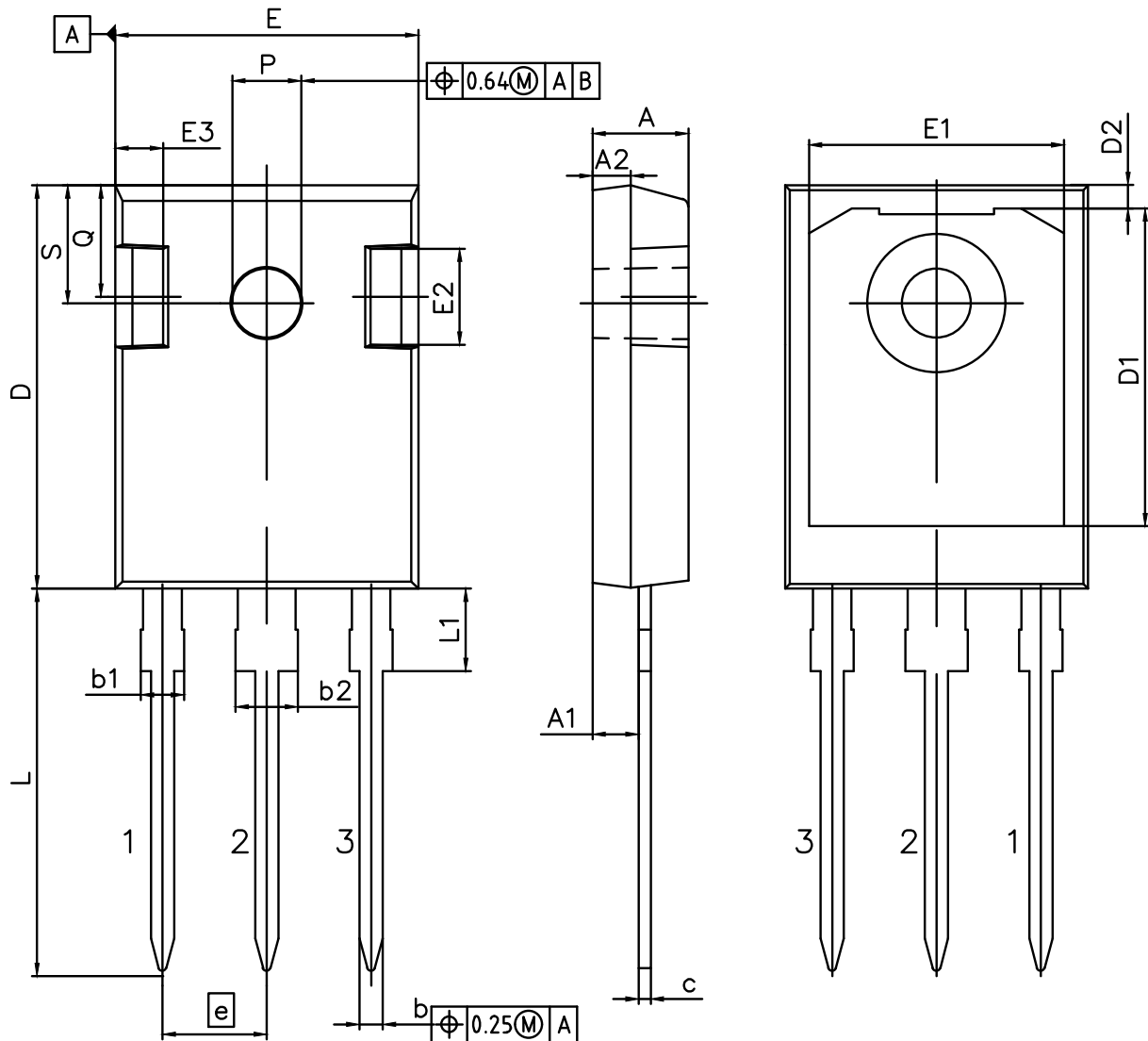


Figure 25. Typical diode forward voltage as a function of junction temperature

Low  $V_{CE(sat)}$  series fifth generation

Package Drawing PG-TO247-3



DIMENSIONS	MILLIMETERS	
	MIN.	MAX.
A	4.70	5.30
A1	2.20	2.60
A2	1.50	2.50
b	1.00	1.40
b1	1.60	2.41
b2	2.57	3.43
c	0.38	0.89
D	20.70	21.50
D1	13.08	17.65
D2	0.51	1.35
E	15.50	16.30
E1	12.38	14.15
E2	3.40	5.10
E3	1.00	2.60
e	5.44	
L	19.80	20.40
L1	3.85	4.50
P	3.50	3.70
Q	5.35	6.25
S	6.04	6.30

<b>DOCUMENT NO.</b> Z8B00003327
<b>REVISION</b> 06
<b>SCALE 3:1</b> 0 1 2 3 4 5mm 
<b>EUROPEAN PROJECTION</b> 
<b>ISSUE DATE</b> 25.07.2018

Low  $V_{CE(sat)}$  series fifth generation

Testing Conditions

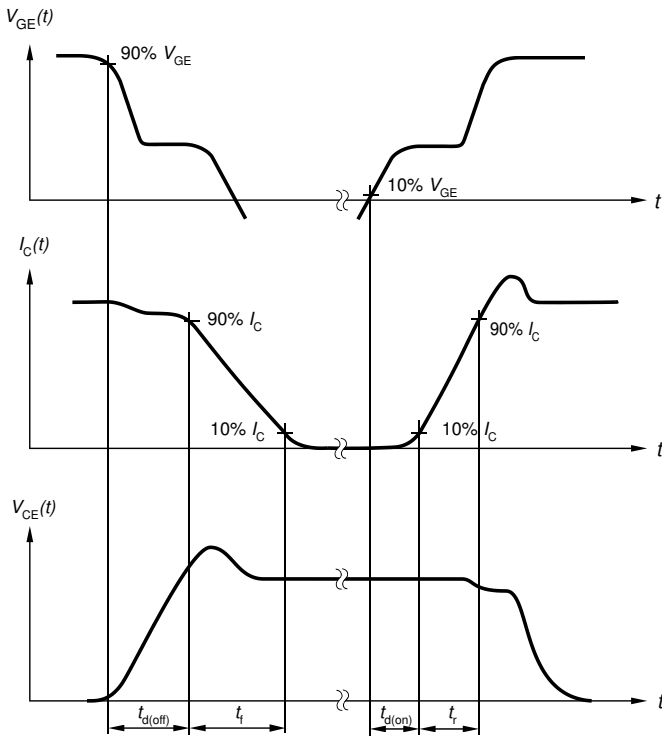


Figure A. Definition of switching times

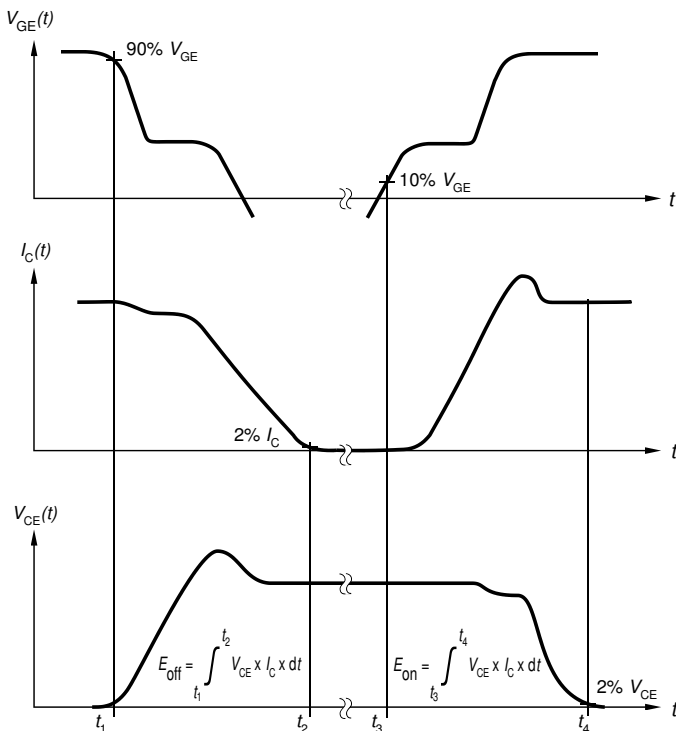


Figure B. Definition of switching losses

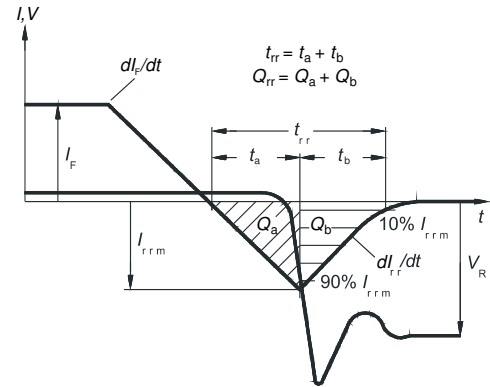


Figure C. Definition of diode switching characteristics

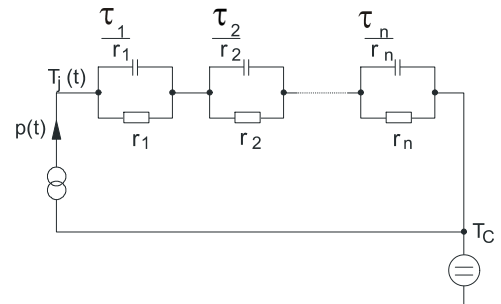


Figure D. Thermal equivalent circuit

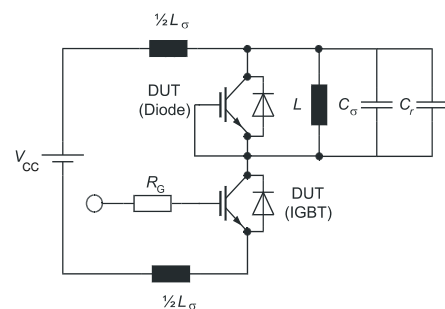


Figure E. **Dynamic test circuit**  
Parasitic inductance  $L_{\sigma}$ ,  
parasitic capacitor  $C_{\sigma}$ ,  
relief capacitor  $C_r$ ,  
(only for ZVT switching)

---

Low  $V_{CE(sat)}$  series fifth generation

### Revision History

---

IKW30N65NL5

**Revision: 2020-10-07, Rev. 2.2**

---

Previous Revision

Revision	Date	Subjects (major changes since last revision)
2.1	2014-12-10	Final data sheet
2.2	2020-10-07	VGE(th): test condition update



## Trademarks

All referenced product or service names and trademarks are the property of their respective owners.

**Published by**  
**Infineon Technologies AG**  
**81726 München, Germany**  
**© Infineon Technologies AG 2020.**  
**All Rights Reserved.**

## Important Notice

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie"). With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office ([www.infineon.com](http://www.infineon.com)).

Please note that this product is not qualified according to the AEC Q100 or AEC Q101 documents of the Automotive Electronics Council.

## Warnings

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.

## X-ON Electronics

Largest Supplier of Electrical and Electronic Components

*Click to view similar products for [IGBT Transistors](#) category:*

*Click to view products by [Infineon](#) manufacturer:*

Other Similar products are found below :

[748152A](#) [APT20GT60BRDQ1G](#) [APT50GT60BRG](#) [NGTB10N60FG](#) [STGFW20V60DF](#) [APT30GP60BG](#) [APT45GR65B2DU30](#)  
[GT50JR22\(STA1ES\)](#) [TIG058E8-TL-H](#) [IGW40N120H3FKSA1](#) [VS-CPV364M4KPBF](#) [NGTB25N120FL2WAG](#) [NGTG40N120FL2WG](#)  
[RJH60F3DPQ-A0#T0](#) [APT40GR120B2SCD10](#) [APT15GT120BRG](#) [APT20GT60BRG](#) [NGTB75N65FL2WAG](#) [NGTG15N120FL2WG](#)  
[IXA30RG1200DHGLB](#) [IXA40RG1200DHGLB](#) [APT70GR65B2DU40](#) [NTE3320](#) [QP12W05S-37A](#) [IHF40N65R5SXXSA1](#) [APT70GR120J](#)  
[APT35GP120JDQ2](#) [XD15H120CX1](#) [XD25H120CX0](#) [XP15PJS120CL1B1](#) [IGW30N60H3FKSA1](#) [STGWA8M120DF3](#) [IGW08T120FKSA1](#)  
[IGW75N60H3FKSA1](#) [FGH60N60SMD\\_F085](#) [FGH75T65UPD](#) [STGWA15H120F2](#) [IKA10N60TXKSA1](#) [IHW20N120R5XKSA1](#)  
[RJH60D2DPP-M0#T2](#) [IKP20N60TXKSA1](#) [IHW20N65R5XKSA1](#) [APT70GR120JD60](#) [AOD5B60D](#) [APT70GR120L](#) [STGWT60H65FB](#)  
[STGWT60H65DFB](#) [STGWT40V60DF](#) [STGWT20V60DF](#) [STGB10NB37LZT4](#)