PD-95346

IRF7503PbF

HEXFET[®] Power MOSFET

International

Generation V Technology

- Ultra Low On-Resistance
- Dual N-Channel MOSFET
- Very Small SOIC Package
- Low Profile (<1.1mm)
- Available in Tape & Reel
- · Fast Switching

• Lead-Free **Description**

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The new Micro8 package, with half the footprint area of the standard SO-8, provides the smallest footprint available in an SOIC outline. This makes the Micro8 an ideal device for applications where printed circuit board space is at a premium. The low profile (<1.1mm) of the Micro8 will allow it to fit easily into extremely thin application environments such as portable electronics and PCMCIA cards.

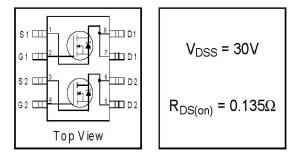
Absolute Maximum Ratings

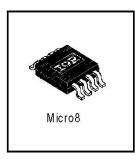
	Parameter	Max.	Units
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V	2.4	
I _D @ T _A = 70°C	Continuous Drain Current, V _{GS} @ 10V	1.9	A
I _{DM}	Pulsed Drain Current ①	14	
P _D @T _A =25°C	Power Dissipation	1.25	W
	Linear Derating Factor	10	m₩/ºC
V _{GS}	Gate-to-Source Voltage	± 20	V
d∨/dt	Peak Diode Recovery dv/dt ②	5.0	V/ns
TJ,TSTG	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

	Parameter	Тур.	Max.	Units
R _{0JA}	Maximum Junction-to-Ambient		100	°C/W

All Micro8 Data Sheets reflect improved Thermal Resistance, Power and Current -Handling Ratings- effective only for product marked with Date Code 505 or later .





Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V(BR)DSS	Drain-to-Source Breakdown Voltage	30		—	V	V _{GS} = 0V, I _D = 250µA
$\Delta V_{(BR)DSS} / \Delta T_J$	Breakdown Voltage Temp. Coefficient		0.059		V/°C	Reference to 25°C, I _D = 1mA
R	Static Drain-to-Source On-Resistance			0.135		V _{GS} = 10V, I _D = 1.7A ③
R _{DS(on)}	Static Drain-to-Source On-Resistance			0.222		V _{GS} = 4.5V, I _D = 0.85A ③
V _{GS(th)}	Gate Threshold Voltage	1.0			V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
g fs	Forward Transconductance	1.9	—		S	V _{DS} = 10V, I _D = 0.85A
IDSS	Drain-to-Source Leakage Current	—		1.0		V _{DS} = 24V, V _{GS} = 0V
-000				25	μA	V _{DS} = 24V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Forward Leakage	—		-100	nA	V _{GS} = -20V
.022	Gate-to-Source Reverse Leakage			100	IIA	V _{GS} = 20V
Qg	Total Gate Charge		7.8	12		I _D = 1.7A
Qgs	Gate-to-Source Charge		1.2	1.8	nC	V _{DS} = 24V
Q _{gd}	Gate-to-Drain ("Miller") Charge		2.5	3.8		V _{GS} = 10V, See Fig. 6 and 9 ③
t _{d(on)}	Turn-On Delay Time		4.7			V _{DD} = 15V
tr	Rise Time		10			I _D = 1.7A
t _{d(off)}	Turn-Off Delay Time		12		ns	R _G = 6.1Ω
tr	Fall Time		5.3			R _D = 8.7Ω, See Fig. 10 ③
Ciss	Input Capacitance		210			V _{GS} = 0V
Coss	Output Capacitance		80		рF	V _{DS} = 25V
Crss	Reverse Transfer Capacitance		32	—		f = 1.0MHz, See Fig. 5

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			4.05		MOSFET symbol
	(Body Diode)	1-		1.25	A	showing the
I _{SM}	Pulsed Source Current			14		integral reverse
	(Body Diode) ①			14		p-n junction diode.
V _{SD}	Diode Forward Voltage			1.2	V	T _J = 25°C, I _S = 1.7A, V _{GS} = 0V ③
trr	Reverse Recovery Time		40	60	ns	T _J = 25°C, I _F = 1.7A
Qrr	Reverse RecoveryCharge		48	72	nC	di/dt = 100A/µs ③

Notes:

① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)

3 Pulse width $\leq300\mu s;$ duty cycle $\leq2\%.$

0 I_{SD} \leq 1.7A, di/dt \leq 120A/µs, V_{DD} \leq V_{(BR)DSS}, T_J \leq 150°C

G Surface mounted on FR-4 board, t \leq 10sec.

2

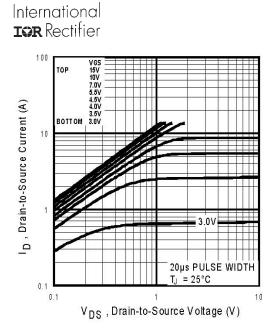


Fig 1. Typical Output Characteristics

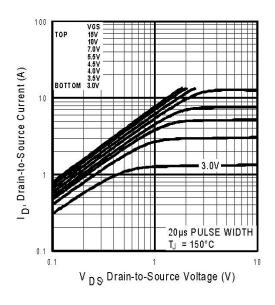


Fig 2. Typical Output Characteristics

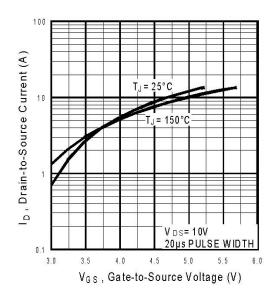


Fig 3. Typical Transfer Characteristics

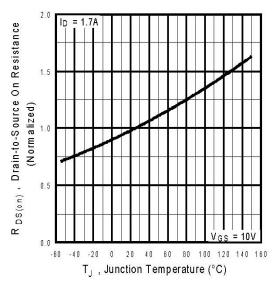


Fig 4. Normalized On-Resistance Vs. Temperature

IRF7503PbF 400 f = 1MHz $V_{GS} = 0V,$ $C_{iss} = C_{gs} + C_{gd}$, C_{ds} SHORTED $C_{rss} = C_{gd}$ $C_{oss} = C_{ds} + C_{qd}$ 300 C, Capacitance (pF) Coss

200

100

0

1

rss



10

 V_{DS} , Drain-to-Source Voltage (V)

100



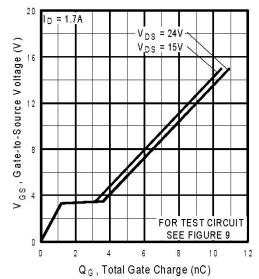
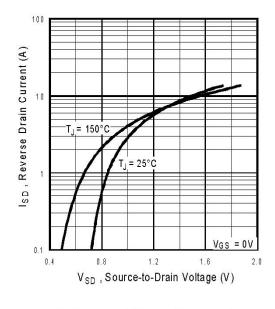
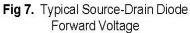
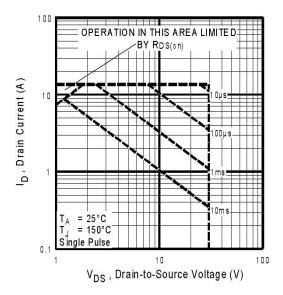


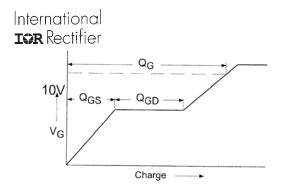
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

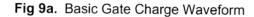












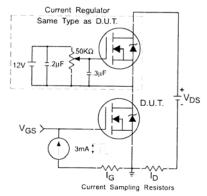


Fig 9b. Gate Charge Test Circuit

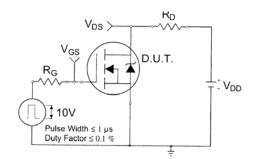


Fig 10a. Switching Time Test Circuit

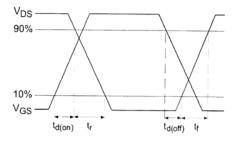


Fig 10b. Switching Time Waveforms

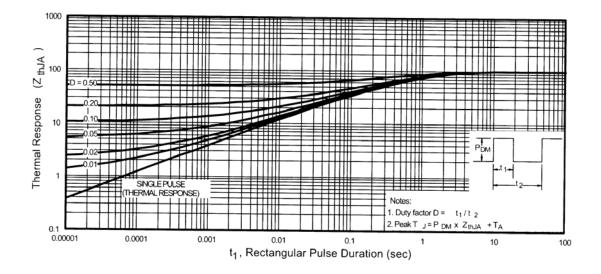
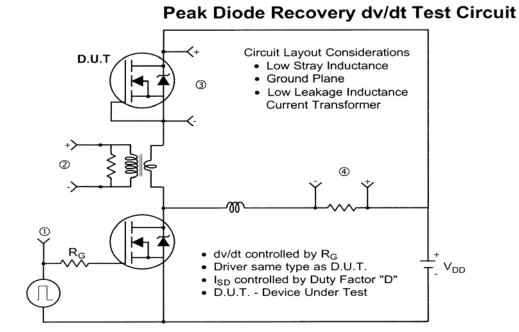
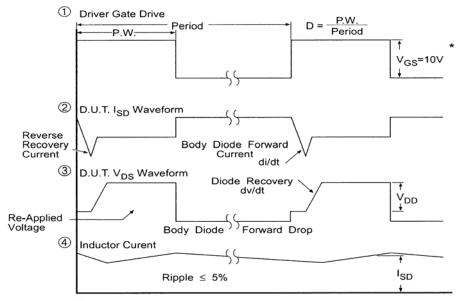


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient www.irf.com

International





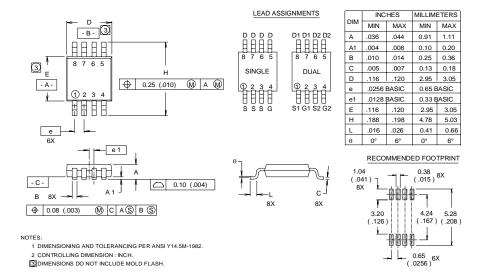
* V_{GS} = 5V for Logic Level Devices

Fig 12. For N-Channel HEXFETS

International

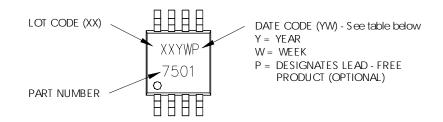
Micro8 Package Outline

Dimensions are shown in milimeters (inches)



Micro8 Part Marking Information

EXAMPLE: THIS IS AN IRF7501



WW = (1-26) IF PRECEDED BY LAST DIGIT OF CALENDAR YEAR

YE AR	Y	WORK WEEK	W
2001	1	01	А
2002	2	02	В
2003	3	03	С
2004	4	04	D
2005	5	1	1
2006	6		
2007	7		
2008	8	1	
2009	9		
2010	0	24	X
		25	Y
		26	Ζ

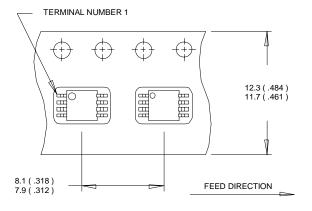
WW = (27-52) IF PRECEDED BY ALETTER

YEAR	Y	WORK WEEK	W
2001	А	27	А
2002	В	28	В
2003	С	29	С
2004	D	30	D
2005	E	1	1
2006	F		
2007	G		
2008	Н	1	1
2009	J		
2010	К	50	Х
		51	Y
		52	Z

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Micro8 Tape & Reel Information

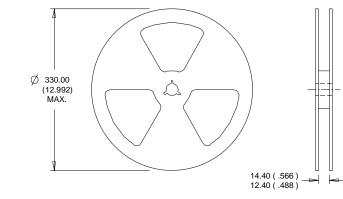
Dimensions are shown in millimeters (inches)



NOTES:

1. OUTLINE CONFORMS TO EIA-481 & EIA-541.

2. CONTROLLING DIMENSION : MILLIMETER.



NOTES : 1. CONTROLLING DIMENSION : MILLIMETER. 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

> Data and specifications subject to change without notice. This product has been designed and qualified for the Consumer market. Qualifications Standards can be found on IR's Web site.

International

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