

TLE4205G

1-A DC Motor Driver

Datasheet Rev. 1.1, 2015-01-15

Automotive Power





1-A DC Motor Driver Overview

Features

- Max. driver current 1 A
- Integrated free-wheeling diodes
- Short-circuit proof to ground
- Inhibit
- ESD protected inputs
- Temperature range 40 °C $\leq T_{i} \leq$ 150 °C
- Green Product (RoHS compliant)
- AEC Qualified



PG-DSO-20

Туре	Marking	Package
TLE4205G	TLE4205G	PG-DSO-20

Description

TLE 4205G is an integrated power full-bridge DC-motor driver for a wide temperature range, as required in automotive applications for example. The circuit contains two power comparators that can be combined to a full-bridge circuit. For inductive loads there are integrated free-wheeling diodes to + $V_{\rm S}$ and ground. The outputs are short-circuit proof up to 18 V supply voltage to ground and turn off when overtemperature occurs. This IC is especially suitable for headlight-beam adjustment in automobiles.

TLE4205G



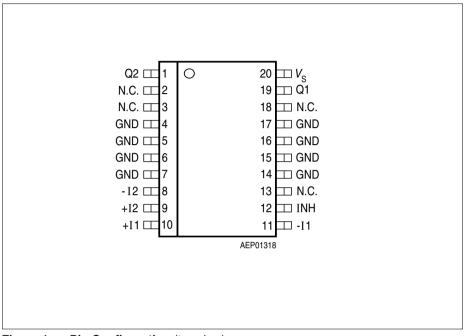


Figure 1 Pin Configuration (top view)



Pin Definitions and Functions

Pin No.	Symbol	Function
1	Q2	Output 2 of channel 2; push-pull B output with DC short-circuit protection to ground. Integrated free-wheeling diodes to ground and the supply voltage.
2	N.C.	Not connected
3	N.C.	Not connected
4-7	GND	Ground
8	- I2	Inverting input channel 2; to be wired according to general rules.
9	+ 12	Non-inverting input channel 2; to be wired according to general rules.
10	+ 1	Non-inverting input channel 1; see pin 9.
11	- I1	Inverting input channel 1; see pin 8.
12	INH	Inhibit; the IC is passive when this pin is open or connected to ground.
13	N.C.	Not connected
14-17	GND	Ground
18	N.C.	Not connected
19	Q1	Output Q1 of channel 1, see pin 1.
20	Vs	Supply voltage V_s ; must be blocked with a ceramic capacitor of at least 100 nF directly on the pins of the IC.



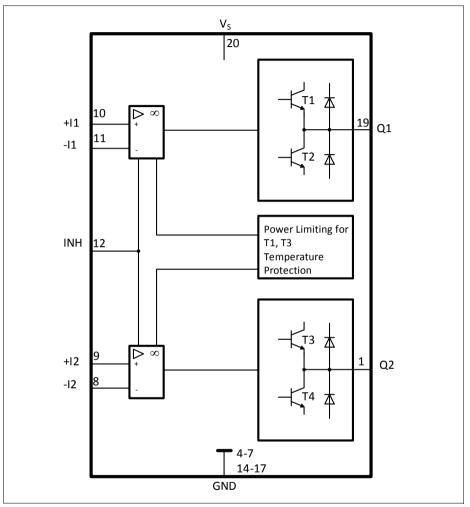


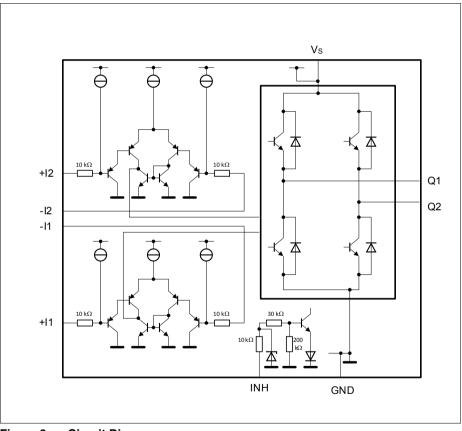
Figure 2 Block Diagram



Circuit Description

The IC contains two amplifiers with typical open-loop gain of 80 dB at 500 Hz.

The input stages consist of PNP-differential amplifiers. This produces a common-mode input range of 0 V to nearly $V_{\rm S}$ and a maximum differential input voltage of $V_{\rm S}$. The IC is guarded against ground shorts by an SOA-protective circuit. The output transistors are turned off if the chip temperature exceeds approx. 160 °C. The IC can be turned off by an inhibit input, which very much reduces current consumption.







Absolute Maximum Ratings

 $T_{\rm i} = -40$ to 150 °C

Parameter	Symbol	Limi	t Values	Unit	Remarks
		min.	max.		
Supply voltage	Vs	- 0.3	45	V	-
Differential input voltage	V_{ID}	-	$\pm V_{\rm S}$	V	$\Delta V_{\text{8-9}} ext{ or } \Delta V_{\text{10-11}}$
Output current	IQ	- 1	1	А	-
Supply current	Is	2.5	3	А	-
Ground current	$I_{\rm GND}$	- 3	2.5	А	12
Input voltage	V_1	– 15	Vs	V	$V_8; V_9; V_{10}; V_{11}$
Inhibit input	V_{lnh}	– 15	Vs	V	V ₁₂
Junction temperature	Tj	-	150	°C	-
Storage temperature	$T_{\rm stg}$	- 50	150	°C	-

Operating Range

Supply voltage	V_{s}	6	32	V	-
Case temperature	T _c	- 40	95	°C	$P_{\text{Dmax}} = 3 \text{ W}$
Thermal resistance junction - ambient	$R_{ m thJA}$	_	65	K/W	
junction - case	$R_{\rm th \ JC}$	-	20	K/W	

Outputs pin 1 and pin 19 short-circuit proof to GND at $V_{\rm S}$ \leq 18 V

Characteristics

 $6 \text{ V} < V_{\text{S}} < 18 \text{ V}; -40 \text{ }^{\circ}\text{C} < T_{\text{j}} < 150 \text{ }^{\circ}\text{C}$

Parameter	Symbol	Limit Values		Unit	Test Condition	
		min.	typ.	max.		

General

Open-circuit current consumption	Is	-	10	30	mA	active, both outputs high
Open-circuit current consumption	Is	-	10	100	μA	inhibit
Turn-ON dead time ref. to $V_{12 \text{ OFF/ON}}$	t _{d ON}	-	10	20	μs	<i>I</i> _{1,19} < 1 A
Turn-OFF dead time ref. to $V_{12 \text{ OFF/ON}}$	t _{d OFF}	-	10	20	μs	<i>I</i> _{1,19} < 1 A



Characteristics (cont'd) 6 V < $V_{\rm S}$ < 18 V; – 40 °C < $T_{\rm j}$ < 150 °C

Parameter	Symbol	mit Va	nit Values		Test Condition	
		min.	typ.	max.	1	
Open-loop gain	G _{vo}	50	80	-	dB	<i>f</i> = 500 Hz
Inputs						
Input zero voltage	$V_{\rm IO}$	- 7.5	-	7.5	mV	$R_{\rm S}$ = 10 kΩ;
Input-voltage drift	$\Delta V_{\rm IO}/\Delta T$	-	20	30	μV/K	-
Input zero current	I _{IO}	- 75	-	75	mA	-
Input current	I	- 300	-	300	nA	-
Input-current drift	$\Delta I_{\rm I}/\Delta T$	-	_	5	nA/K	-
Input common-mode range, positive	V _{IC}	-	-	$V_{\rm S}-2$	V	-
Input common-mode range, negative	V _{IC}	-	-	- 0.5	V	-
Power-supply rejection ratio	PSSR	-	-	200	μV/V	$R_{\rm S}$ = 10 kΩ;
Common-mode rejection ratio	CMRR	70	80	-	dB	-



Characteristics (cont'd) 6 V < $V_{\rm S}$ < 18 V; – 40 °C < $T_{\rm j}$ < 150 °C

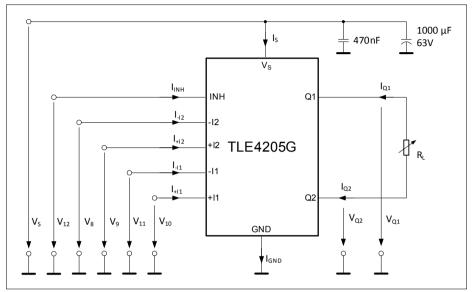
Parameter	Symbol	Limit Values			Unit	Test Condition
		min.	typ.	max.	1	
Outputs						
Saturation voltage	V _{Sat U}	-	1.35	1.5	V	$I_{\rm Q} = -0.6 {\rm A}$
Saturation voltage	V _{Sat L}	-	0.8	1.2	V	$I_{\rm Q} = 0.6 {\rm A}$
Forward voltage of free-wheeling diode	$V_{\rm FU}$	-	1	1.5	V	<i>I</i> _F = 0.6 A
Forward voltage of free-wheeling diode	V_{FL}	-	1	1.5	V	$I_{\rm F} = 0.6 {\rm A}$
Slew rate of V _Q	$\mathrm{d}V_{\mathrm{q}}\mathrm{d}t_{\mathrm{r}}$	-	0.5	-	V/µs	-
Inhibit Input			÷			
Switching threshold high	V_{IH}	2	-	-	V	-
Switching threshold low	V_{IL}	-	-	0.8	V	-
H-input current	I _{IH}	-	100	-	μA	$V_{12} = 5 \text{ V}$
L-input current	I _{IH}	-	0	-	μA	$V_{12} = 0 \text{ V}$

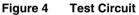
Note: $V_{\text{Sat U}}$ = upper

 $V_{\text{Sat L}} = \text{lower}$



TLE4205G





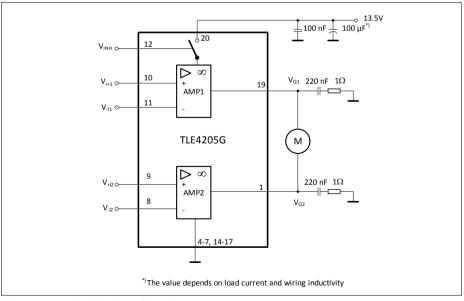
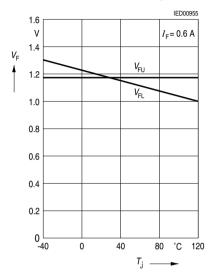


Figure 5 Application Circuit

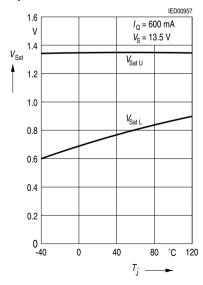




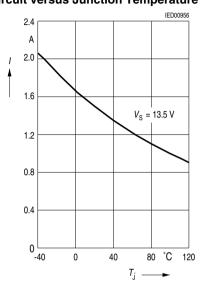
Forward Voltage of the Free-Wheeling Diodes versus Junction Temperature



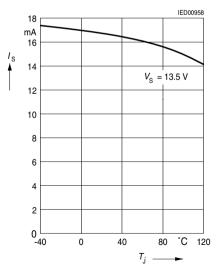
Saturation Voltage versus Junction Temperature



Start Point of the SOA-Protection Circuit versus Junction Temperature



Current Consumption versus Junction Temperature





Package Outlines

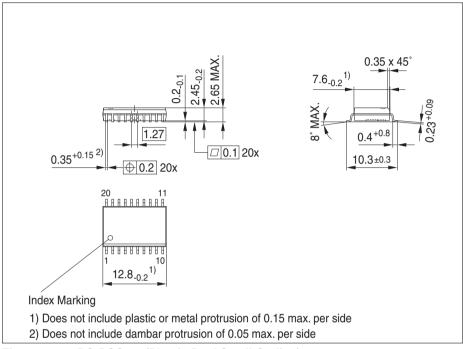


Figure 6 PG-DSO-20 (Plastic Dual Small Outline)

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

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Dimensions in mm



Revision History

Revision	Date	Changes					
1.1	2015-01-19	 Initial version of RoHS-compliant derivate of TLE 4205G Page 1: Added Coverpage, All pages: Infineon logo updated Page 2: "added AEC qualified" and "RoHS" logo, "Green Product (RoHS compliant)" and "AEC qualified" statement added to feature list, package name changed to RoHS compliant versions, package picture updated Page 12: Package name changed to RoHS compliant versions, "Green Product" description added Page 13: added Revision History Page 14: added Legal Disclaimer Page 7, Page 9: V9 designating the voltage at INH pin renamed V12 					

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